

C0510X7S0E105M030BC

TDK Item Description : C0510X7S0E105MT****

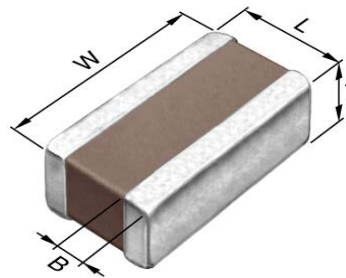


Application & Main Feature

Commercial Grade (Low ESL Reverse Geometry)

Series

C0510 [EIA 0204]



Dimensions

L	0.52mm +/-0.05mm
W	1.00mm +/-0.05mm
T	0.30mm +/-0.05mm
B	0.10mm Min.

Temperature Characteristic

X7S (-55 to 125 degC ±22%)

Rated Voltage

0E(2.5Vdc)

Capacitance

1uF

Capacitance Tolerance

M (+/-20%)

Dissipation Factor

10% Max.

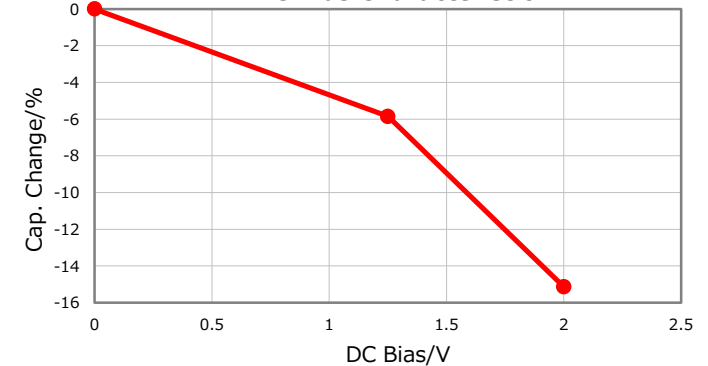
Insulation Resistance

100Mohm Min.

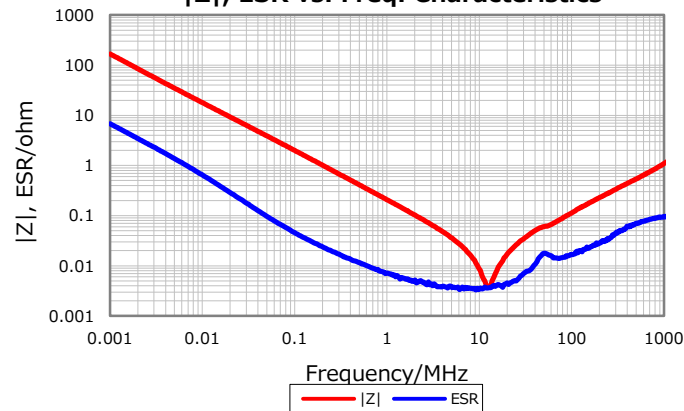
AEC-Q200

Not Applicable

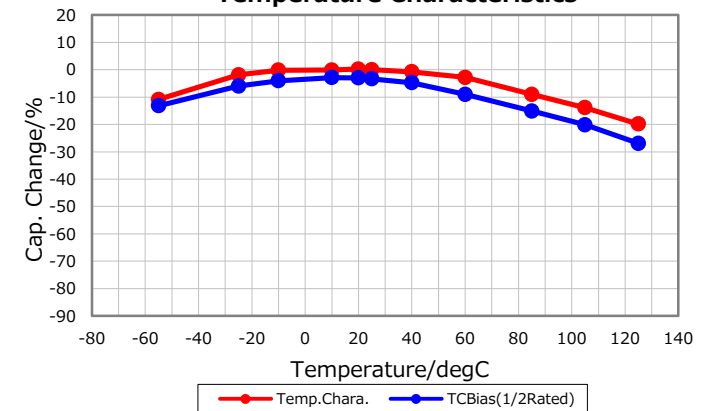
DC Bias Characteristic



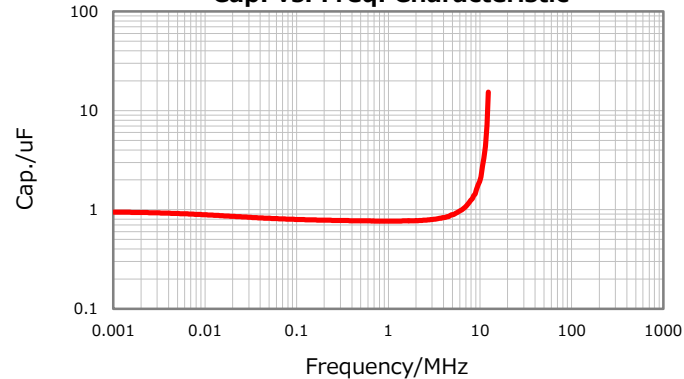
|Z|, ESR vs. Freq. Characteristics



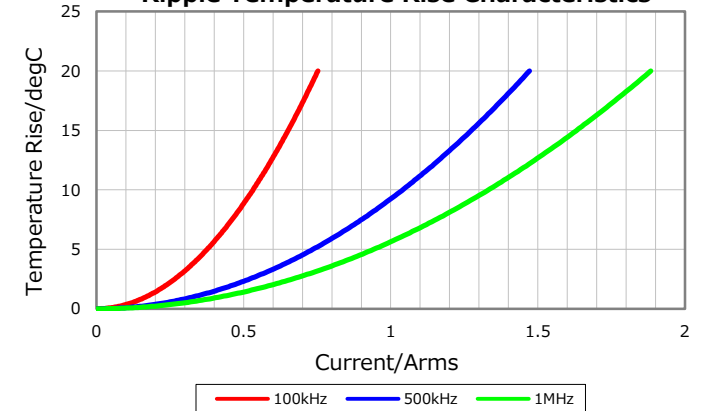
Temperature Characteristics



Cap. vs. Freq. Characteristic



Ripple Temperature Rise Characteristics



Characterization Sheet (Multilayer Ceramic Chip Capacitors)



All specifications are subject to change without notice.

November 27, 2017