

# C1005JB0J475M050BC

TDK Item Description : C1005JB0J475MT\*\*\*



## Application & Main Feature

Commercial Grade ( General (Up to 50V) )

## Series

C1005 [EIA CC0402]



## Dimensions

L 1.00mm +0.15,-0.1mm  
 W 0.50mm +0.15,-0.1mm  
 T 0.50mm +0.15,-0.1mm  
 B 0.10mm Min.  
 G 0.30mm Min.

## Temperature Characteristic

JB (-25 to 85 degC +/-10%)

## Rated Voltage

0J (6.3Vdc)

## Capacitance

4.7uF

## Capacitance Tolerance

M (+/-20%)

## Dissipation Factor

10% Max.

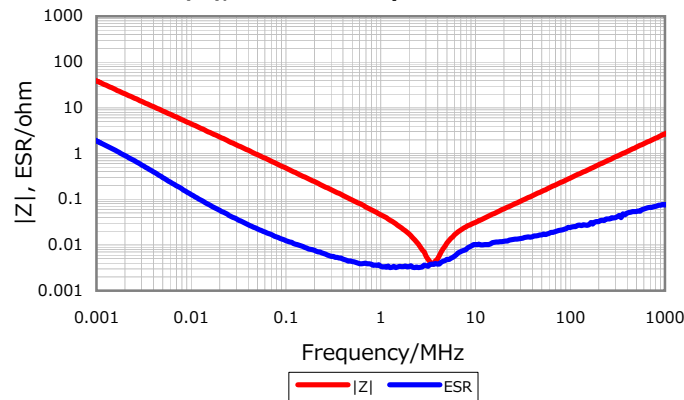
## Insulation Resistance

21Mohm Min.

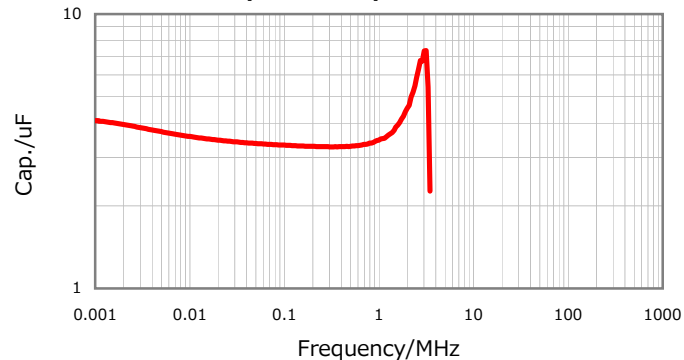
## AEC-Q200

Not Applicable

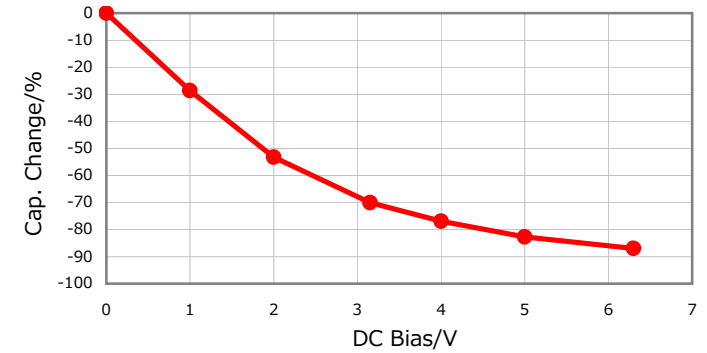
## |Z|, ESR vs. Freq. Characteristics



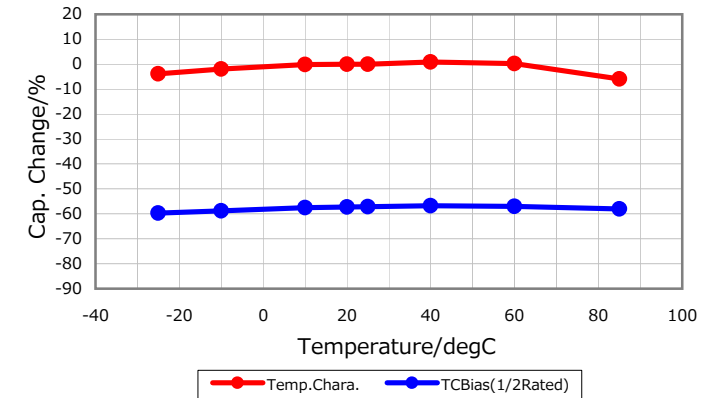
## Cap. vs. Freq. Characteristic



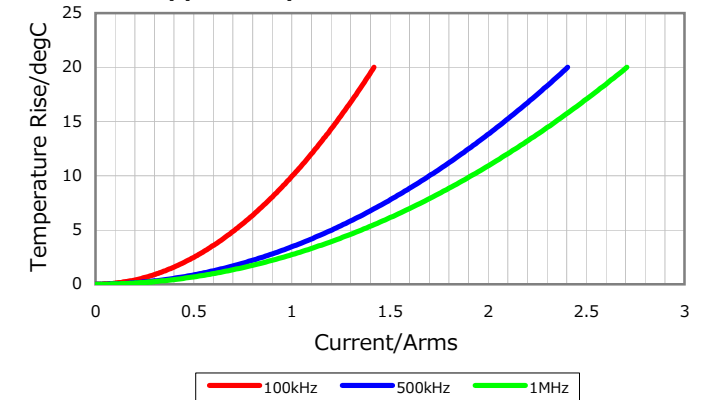
## DC Bias Characteristic



## Temperature Characteristics



## Ripple Temperature Rise Characteristics



Characterization Sheet ( Multilayer Ceramic Chip Capacitors )



All specifications are subject to change without notice.

January 3, 2016