

DELIVERY SPECIFICATION

SPEC. No. A-Softhv-a

D A T E : Oct., 2024

To

Non-Controlled Copy

CUSTOMER'S PRODUCT NAME	TDK'S PRODUCT NAME Multilayer Ceramic Chip Capacitors High Voltage Series (Soft Termination) Tape packaging 【RoHS compliant】 CGA6,CGA7,CGA8 Type C0G,X7R Characteristics
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Please return this specification to TDK representatives with your signature.
 If orders are placed without returned specification, please allow us to judge that specification is accepted by your side.

RECEIPT CONFIRMATION

DATE: _____ YEAR _____ MONTH _____ DAY _____

Test conditions in this specification based on AEC-Q200 for automotive application.

TDK Corporation
 Sales
 Electronic Components
 Sales & Marketing Group

Engineering
 Electronic Components Business Company
 Ceramic Capacitors Business Group

APPROVED	Person in charge

APPROVED	CHECKED	Person in charge

SCOPE

This delivery specification shall be applied to Multilayer ceramic chip capacitors to be delivered to

PRODUCTION PLACES

Production places defined in this specification shall be TDK Corporation, TDK(Suzhou)Co.,Ltd and TDK Components U.S.A.,Inc.

PRODUCT NAME

The name of the product to be defined in this specifications shall be CGA◇◇◇○○○△△□□×T※※※S.

REFERENCE STANDARD

JIS C 5101-1 : 2010	Fixed capacitors for use in electronic equipment-Part 1: Generic specification
C 5101-21 : 2014	Fixed capacitors for use in electronic equipment-Part21 : Sectional specification : Fixed surface mount multilayer capacitors of ceramic dielectric,Class1
C 5101-22 : 2014	Fixed capacitors for use in electronic equipment-Part22 : Sectional specification : Fixed surface mount multilayer capacitors of ceramic dielectric,Class 2
C 0806-3 : 2014	Packaging of components for automatic handling - Part 3: Packaging of surface mount components on continuous tapes
JEITA RCR-2335 C 2014	Safety application guide for fixed ceramic capacitors for use in electronic equipment

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<EXPLANATORY NOTE>

When the mistrust in the spec arises, this specification is given priority. And it will be confirmed by written spec change after conference of both posts involved.

This specification warrants the quality of the ceramic chip capacitor. Capacitors should be evaluated or confirmed a state of mounted on your product.

If the use of the capacitors goes beyond the bounds of this specification, we can not afford to guarantee.

Division	Date	SPEC. No.
Ceramic Capacitors Business Group	October, 2024	A-Softhv-a

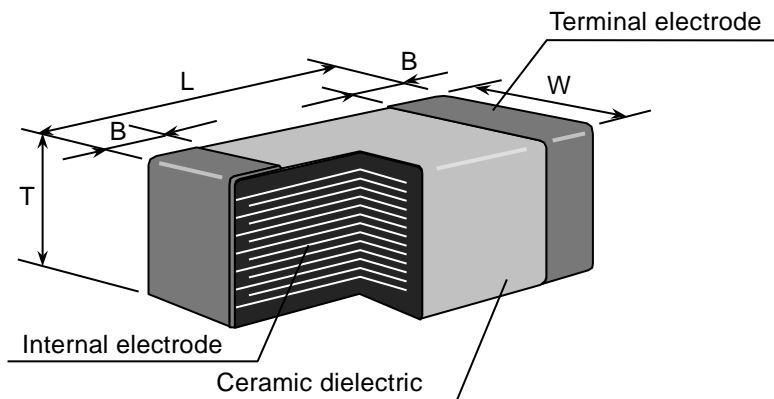
1. CODE CONSTRUCTION

(Example) CGA 6 M 1 C0G 3 A 102 J T ※※※S
 CGA 8 K 1 X7R 3 D 222 M T ※※※S
 (1) (2) (3) (4) (5) (6) (7) (8) (9) (10)

(1) Series

Symbol	Series
CGA	For automotive application

(2) Case size



Case size Symbol	Case size (EIA style)	Dimensions (Unit : mm)			
		L	W	T	B
6	CGA6 (CC1210)	3.20 ^{+0.50} _{-0.40}	2.50±0.30	2.00 ^{+0.30} _{-0.20}	0.20 min.
				2.30 ^{+0.30} _{-0.20}	
7	CGA7 (CC1808)	4.50 ^{+0.50} _{-0.40}	2.00±0.20	1.30±0.20	0.20 min.
8	CGA8 (CC1812)	4.50 ^{+0.50} _{-0.40}	3.20±0.40	1.30±0.20	0.20 min.
				2.50±0.30	

*As for each item, please refer to detail page on TDK web.

(3) Thickness

Thickness	Dimension(mm)
K	1.30
M	2.00
N	2.30
P	2.50

(4) Voltage condition in the life test

* Details are shown in table1 No.16 at 7.PERFORMANCE.

Sign	Condition
1	Rated Voltage

(5) Temperature Characteristics

* Details are shown in table 1 No.6 and No.7 at 7.PERFORMANCE.

(6) Rated Voltage

Symbol	Rated Voltage
3 A	DC 1kV
3 D	DC 2kV
3 F	DC 3kV

2. COMBINATION OF RATED CAPACITANCE AND TOLERANCE

Class	Temperature Characteristics	Capacitance tolerance	Rated capacitance
1	C0G	G ($\pm 2\%$) J ($\pm 5\%$) K ($\pm 10\%$)	E – 12 series
2	X7R	K ($\pm 10\%$) M ($\pm 20\%$)	E – 3 series

Capacitance Step in E series

E series	Capacitance Step											
E-3	1.0				2.2				4.7			
E-12	1.0	1.2	1.5	1.8	2.2	2.7	3.3	3.9	4.7	5.6	6.8	8.2

3. OPERATING TEMPERATURE RANGE

Min. operating Temperature	Max. operating Temperature	Reference Temperature
-55°C	125°C	25°C

4. STORING CONDITION AND TERM

Storing temperature	Storing humidity	Storing term
5~40°C	20~70%RH	Within 6 months upon receipt.

5. P.C. BOARD

When mounting on an aluminum substrate, the ceramic chip capacitor. Capacitors are more likely to be affected by heat stress from the substrate. Please inquire separate specification when mounted on the substrate.

6. INDUSTRIAL WASTE DISPOSAL

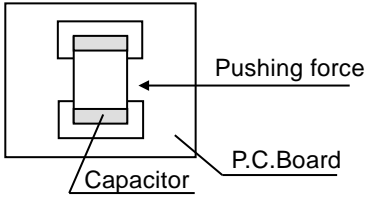
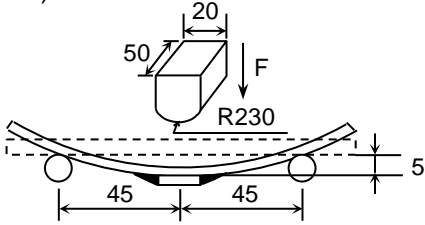
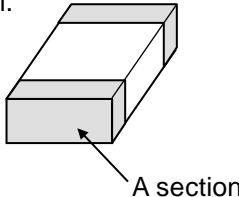
Dispose this product as industrial waste in accordance with the Industrial Waste Law.

7. PERFORMANCE

Table 1

No.	Item	Performance	Test or inspection method													
1	External Appearance	No defects which may affect performance.	Inspect with magnifying glass (3×).													
2	Insulation Resistance	10,000MΩ min.	Measuring voltage : 500V DC Voltage application time : 60s.													
3	Voltage Proof	Withstand test voltage without insulation breakdown or other damage.	Apply voltage : 1.2 × rated voltage Voltage application time : 1s. Charge/discharge current : 50mA or lower													
4	Capacitance	Within the specified tolerance.	《 Class 1 》 <table border="1"> <thead> <tr> <th>Capacitance</th> <th>Measuring frequency</th> <th>Measuring voltage</th> </tr> </thead> <tbody> <tr> <td>1000pF and under</td> <td>1MHz±10%</td> <td rowspan="2">0.5~5 Vrms.</td> </tr> <tr> <td>Over 1000pF</td> <td>1kHz±10%</td> </tr> </tbody> </table> 《 Class 2 》 <table border="1"> <thead> <tr> <th>Measuring frequency</th> <th>Measuring voltage</th> </tr> </thead> <tbody> <tr> <td>1kHz±10%</td> <td>1.0±0.2Vrms</td> </tr> </tbody> </table>	Capacitance	Measuring frequency	Measuring voltage	1000pF and under	1MHz±10%	0.5~5 Vrms.	Over 1000pF	1kHz±10%	Measuring frequency	Measuring voltage	1kHz±10%	1.0±0.2Vrms	
Capacitance	Measuring frequency	Measuring voltage														
1000pF and under	1MHz±10%	0.5~5 Vrms.														
Over 1000pF	1kHz±10%															
Measuring frequency	Measuring voltage															
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5	<table border="1"> <tr> <td>Q</td> <td>Class1</td> </tr> <tr> <td>Dissipation Factor</td> <td>Class2</td> </tr> </table>	Q	Class1	Dissipation Factor	Class2	Please refer to detail page on TDK web.	See No.4 in this table for measuring condition.									
Q	Class1															
Dissipation Factor	Class2															
6	Temperature Characteristics of Capacitance	<table border="1"> <tr> <td>T.C.</td> <td>Temperature Coefficient (ppm/°C)</td> </tr> <tr> <td>COG</td> <td>0 ± 30</td> </tr> <tr> <td>Capacitance drift</td> <td>Within ± 0.2%</td> </tr> </table>	T.C.	Temperature Coefficient (ppm/°C)	COG	0 ± 30	Capacitance drift	Within ± 0.2%	Temperature coefficient shall be calculated based on values at 25°C and 85°C temperature. Measuring temperature below 25°C shall be -10°C and -25°C.							
T.C.	Temperature Coefficient (ppm/°C)															
COG	0 ± 30															
Capacitance drift	Within ± 0.2%															
7	Temperature Characteristics of Capacitance (Class2)	<table border="1"> <tr> <td>Capacitance Change (%)</td> </tr> <tr> <td>No voltage applied</td> </tr> <tr> <td>X7R : ± 15</td> </tr> </table>	Capacitance Change (%)	No voltage applied	X7R : ± 15	Capacitance shall be measured by the steps shown in the following table after thermal equilibrium is obtained for each step. ΔC be calculated ref. STEP3 reading <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature(°C)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>25 ± 2</td> </tr> <tr> <td>2</td> <td>-55 ± 2</td> </tr> <tr> <td>3</td> <td>25 ± 2</td> </tr> <tr> <td>4</td> <td>125 ± 2</td> </tr> </tbody> </table> As for measuring voltage, please contact with our sales representative.	Step	Temperature(°C)	1	25 ± 2	2	-55 ± 2	3	25 ± 2	4	125 ± 2
Capacitance Change (%)																
No voltage applied																
X7R : ± 15																
Step	Temperature(°C)															
1	25 ± 2															
2	-55 ± 2															
3	25 ± 2															
4	125 ± 2															

(continued)

No.	Item		Performance	Test or inspection method
8	Robustness of Terminations		No sign of termination coming off, breakage of ceramic, or other abnormal signs.	Reflow solder the capacitors on a P.C.Board shown in Appendix 2. Apply a pushing force gradually at the center of a specimen in a horizontal direction of P.C.board. Pushing force : 17.7N Holding time : 10±1s. 
9	Bending	External appearance	No mechanical damage.	Reflow solder the capacitors on a P.C.Board shown in Appendix 1. (2mm is applied for CGA7 and CGA8 types.)  (Unit : mm)
10	Solderability		New solder to cover over 75% of termination. 25% may have pin holes or rough spots but not concentrated in one spot. Ceramic surface of A sections shall not be exposed due to melting or shifting of termination material. 	Solder : Sn-3.0Ag-0.5Cu Flux : Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution. Solder temp. : 245±5°C Dwell time : 3±0.3s. Solder position : Until both terminations are completely soaked.

(continued)

No.	Item		Performance	Test or inspection method		
11	Resistance to solder heat	External appearance	No cracks are allowed and terminations shall be covered at least 60% with new solder.	Solder : Sn-3.0Ag-0.5Cu Flux : Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution. Solder temp. : 260±5°C Dwell time : 10±1s. Solder position : Until both terminations are completely soaked. Pre-heating : Temp. — 110~140°C Time — 30~60s. Leave the capacitors in ambient condition for Class 1 : 6~24h Class 2 : 24±2h before measurement.		
		Capacitance	Characteristics		Change from the value before test	
			Class1		C0G	± 2.5 %
			Class2		X7R	± 7.5 %
		Q (Class1)	Meet the initial spec.			
		D.F. (Class2)	Meet the initial spec.			
		Insulation Resistance	Meet the initial spec.			
Voltage proof	No insulation breakdown or other damage.					
12	Vibration	External appearance	No mechanical damage.	Applied force : 5G max. Frequency : 10~2,000Hz Reciprocating sweep time : 20 min. Cycle : 12 cycles in each 3 mutually perpendicular directions. Reflow solder the capacitors on a P.C.Board shown in Appendix 2 before testing.		
		Capacitance	Characteristics		Change from the value before test	
			Class1		C0G	± 2.5 %
			Class2		X7R	± 7.5 %
		Q (Class1)	Meet the initial spec.			
D.F. (Class2)	Meet the initial spec.					

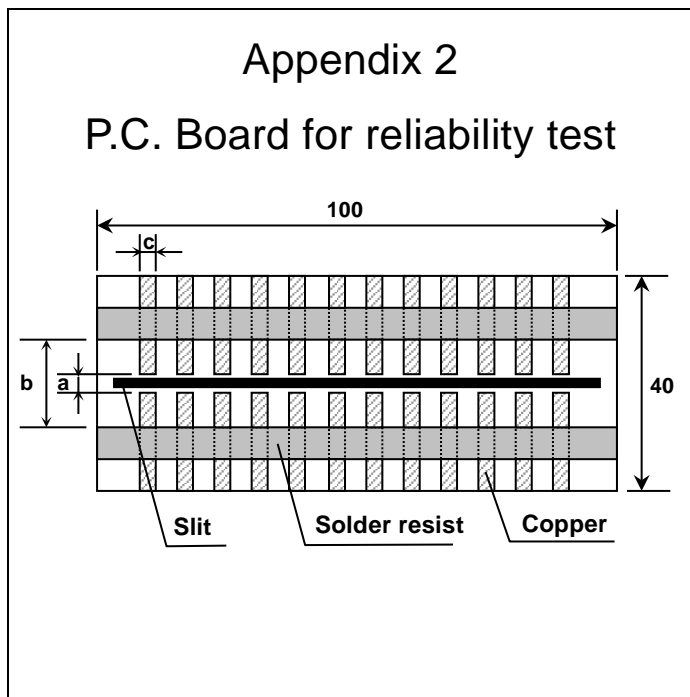
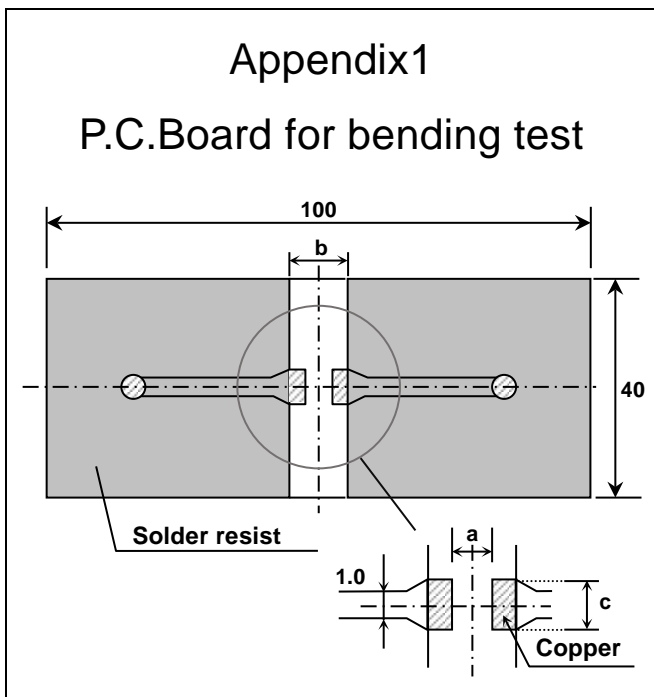
(continued)

No.	Item		Performance	Test or inspection method															
13	Temperature cycle	External appearance	No mechanical damage.	Expose the capacitors in the condition step1 through step 4 listed in the following table. Temp. cycle : 1,000 cycles <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature(°C)</th> <th>Time (min.)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-55 ± 3</td> <td>30 ± 3</td> </tr> <tr> <td>2</td> <td>Ambient Temp.</td> <td>2 ~ 5</td> </tr> <tr> <td>3</td> <td>125 ± 2</td> <td>30 ± 2</td> </tr> <tr> <td>4</td> <td>Ambient Temp.</td> <td>2 ~ 5</td> </tr> </tbody> </table> Leave the capacitors in ambient condition for Class 1 : 6~24h Class 2 : 24±2h before measurement. Reflow solder the capacitors on a P.C.Board shown in Appendix 2 before testing.	Step	Temperature(°C)	Time (min.)	1	-55 ± 3	30 ± 3	2	Ambient Temp.	2 ~ 5	3	125 ± 2	30 ± 2	4	Ambient Temp.	2 ~ 5
		Step	Temperature(°C)		Time (min.)														
		1	-55 ± 3		30 ± 3														
		2	Ambient Temp.		2 ~ 5														
		3	125 ± 2		30 ± 2														
		4	Ambient Temp.		2 ~ 5														
		Capacitance	Characteristics		Change from the value before test														
Class1	C0G		Please contact with our sales representative.																
Class2	X7R																		
Q (Class1)	Meet the initial spec.																		
D.F. (Class2)	Meet the initial spec.																		
Insulation Resistance	Meet the initial spec.																		
Voltage proof	No insulation breakdown or other damage.																		
14	Moisture Resistance (Steady State)	External appearance	No mechanical damage.	Test temp. : 40±2°C Test humidity : 90~95%RH Test time : 500 +24,0h Leave the capacitors in ambient condition for Class 1 : 6~24h Class 2 : 24±2h before measurement. Reflow solder the capacitors on a P.C.Board shown in Appendix2 before testing.															
		Capacitance	Characteristics		Change from the value before test														
			Class1		C0G	Please contact with our sales representative.													
			Class2		X7R														
		Q (Class1)	350 min.																
		D.F. (Class2)	200% of initial spec. max.																
Insulation Resistance	1,000MΩ min.																		

(continued)

No.	Item		Performance	Test or inspection method		
15	Moisture Resistance	External appearance	No mechanical damage.	Test temp. : 85±2°C Test humidity : 85%RH Applied voltage : 1kV DC Test time : 1,000 +48,0h Charge/discharge current : 50mA or lower Leave the capacitors in ambient condition for Class 1 : 6~24h Class 2 : 24±2h before measurement. Reflow solder the capacitors on a P.C.Board shown in Appendix2 before testing. Initial value setting (only for class 2) Voltage conditioning 《After voltage treat the capacitors under testing temperature and voltage for 1 hour,》 leave the capacitors in ambient condition for 24±2h before measurement. Use this measurement for initial value.		
		Capacitance	Characteristics		Change from the value before test	
			Class1		C0G	Please contact with our sales representative.
			Class2		X7R	
		Q (Class1)	200 min.			
D.F. (Class2)	200% of initial spec. max.					
	Insulation Resistance	500MΩ min.				
16	Life	External appearance	No mechanical damage.	Test temp. : 125±2°C Applied voltage : Please contact with our sales representative. Test time : 1,000 +48,0h Charge/discharge current : 50mA or lower Leave the capacitors in ambient condition for Class 1 : 6~24h Class 2 : 24±2h before measurement. Reflow solder the capacitors on a P.C.Board shown in Appendix2 before testing. Initial value setting (only for class 2) Voltage conditioning 《After voltage treat the capacitors under testing temperature and voltage for 1 hour,》 leave the capacitors in ambient condition for 24±2h before measurement. Use this measurement for initial value.		
		Capacitance	Characteristics		Change from the value before test	
			Class1		C0G	Please contact with our sales representative.
			Class2		X7R	
		Q (Class1)	350 min.			
D.F. (Class2)	200% of initial spec. max.					
	Insulation Resistance	1,000MΩ min.				

*As for the initial measurement of capacitors (Class2) on number 7,11,12,13 and 14 leave capacitors at 150 0,-10°C for 1 hour and measure the value after leaving capacitors for 24±2h in ambient condition.



(Unit : mm)

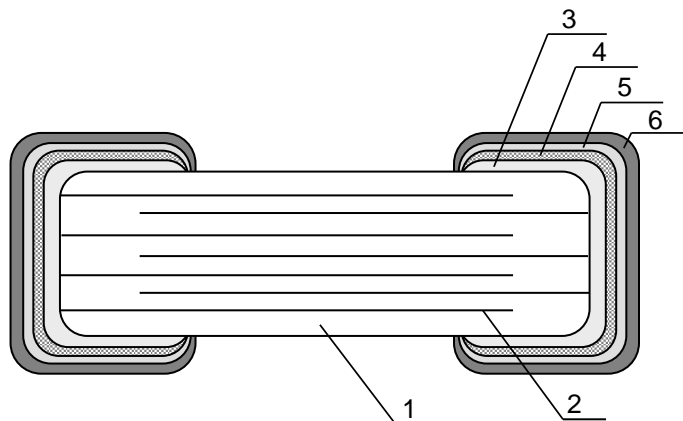
Symbol	Dimensions		
	a	b	c
CGA6 (CC1210)	2.2	5.0	2.9
CGA7 (CC1808)	3.5	7.0	2.5
CGA8 (CC1812)	3.5	7.0	3.7

1. Material : Glass Epoxy(As per JIS C6484 GE4)

2. Thickness : 1.6mm

- Copper(Thickness:0.035mm)
- Solder resist

8. INSIDE STRUCTURE AND MATERIAL



No.	NAME	MATERIAL	
		Class1	Class2
1	Dielectric	CaZrO ₃	BaTiO ₃
2	Electrode	Nickel (Ni)	
3	Termination	Copper (Cu)	
4		Conductive resin (Filler : Ag)	
5		Nickel (Ni)	
6		Tin (Sn)	

9. CAUTION FOR PRODUCTS WITH SOFT TERMINATION

This product contains Ag (Silver) as part of the middle layer of termination. To avoid electromigration of Ag under high temperature and humidity, and failures caused by corrosive gas, chip capacitors on P.C boards should be protected by moisture proof-sealing.

10. PACKAGING

Packaging shall be done to protect the components from the damage during transportation and storing, and a label which has the following information shall be attached.

Tape packaging is as per 14. TAPE PACKAGING SPECIFICATION.

- 1) Inspection No.
- 2) TDK P/N
- 3) Customer's P/N
- 4) Quantity

*Composition of Inspection No.

Example F 4 A - 23 - 001
 (a) (b) (c) (d) (e)

- (a) Line code
- (b) Last digit of the year
- (c) Month and A for January and B for February and so on. (Skip I)
- (d) Inspection Date of the month.
- (e) Serial No. of the day

*Composition of new Inspection No.

(Implemented on and after May 1, 2019 in sequence)

Example

I	F	4	E	2	3	A	0	0	1
---	---	---	---	---	---	---	---	---	---

 (a) (b) (c) (d) (e) (f) (g)

- (a) Prefix
- (b) Line code
- (c) Last digit of the year
- (d) Month and A for January and B for February and so on. (Skip I)
- (e) Inspection Date of the month.
- (f) Serial No. of the day(00 ~ ZZ)
- (g) Suffix(00 ~ ZZ)

* It was shifted to the new inspection No. on and after May 2019, but the implementation timing may be different depending on shipment bases.

Until the shift is completed, either current or new composition of inspection No. will be applied.

11. RECOMMENDATION


It is recommended to provide a slit (about 1mm wide) in the board under the components to improve washing Flux. And please make sure to dry detergent up completely before.


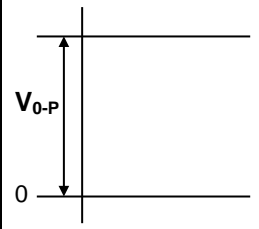
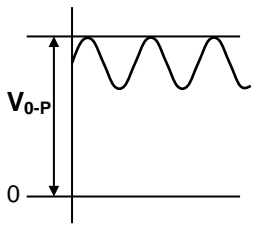
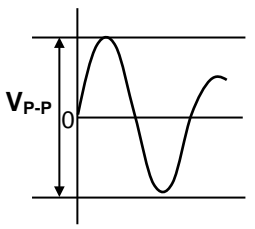
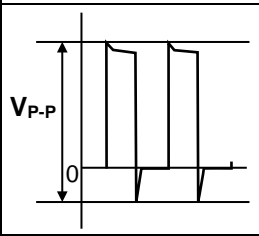
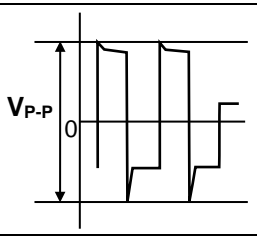
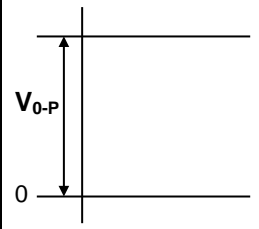
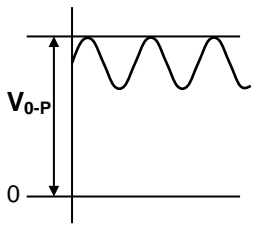
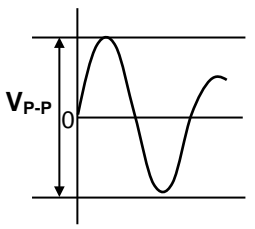
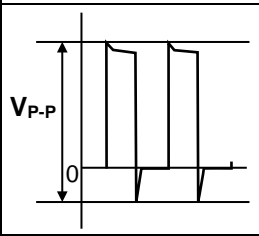
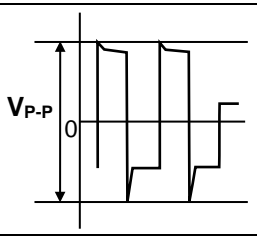
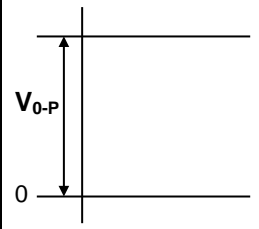
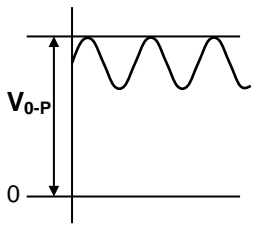
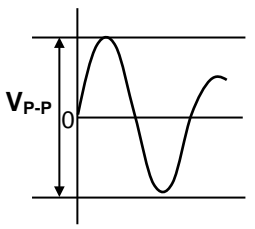
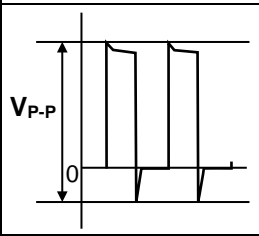
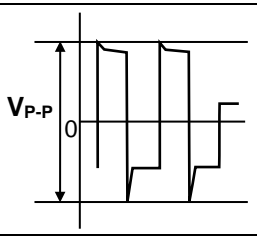
It is recommended to use activated flux (Chlorine content : less than 0.1wt%) such Rosin due to high voltage usage.

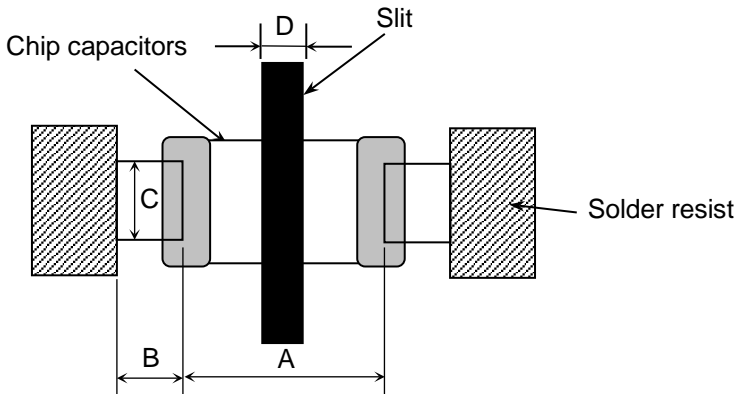
12. SOLDERING CONDITION

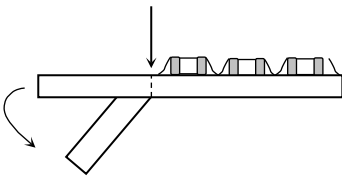
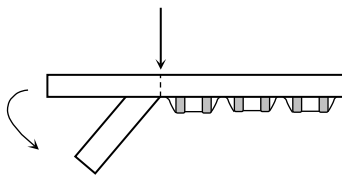
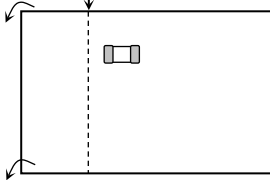
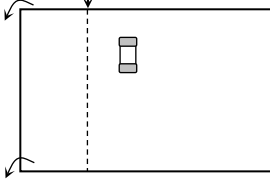
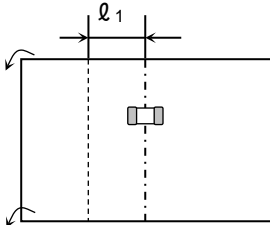
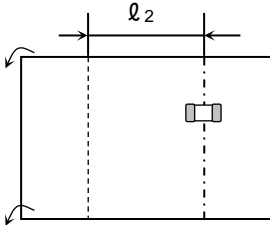
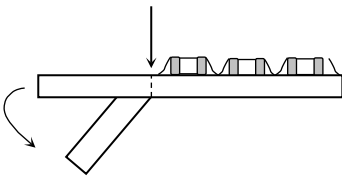
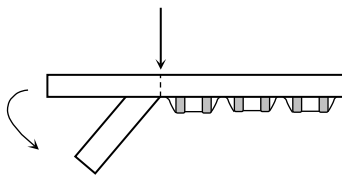
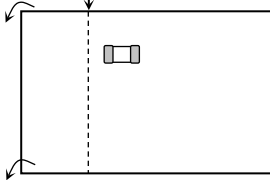
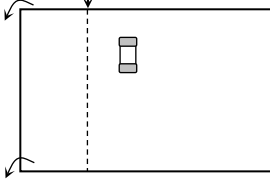
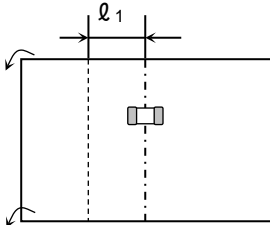
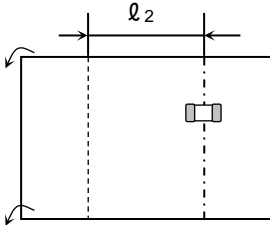
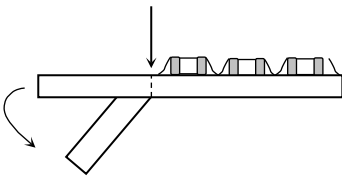
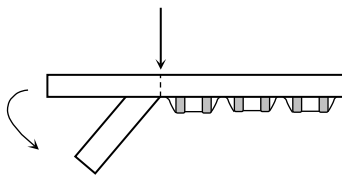
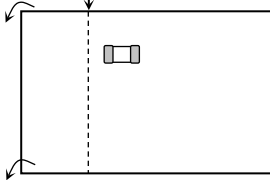
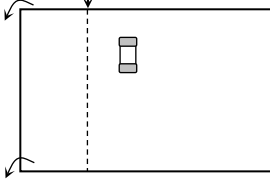
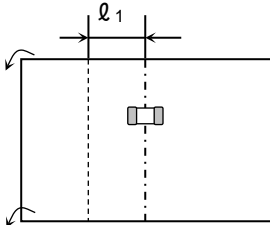
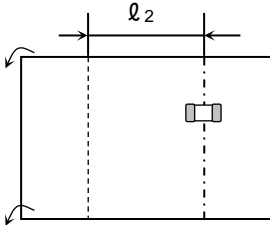
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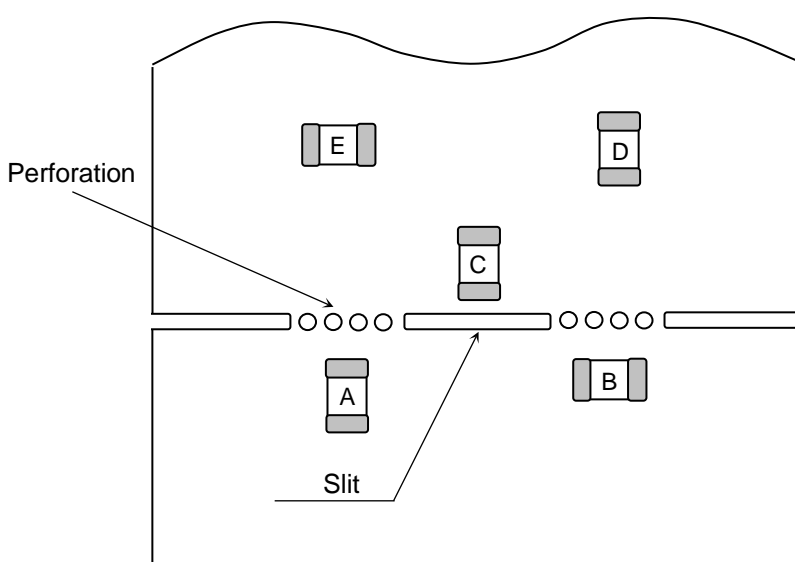
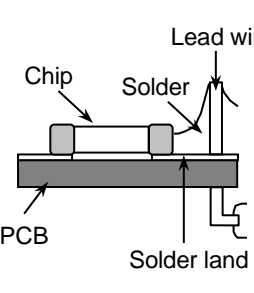
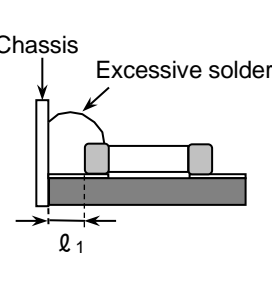
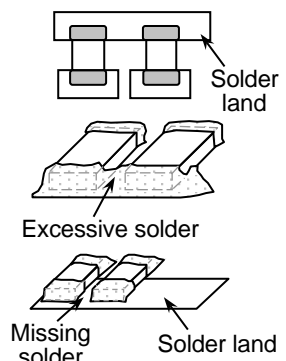
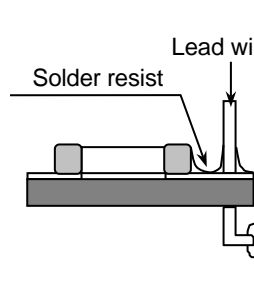
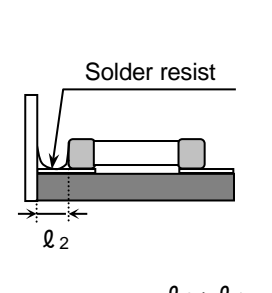
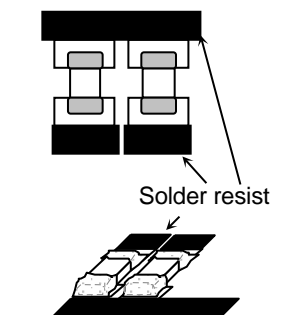
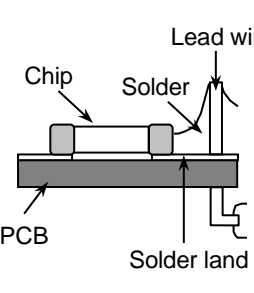
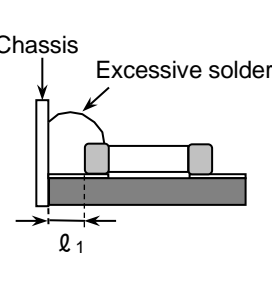
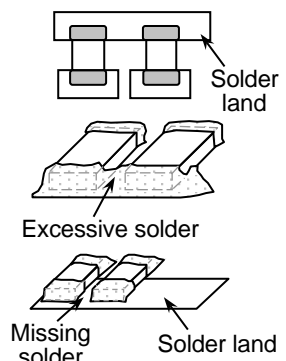
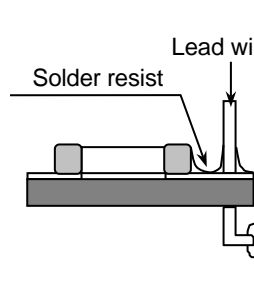
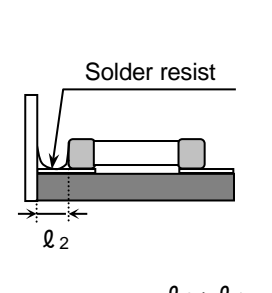
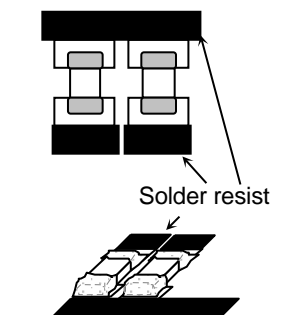
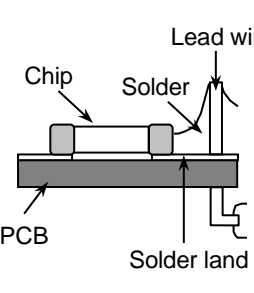
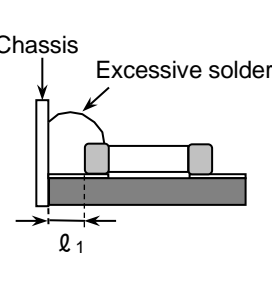
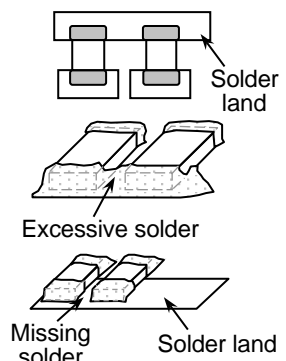
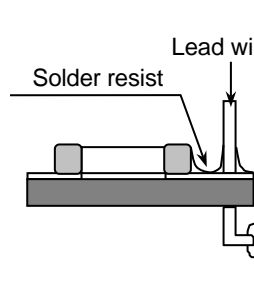
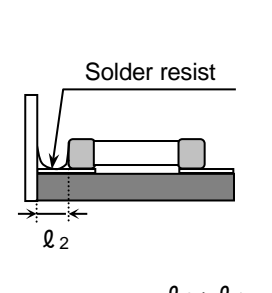
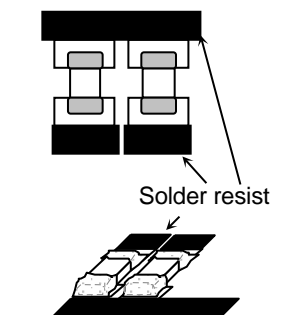
13. CAUTION

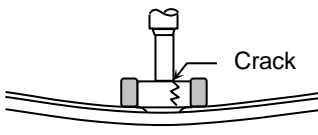
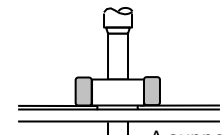
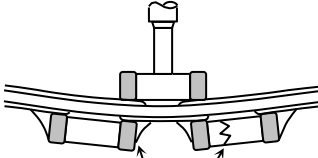
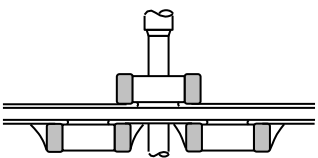
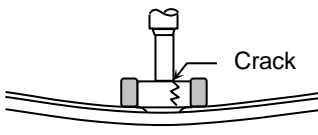
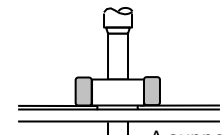
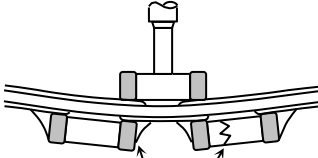
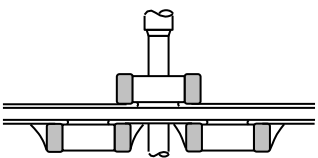
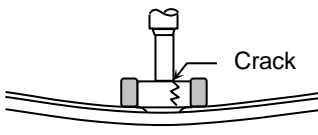
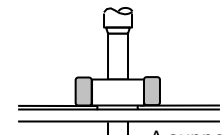
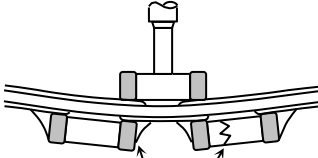
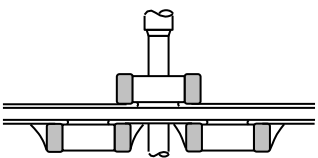
No.	Process	Condition
1	Operating Condition (Storage, Use, Transportation)	<p>1-1. Storage, Use</p> <p>The capacitors must be stored in an ambient temperature of 5 to 40°C with a relative humidity of 20 to 70%RH. JIS C 60721-3-1 Class 1K2 should be followed for the other climatic conditions.</p> <ol style="list-style-type: none"> 1) High temperature and humidity environment may affect a capacitor's solder ability because it accelerates terminal oxidization. They also deteriorate performance of taping and packaging. Therefore, SMD capacitors shall be used within 6 months. For capacitors with terminal electrodes consisting of silver or silver-palladium which tend to become oxidized or sulfurized, use as soon as possible, such as within one month after opening the bag. 2) When capacitors are stored for a longer time period than 6 months, confirm the solderability of the capacitors prior to use. During storage, keep the minimum packaging unit in its original packaging without opening it. Do not deviate from the above temperature and humidity conditions even for a short term. 3) Corrosive gasses in the air or atmosphere may result in deterioration of the reliability, such as poor solderability of the terminal electrodes. Do not store capacitors where they will be exposed to corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine ammonia etc.) 4) Solderability and electrical performance may deteriorate due to photochemical change in the terminal electrode if stored in direct sunlight, or due to condensation from rapid changes in humidity. The capacitors especially which use resin material must be operated and stored in an environment free of dew condensation, as moisture absorption due to condensation may affect the performance. 5) Refer to JIS C 60721-3-1, class 1K2 for other climate conditions. <p>1-2. Handling in transportation</p> <p>In case of the transportation of the capacitors, the performance of the capacitors may be deteriorated depending on the transportation condition. (Refer to JEITA RCR-2335C 9.2 Handling in transportation)</p>
2	Circuit design  Caution	<p>2-1. Operating temperature</p> <ol style="list-style-type: none"> 1) Upper category temperature (maximum operating temperature) is specified. It is necessary to select a capacitor whose rated temperature is higher than the operating temperature. Also, it is necessary to consider the temperature distribution in the equipment and seasonal temperature variation. 2) Surface temperature including self heating should be below maximum operating temperature. Due to dielectric loss, capacitors will heat itself when AC is applied due to ESR. Especially at high frequencies, please be careful that the heat might be so extreme. Also, even if the surface temperature of the capacitor includes self-heating and is the maximum operating temperature or lower, excessive heating of the capacitor due to self-heating may cause deterioration of the characteristics and reliability of the capacitor. The self-heating temperature rise of the capacitor changes depending on the difference in heat radiation due to the mounting method to the device, the ambient temperature, the cooling method of the device and circuit board material and the design, etc. The load should be contained so that the self-heating temperature rise of the capacitor body in a natural convection environment at an ambient temperature of 25°C remain below 20°C. <p>When using in a high-frequency circuit or a circuit in which a capacitor generates heat, such as when a high-frequency ripple current flows, pay attention to the above precautions. (Note that accurate measurement may not be possible with self-heating measurement when the equipment applies cooling other than natural convection such as a cooling fan.)</p>

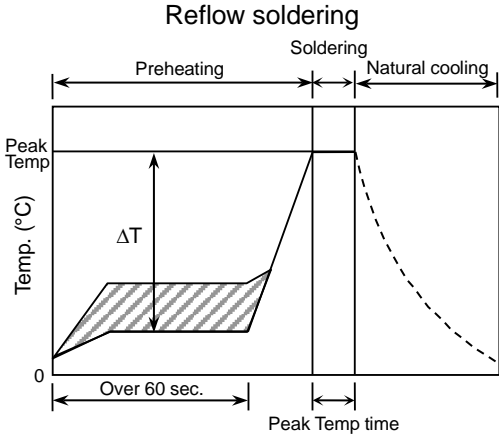
No.	Process	Condition														
2	Circuit design  Caution	<p>3) The electrical characteristics of the capacitors will vary depending on the temperature. The capacitors should be selected and designed in taking the temperature into consideration.</p> <p>2-2. When overvoltage is applied Applying overvoltage to a capacitor may cause dielectric breakdown and result in a short circuit. The duration until dielectric breakdown depends on the applied voltage and the ambient temperature.</p> <p>2-3. Operating voltage 1) Operating voltage across the terminals should be below the rated voltage. When AC and DC are super imposed, V_{0-P} must be below the rated voltage. — (1) and (2) AC or pulse with overshooting, V_{P-P} must be below the rated voltage. — (3), (4) and (5) When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use the capacitors within rated voltage containing these Irregular voltage.</p> <table border="1" data-bbox="469 757 1442 1025"> <thead> <tr> <th data-bbox="469 757 655 790">Voltage</th> <th data-bbox="660 757 919 790">(1) DC voltage</th> <th data-bbox="924 757 1182 790">(2) DC+AC voltage</th> <th data-bbox="1187 757 1442 790">(3) AC voltage</th> </tr> </thead> <tbody> <tr> <td data-bbox="469 797 655 1025">Positional Measurement (Rated voltage)</td> <td data-bbox="660 797 919 1025">  </td> <td data-bbox="924 797 1182 1025">  </td> <td data-bbox="1187 797 1442 1025">  </td> </tr> </tbody> </table> <table border="1" data-bbox="469 1055 1182 1330"> <thead> <tr> <th data-bbox="469 1055 655 1088">Voltage</th> <th data-bbox="660 1055 919 1088">(4) Pulse voltage (A)</th> <th data-bbox="924 1055 1182 1088">(5) Pulse voltage (B)</th> </tr> </thead> <tbody> <tr> <td data-bbox="469 1095 655 1330">Positional Measurement (Rated voltage)</td> <td data-bbox="660 1095 919 1330">  </td> <td data-bbox="924 1095 1182 1330">  </td> </tr> </tbody> </table> <p>2) Even below the rated voltage, if repetitive high frequency AC or pulse is applied, the reliability of the capacitors may be reduced.</p> <p>3) The effective capacitance will vary depending on applied DC and AC voltages. The capacitors should be selected and designed in taking the voltages into consideration.</p> <p>4) Abnormal voltage (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated voltage.</p> <p>5) When capacitors are used in a series connection, it is necessary to add a balancing circuit such as voltage dividing resistors in order to avoid an imbalance in the voltage applied to each capacitor.</p> <p>2-4. Frequency When the capacitors (Class 2) are used in AC and/or pulse voltages, the capacitors may vibrate themselves and generate audible sound.</p>	Voltage	(1) DC voltage	(2) DC+AC voltage	(3) AC voltage	Positional Measurement (Rated voltage)				Voltage	(4) Pulse voltage (A)	(5) Pulse voltage (B)	Positional Measurement (Rated voltage)		
Voltage	(1) DC voltage	(2) DC+AC voltage	(3) AC voltage													
Positional Measurement (Rated voltage)																
Voltage	(4) Pulse voltage (A)	(5) Pulse voltage (B)														
Positional Measurement (Rated voltage)																

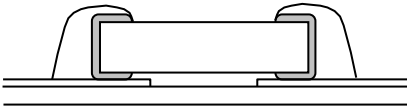
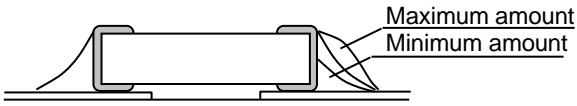
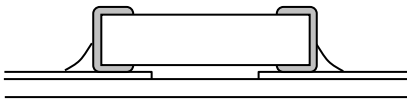
No.	Process	Condition																																			
3	Designing P.C.board	<p>The amount of solder at the terminations has a direct effect on the reliability of the capacitors.</p> <ol style="list-style-type: none"> 1) The greater the amount of solder, the higher the stress on the chip capacitors, and the more likely that it will break. When designing a P.C.board, determine the shape and size of the solder lands to have proper amount of solder on the terminations. 2) Avoid using common solder land for multiple terminations and provide individual solder land for each terminations. 3) Size and recommended land dimensions. <div style="text-align: center;">  </div> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th colspan="2" style="text-align: left;">Reflow soldering</th> <th colspan="3" style="text-align: right;">(mm)</th> </tr> <tr> <th style="text-align: left;">Case size</th> <th></th> <th>CGA6 (CC1210)</th> <th>CGA7 (CC1808)</th> <th>CGA8 (CC1812)</th> </tr> <tr> <th style="text-align: left;">Symbol</th> <th></th> <th></th> <th></th> <th></th> </tr> </thead> <tbody> <tr> <td>A</td> <td></td> <td>2.0 ~ 2.4</td> <td>3.1 ~ 3.7</td> <td>3.1 ~ 3.7</td> </tr> <tr> <td>B</td> <td></td> <td>1.0 ~ 1.2</td> <td>1.2 ~ 1.4</td> <td>1.2 ~ 1.4</td> </tr> <tr> <td>C</td> <td></td> <td>1.9 ~ 2.5</td> <td>1.5 ~ 2.0</td> <td>2.4 ~ 3.2</td> </tr> <tr> <td>D</td> <td></td> <td>1.0 ~ 1.3</td> <td>1.0 ~ 1.3</td> <td>1.0 ~ 1.3</td> </tr> </tbody> </table> <ol style="list-style-type: none"> 4) It is recommended to provide a slit (about 1mm width) in the board under the components to improve washing flux. And please make sure to dry detergent up completely before. <p>It is recommended to use low activated flux (Chlorine content : less than 0.1wt%) such Rosin due to high voltage usage.</p>	Reflow soldering		(mm)			Case size		CGA6 (CC1210)	CGA7 (CC1808)	CGA8 (CC1812)	Symbol					A		2.0 ~ 2.4	3.1 ~ 3.7	3.1 ~ 3.7	B		1.0 ~ 1.2	1.2 ~ 1.4	1.2 ~ 1.4	C		1.9 ~ 2.5	1.5 ~ 2.0	2.4 ~ 3.2	D		1.0 ~ 1.3	1.0 ~ 1.3	1.0 ~ 1.3
Reflow soldering		(mm)																																			
Case size		CGA6 (CC1210)	CGA7 (CC1808)	CGA8 (CC1812)																																	
Symbol																																					
A		2.0 ~ 2.4	3.1 ~ 3.7	3.1 ~ 3.7																																	
B		1.0 ~ 1.2	1.2 ~ 1.4	1.2 ~ 1.4																																	
C		1.9 ~ 2.5	1.5 ~ 2.0	2.4 ~ 3.2																																	
D		1.0 ~ 1.3	1.0 ~ 1.3	1.0 ~ 1.3																																	

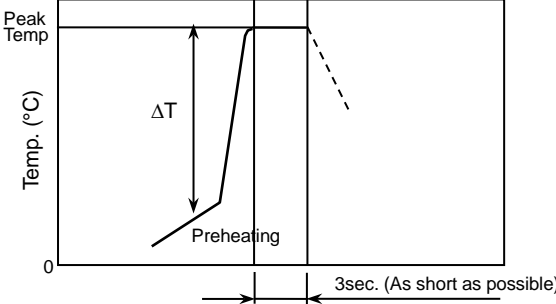
No.	Process	Condition												
3	Designing P.C.board	<p>5) Recommended chip capacitors layout is as following.</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 15%;"></th> <th style="width: 35%;">Disadvantage against bending stress</th> <th style="width: 35%;">Advantage against bending stress</th> </tr> </thead> <tbody> <tr> <td style="text-align: center; vertical-align: middle;">Mounting face</td> <td style="text-align: center;"> <p>Perforation or slit</p>  <p>Break P.C.board with mounted side up.</p> </td> <td style="text-align: center;"> <p>Perforation or slit</p>  <p>Break P.C.board with mounted side down.</p> </td> </tr> <tr> <td style="text-align: center; vertical-align: middle;">Chip arrangement (Direction)</td> <td style="text-align: center;"> <p>Perforation or slit</p>  </td> <td style="text-align: center;"> <p>Perforation or slit</p>  </td> </tr> <tr> <td style="text-align: center; vertical-align: middle;">Distance from slit</td> <td style="text-align: center;"> <p>Closer to slit is higher stress</p>  <p>$(l_1 < l_2)$</p> </td> <td style="text-align: center;"> <p>Away from slit is less stress</p>  <p>$(l_1 < l_2)$</p> </td> </tr> </tbody> </table>		Disadvantage against bending stress	Advantage against bending stress	Mounting face	<p>Perforation or slit</p>  <p>Break P.C.board with mounted side up.</p>	<p>Perforation or slit</p>  <p>Break P.C.board with mounted side down.</p>	Chip arrangement (Direction)	<p>Perforation or slit</p> 	<p>Perforation or slit</p> 	Distance from slit	<p>Closer to slit is higher stress</p>  <p>$(l_1 < l_2)$</p>	<p>Away from slit is less stress</p>  <p>$(l_1 < l_2)$</p>
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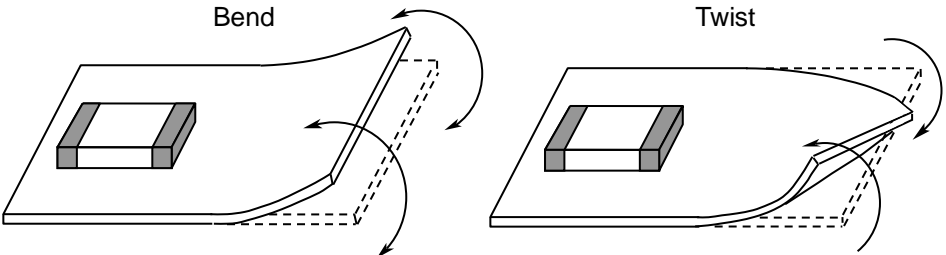
No.	Process	Condition												
3	Designing P.C.board	<p>5) Mechanical stress varies according to location of chip capacitors on the P.C.board.</p>  <p>Stress force $A > B > E$ $A > D > E$ $A > C$</p> <p>When dividing printed wiring boards, the intensities of mechanical stress applied to capacitors are different according to each dividing method in the order of : Push-back < Slit < V-groove < Perforation. Therefore consider not only position of capacitors, but also the way of the dividing the printed wiring boards.</p> <p>6) Layout recommendation</p> <table border="1" data-bbox="375 1030 1481 1926"> <thead> <tr> <th data-bbox="375 1030 539 1142">Example</th> <th data-bbox="539 1030 842 1142">Use of common solder land</th> <th data-bbox="842 1030 1150 1142">Soldering with chassis</th> <th data-bbox="1150 1030 1481 1142">Use of common solder land with other SMD</th> </tr> </thead> <tbody> <tr> <td data-bbox="375 1142 539 1523">Need to avoid</td> <td data-bbox="539 1142 842 1523">  </td> <td data-bbox="842 1142 1150 1523">  </td> <td data-bbox="1150 1142 1481 1523">  </td> </tr> <tr> <td data-bbox="375 1523 539 1926">Recommendation</td> <td data-bbox="539 1523 842 1926">  </td> <td data-bbox="842 1523 1150 1926">  <p>$l_2 > l_1$</p> </td> <td data-bbox="1150 1523 1481 1926">  </td> </tr> </tbody> </table> <p>7) When mounting on an aluminum substrate, it is more likely to be affected by heat stress from the substrate. Please inquire separate specification when mounted on the substrate.</p>	Example	Use of common solder land	Soldering with chassis	Use of common solder land with other SMD	Need to avoid				Recommendation		 <p>$l_2 > l_1$</p>	
Example	Use of common solder land	Soldering with chassis	Use of common solder land with other SMD											
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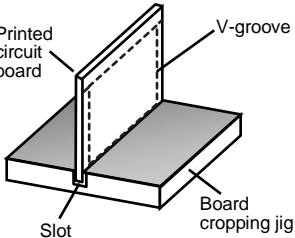
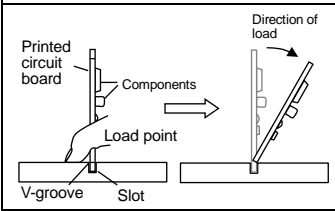
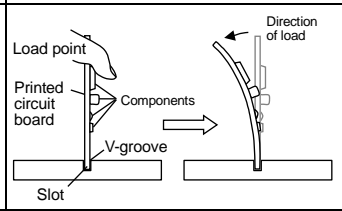
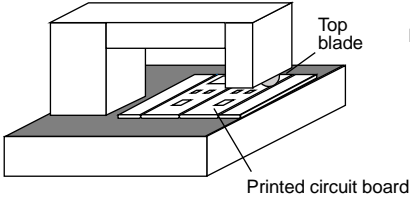
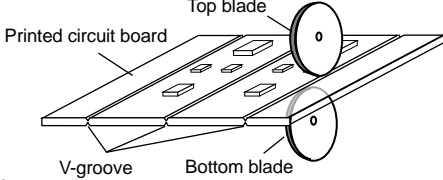
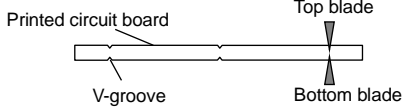
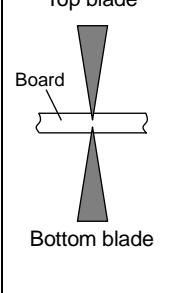
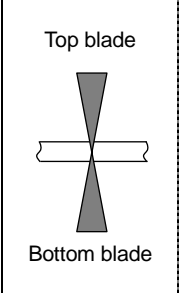
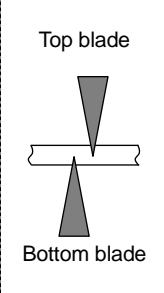
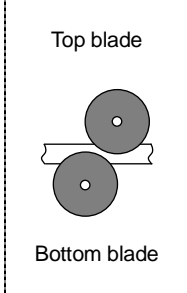
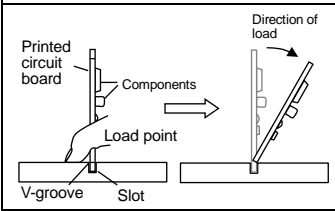
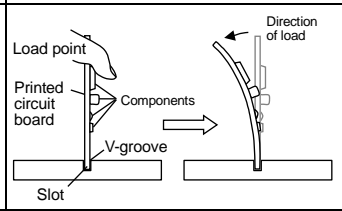
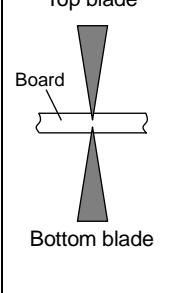
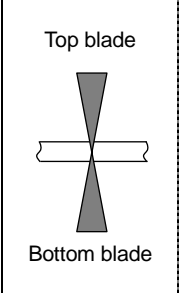
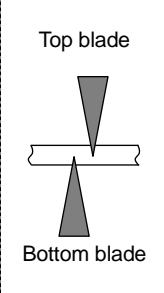
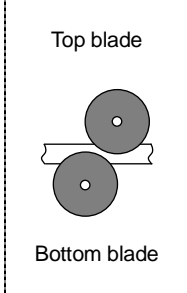
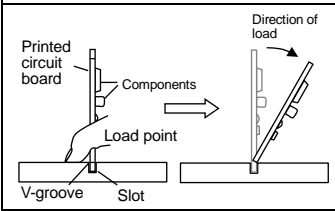
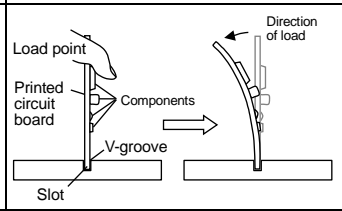
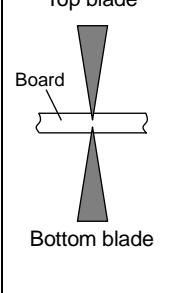
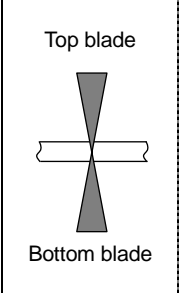
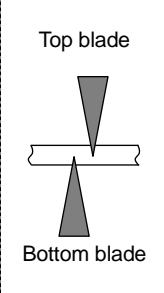
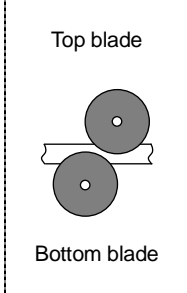
No.	Process	Condition									
4	Mounting	<p>4-1. Stress from mounting head</p> <p>If the mounting head is adjusted too low, it may induce excessive stress in the chip capacitors to result in cracking. Please take following precautions.</p> <ol style="list-style-type: none"> 1) Adjust the bottom dead center of the mounting head to reach on the P.C.board surface and not press it. 2) Adjust the mounting head pressure to be 1 to 3N of static weight. 3) To minimize the impact energy from mounting head, it is important to provide support from the bottom side of the P.C.board. See following examples. <table border="1" data-bbox="486 600 1436 1160"> <thead> <tr> <th data-bbox="486 600 667 649"></th> <th data-bbox="667 600 1062 649">Not recommended</th> <th data-bbox="1062 600 1436 649">Recommended</th> </tr> </thead> <tbody> <tr> <td data-bbox="486 649 667 896">Single sided mounting</td> <td data-bbox="667 649 1062 896">  <p>Crack</p> </td> <td data-bbox="1062 649 1436 896">  <p>Support pin</p> <p>A support pin is not to be underneath the capacitor.</p> </td> </tr> <tr> <td data-bbox="486 896 667 1160">Double-sides mounting</td> <td data-bbox="667 896 1062 1160">  <p>Solder peeling</p> <p>Crack</p> </td> <td data-bbox="1062 896 1436 1160">  <p>Support pin</p> </td> </tr> </tbody> </table> <p>When the centering jaw is worn out, it may give mechanical impact on the capacitors to cause crack. Please control the close up dimension of the centering jaw and provide sufficient preventive maintenance and replacement of it.</p>		Not recommended	Recommended	Single sided mounting	 <p>Crack</p>	 <p>Support pin</p> <p>A support pin is not to be underneath the capacitor.</p>	Double-sides mounting	 <p>Solder peeling</p> <p>Crack</p>	 <p>Support pin</p>
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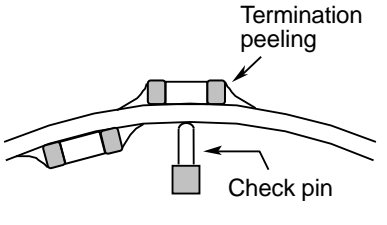
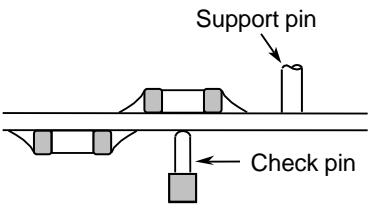
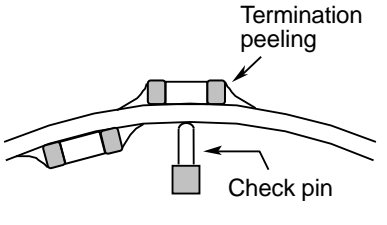
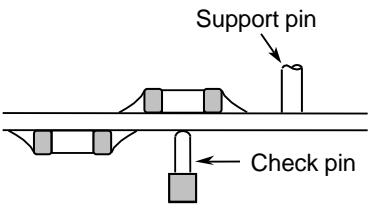
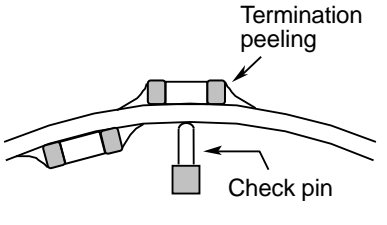
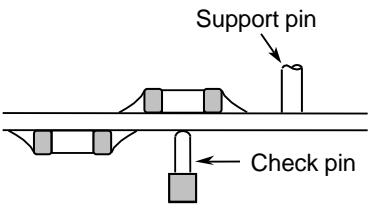
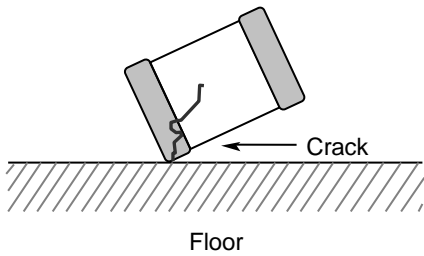
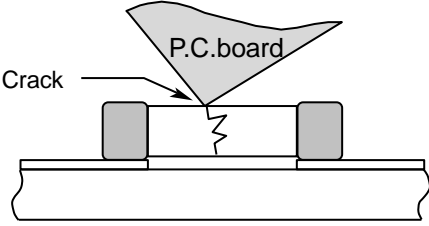
No.	Process	Condition																		
5	Soldering	<p>5-1. Flux selection</p> <p>Flux can seriously affect the performance of capacitors. Confirm the following to select the appropriate flux.</p> <ol style="list-style-type: none"> 1) It is recommended to use a mildly activated rosin flux (less than 0.1wt% chlorine). Strong flux is not recommended. 2) Excessive flux must be avoided. Please provide proper amount of flux. 3) When water-soluble flux is used, enough washing is necessary. <p>5-2. Recommended Reflow soldering temperature profile</p> <div style="text-align: center;">  <p>The graph shows a temperature profile for reflow soldering. The y-axis is labeled 'Temp. (°C)' and the x-axis is labeled 'Time'. The profile is divided into three phases: 'Preheating', 'Soldering', and 'Natural cooling'. The 'Preheating' phase shows a ramp up to a temperature level, followed by a horizontal segment. A shaded area under this horizontal segment is labeled 'Over 60 sec.'. The 'Soldering' phase shows a further ramp up to a 'Peak Temp', followed by a horizontal segment labeled 'Peak Temp time'. The 'Natural cooling' phase shows a dashed line representing the temperature decreasing. A vertical double-headed arrow between the preheating level and the peak is labeled 'ΔT'.</p> </div> <p>5-3. Recommended soldering peak temp and peak temp duration for Reflow soldering</p> <p>Pb free solder is recommended, but if Sn-37Pb must be used, refer to below.</p> <table border="1" data-bbox="590 1124 1334 1357"> <thead> <tr> <th rowspan="2" style="text-align: center;">Temp./Duration</th> <th colspan="2" style="text-align: center;">Reflow soldering</th> </tr> <tr> <th style="text-align: center;">Peak temp(°C)</th> <th style="text-align: center;">Duration(sec.)</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Solder</td> <td></td> <td></td> </tr> <tr> <td style="text-align: center;">Lead Free Solder</td> <td style="text-align: center;">260 max.</td> <td style="text-align: center;">10 max.</td> </tr> <tr> <td style="text-align: center;">Sn-Pb Solder</td> <td style="text-align: center;">230 max.</td> <td style="text-align: center;">20 max.</td> </tr> </tbody> </table> <p>Recommended solder compositions Lead Free Solder : Sn-3.0Ag-0.5Cu</p> <p>5-4. Avoiding thermal shock</p> <ol style="list-style-type: none"> 1) Preheating condition <table border="1" data-bbox="539 1554 1184 1653" style="margin-left: 40px;"> <thead> <tr> <th style="text-align: center;">Soldering</th> <th style="text-align: center;">Temp. (°C)</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Reflow soldering</td> <td style="text-align: center;">$\Delta T \leq 130$</td> </tr> </tbody> </table> 2) Cooling condition <p>Natural cooling using air is recommended. If the chips are dipped into a solvent for cleaning, the temperature difference (ΔT) must be less than 100°C.</p> 	Temp./Duration	Reflow soldering		Peak temp(°C)	Duration(sec.)	Solder			Lead Free Solder	260 max.	10 max.	Sn-Pb Solder	230 max.	20 max.	Soldering	Temp. (°C)	Reflow soldering	$\Delta T \leq 130$
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No.	Process	Condition
5	Soldering	<p data-bbox="437 192 692 221">5-5. Amount of solder</p> <p data-bbox="520 228 1461 315">Excessive solder will induce higher tensile force in chip capacitors when temperature changes and it may result in chip cracking. In sufficient solder may detach the capacitors from the P.C.board.</p> <hr/> <div style="display: flex; justify-content: space-between;"> <div data-bbox="496 374 617 439">Excessive solder</div> <div data-bbox="683 356 1091 461">  </div> <div data-bbox="1123 360 1406 450">Higher tensile force in chip capacitors to cause crack</div> </div> <hr/> <div style="display: flex; justify-content: space-between;"> <div data-bbox="496 544 612 573">Adequate</div> <div data-bbox="683 504 1262 609">  </div> </div> <hr/> <div style="display: flex; justify-content: space-between;"> <div data-bbox="496 678 625 743">Insufficient solder</div> <div data-bbox="683 663 1091 768">  </div> <div data-bbox="1123 651 1406 770">Low robustness may cause contact failure or chip capacitors come off the P.C.board.</div> </div> <hr/> <p data-bbox="437 842 644 871">5-6. Sn-Zn solder</p> <p data-bbox="464 878 1145 936">Sn-Zn solder affects product reliability. Please contact TDK in advance when utilize Sn-Zn solder.</p> <p data-bbox="437 972 858 1001">5-7. Countermeasure for tombstone</p> <p data-bbox="464 1008 1433 1126">The misalignment between the mounted positions of the capacitors and the land patterns should be minimized. The tombstone phenomenon may occur especially the capacitors are mounted (in longitudinal direction) in the same direction of the reflow soldering.</p> <p data-bbox="464 1133 1422 1191">(Refer to JEITA RCR-2335C Annex A (Informative), Recommendations to prevent the tombstone phenomenon.)</p>

No.	Process	Condition																
6	Solder repairing	<p>Solder repairing is unavoidable, refer to below.</p> <p>6-1. Solder repair by solder iron</p> <p>1) Selection of the soldering iron tip</p> <p>Tip temperature of solder iron varies by its type, P.C.board material and solder land size. The higher the tip temperature, the quicker the operation. However, heat shock may cause a crack in the chip capacitors.</p> <p>Please make sure the tip temp. before soldering and keep the peak temp and time in accordance with following recommended condition.</p> <div style="text-align: center;"> <p>Manual soldering (Solder iron)</p>  </div> <table border="1" style="margin: 10px auto; width: 80%; border-collapse: collapse;"> <thead> <tr> <th colspan="4" style="text-align: center;">Recommended solder iron condition (Sn-Pb Solder and Lead Free Solder)</th> </tr> <tr> <th style="width: 25%;">Temp. (°C)</th> <th style="width: 25%;">Duration (sec.)</th> <th style="width: 25%;">Wattage (W)</th> <th style="width: 25%;">Shape (mm)</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">280 max.</td> <td style="text-align: center;">3 max.</td> <td style="text-align: center;">20 max.</td> <td style="text-align: center;">Ø 3.0 max.</td> </tr> </tbody> </table> <p>* Please preheat the chip capacitors with the condition in 6-2 to avoid the thermal shock.</p> <p>2) Direct contact of the soldering iron with ceramic dielectric of chip capacitors may cause crack. Do not touch the ceramic dielectric and the terminations by solder iron.</p> <p>3) It is not recommended to reuse dismantled capacitors.</p> <p>6-2. Avoiding thermal shock</p> <p>Preheating condition</p> <table border="1" style="margin: 10px auto; width: 60%; border-collapse: collapse;"> <thead> <tr> <th style="width: 50%;">Soldering</th> <th style="width: 50%;">Temp. (°C)</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Manual soldering</td> <td style="text-align: center;">$\Delta T \leq 130$</td> </tr> </tbody> </table>	Recommended solder iron condition (Sn-Pb Solder and Lead Free Solder)				Temp. (°C)	Duration (sec.)	Wattage (W)	Shape (mm)	280 max.	3 max.	20 max.	Ø 3.0 max.	Soldering	Temp. (°C)	Manual soldering	$\Delta T \leq 130$
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No.	Process	Condition
7	Cleaning	<p>1) If an unsuitable cleaning fluid is used, flux residue or some foreign articles may stick to chip capacitors surface to deteriorate especially the insulation resistance.</p> <p>2) If cleaning condition is not suitable, it may damage the chip capacitors.</p> <p>2)-1. Insufficient washing</p> <p>(1) Terminal electrodes may corrode by Halogen in the flux.</p> <p>(2) Halogen in the flux may adhere on the surface of capacitors, and lower the insulation resistance.</p> <p>(3) Water soluble flux has higher tendency to have above mentioned problems (1) and (2).</p> <p>2)-2. Excessive washing</p> <p>When ultrasonic cleaning equipment is used, excessive ultrasonic power or direct vibration transfer to a printed wiring board may generate a resonant vibration in the board. This may cause a crack in a capacitor or its solder joints to the board and degradation in the terminal strength of the capacitor. In order to avoid this, the following cleaning conditions are recommended.</p> <p style="text-align: center;">Power : 20 W/ ℓ max. Frequency : 40 kHz max. Washing time : 5 minutes max.</p> <p>2)-3. If the cleaning fluid is contaminated, density of Halogen increases, and it may bring the same result as insufficient cleaning.</p>
8	Coating and molding of the P.C.board	<p>1) This product contains Ag (Silver) as part of the middle layer of termination. To avoid electromigration of Ag under high temperature and humidity, and failures caused by corrosive gas, chip capacitors on P.C boards should be protected by moisture proof-sealing.</p> <p>2) When the P.C.board is coated, please verify the quality influence on the product.</p> <p>3) Please verify carefully that there is no harmful decomposing or reaction gas emission during curing which may damage the chip capacitors.</p> <p>4) Please verify the curing temperature.</p>
9	Handling after chip mounted ⚠ Caution	<p>1) Please pay attention not to bend or distort the P.C.board after soldering in handling otherwise the chip capacitors may crack.</p> <div style="text-align: center;">  </div>

No.	Process	Condition																
9	Handling after chip mounted  Caution	<p>2) Printed circuit board cropping should not be carried out by hand, but by using the proper tooling. Printed circuit board cropping should be carried out using a board cropping jig as shown in the following figure or a board cropping apparatus to prevent inducing mechanical stress on the board.</p> <p>(1) Example of a board cropping jig Recommended example: The board should be pushed from the back side, close to the cropping jig so that the board is not bent and the stress applied to the capacitor is compressive. Unrecommended example: If the pushing point is far from the cropping jig and the pushing direction is from the front side of the board, large tensile stress is applied to the capacitor, which may cause cracks.</p> <div style="display: flex; justify-content: space-around;"> <div data-bbox="459 589 754 869"> <p>Outline of jig</p>  </div> <div data-bbox="762 577 1441 842"> <table border="1"> <thead> <tr> <th data-bbox="762 577 1098 633">Recommended</th> <th data-bbox="1098 577 1441 633">Unrecommended</th> </tr> </thead> <tbody> <tr> <td data-bbox="762 633 1098 842">  </td> <td data-bbox="1098 633 1441 842">  </td> </tr> </tbody> </table> </div> </div> <p>(2) Example of a board cropping machine</p> <p>An outline of a printed circuit board cropping machine is shown below. The top and bottom blades are aligned with one another along the lines with the V-grooves on printed circuit board when cropping the board.</p> <p>Unrecommended example: Misalignment of blade position between top and bottom, right and left, or front and rear blades may cause a crack in the capacitor.</p> <div style="display: flex; justify-content: space-around; margin-top: 20px;"> <div data-bbox="555 1171 967 1435"> <p>Outline of machine</p>  </div> <div data-bbox="962 1171 1406 1417"> <p>Principle of operation</p>  </div> </div> <div style="text-align: center; margin-top: 20px;"> <p>Cross-section diagram</p>  </div> <table border="1" style="width: 100%; margin-top: 20px; text-align: center;"> <thead> <tr> <th data-bbox="643 1653 823 1742">Recommended</th> <th colspan="3" data-bbox="823 1653 1353 1697">Unrecommended</th> </tr> <tr> <th data-bbox="643 1742 823 1787"></th> <th data-bbox="823 1697 1002 1787">Top-bottom misalignment</th> <th data-bbox="1002 1697 1166 1787">Left-right misalignment</th> <th data-bbox="1166 1697 1353 1787">Front-rear misalignment</th> </tr> </thead> <tbody> <tr> <td data-bbox="643 1787 823 2078">  </td> <td data-bbox="823 1787 1002 2078">  </td> <td data-bbox="1002 1787 1166 2078">  </td> <td data-bbox="1166 1787 1353 2078">  </td> </tr> </tbody> </table>	Recommended	Unrecommended			Recommended	Unrecommended				Top-bottom misalignment	Left-right misalignment	Front-rear misalignment				
Recommended	Unrecommended																	
																		
Recommended	Unrecommended																	
	Top-bottom misalignment	Left-right misalignment	Front-rear misalignment															
																		

No.	Process	Condition						
9	Handling after chip mounted  Caution	<p data-bbox="475 174 1460 315">3) When functional check of the P.C.board is performed, check pin pressure tends to be adjusted higher for fear of loose contact. But if the pressure is excessive and bend the P.C.board, it may crack the chip capacitors or peel the terminations off. Please adjust the check pins not to bend the P.C.board.</p> <table border="1" data-bbox="475 371 1433 674"> <thead> <tr> <th data-bbox="475 371 616 434">Item</th> <th data-bbox="616 371 1034 434">Not recommended</th> <th data-bbox="1034 371 1433 434">Recommended</th> </tr> </thead> <tbody> <tr> <td data-bbox="475 434 616 674">Board bending</td> <td data-bbox="616 434 1034 674">  </td> <td data-bbox="1034 434 1433 674">  </td> </tr> </tbody> </table>	Item	Not recommended	Recommended	Board bending		
Item	Not recommended	Recommended						
Board bending								
10	Handling of loose chip capacitors	<p data-bbox="475 728 1460 837">1) If dropped the chip capacitors may crack. Once dropped do not use it. Especially, the large case sized chip capacitors are tendency to have cracks easily, so please handle with care.</p>  <p data-bbox="475 1070 1460 1137">2) Piling the P.C.board after mounting for storage or handling, the corner of the P.C. board may hit the chip capacitors of another board to cause crack.</p> 						
11	Capacitance aging	<p data-bbox="475 1429 1460 1525">The capacitors (Class 2) have aging in the capacitance. They may not be used in precision time constant circuit. In case of the time constant circuit, the evaluation should be done well.</p>						
12	Estimated life and estimated failure rate of capacitors	<p data-bbox="475 1563 1460 1727">As per the estimated life and the estimated failure rate depend on the temperature and the voltage. This can be calculated by the equation described in JEITA RCR-2335C Annex F (Informative) Calculation of the estimated lifetime and the estimated failure rate (Voltage acceleration coefficient : 3 multiplication rule, Temperature acceleration coefficient : 10°C rule)</p> <p data-bbox="475 1733 1460 1800">The failure rate can be decreased by reducing the temperature and the voltage but they will not be guaranteed.</p>						

No.	Process	Condition
13	Caution during operation of equipment	<p>1) A capacitor shall not be touched directly with bare hands during operation in order to avoid electric shock. Electric energy held by the capacitor may be discharged through the human body when touched with a bare hand. Even when the equipment is off, a capacitor may stay charged. The capacitor should be handled after being completely discharged using a resistor.</p> <p>2) The terminals of a capacitor shall not be short-circuited by any accidental contact with a conductive object. A capacitor shall not be exposed to a conductive liquid such as an acid or alkali solution. A conductive object or liquid, such as acid and alkali, between the terminals may lead to the breakdown of a capacitor due to short circuit</p> <p>3) Confirm that the environment to which the equipment will be exposed during transportation and operation meets the specified conditions. Do not to use the equipment in the following environments.</p> <p>(1) Environment where a capacitor is splattered with water or oil (2) Environment where a capacitor is exposed to direct sunlight (3) Environment where a capacitor is exposed to Ozone, ultraviolet rays or radiation (4) Environment where a capacitor exposed to corrosive gas(e.g. hydrogen sulfide, sulfur dioxide, chlorine. ammonia gas etc.) (5) Environment where a capacitor exposed to vibration or mechanical shock exceeding the specified limits. (6) Atmosphere change with causes condensation</p>
14	Others  Caution	<p>The product listed in this specification is intended for use in automotive applications under-normal operation and usage conditions.</p> <p>The product is not designed or warranted to meet the requirements of application listed below, whose performance and/or quality requires a more stringent level of safety or reliability, or whose failure, malfunction or defect could cause serious damage to society, person or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below or for any other use exceeding the range or conditions set forth in this specification sheet. If you intend to use the products in the applications listed below or if you have special requirements exceeding the range or conditions set forth in this specification, please contact us.</p> <p>(1) Aerospace/Aviation equipment (2) Transportation equipment (electric trains, ships etc.) (3) Medical equipment (Excepting Pharmaceutical Affairs Law classification Class1, 2) (4) Power-generation control equipment (5) Atomic energy-related equipment (6) Seabed equipment (7) Transportation control equipment (8) Public information-processing equipment (9) Military equipment (10) Electric heating apparatus, burning equipment (11) Disaster prevention/crime prevention equipment (12) Safety equipment (13) Other applications that are not considered general-purpose applications</p> <p>When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment. In addition, although the product listed in this specification is intended for use in automotive applications as described above, it is not prohibited to use for general electronic equipment, whose performance and/or quality doesn't require a more stringent level of safety or reliability, or whose failure, malfunction or defect could not cause serious damage to society, person or property. Therefore, the description of this caution will be applied, when the product is used in general electronic equipment under a normal operation and usage conditions.</p>

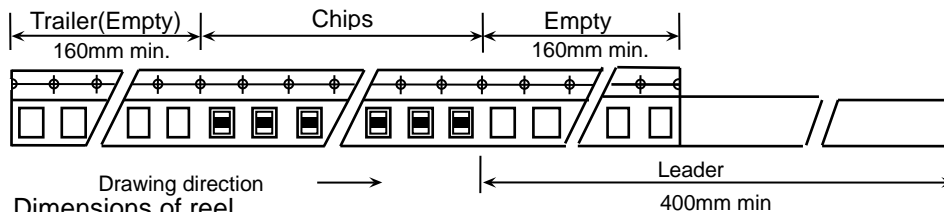
14. TAPE PACKAGING SPECIFICATION

1. CONSTRUCTION AND DIMENSION OF TAPING

1-1. Dimensions of carrier tape

Dimensions of plastic tape shall be according to Appendix 3, 4.

1-2. Empty part and leader of taping

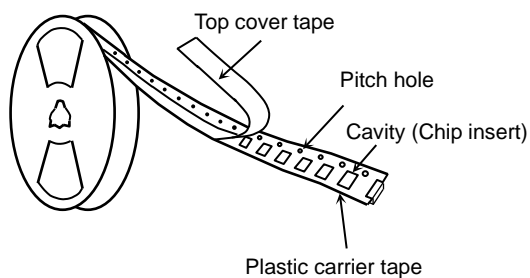


1-3. Dimensions of reel

Dimensions of $\varnothing 178$ reel shall be according to Appendix 5.

Dimensions of $\varnothing 330$ reel shall be according to Appendix 6.

1-4. Structure of taping



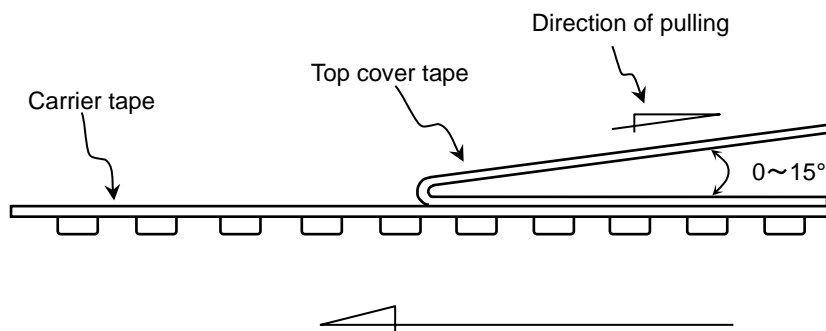
2. CHIP QUANTITY

Please refer to detail page on TDK web.

3. PERFORMANCE SPECIFICATIONS

3-1. Fixing peeling strength (top cover tape)

$$0.05\text{N} < \text{Peeling strength} < 0.7\text{N}$$



3-2. Carrier tape shall be flexible enough to be wound around a minimum radius of 30mm with components in tape.

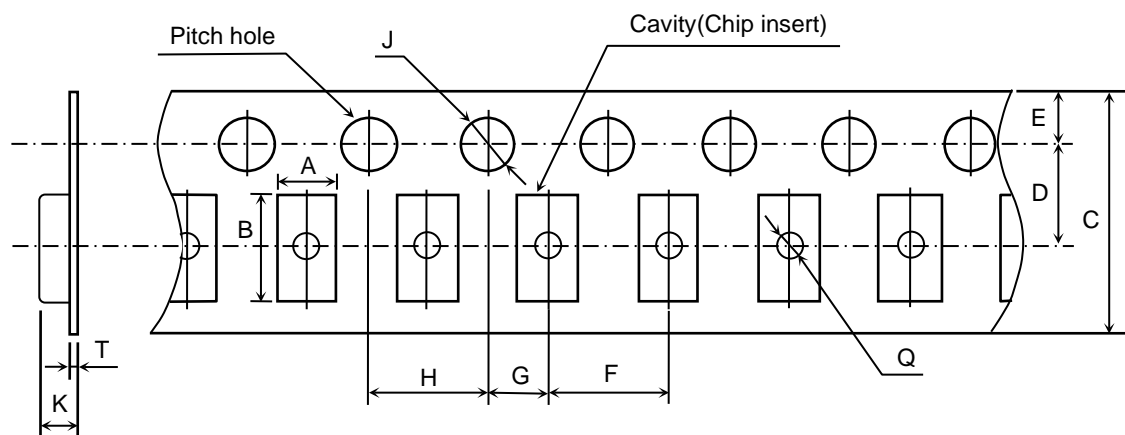
3-3. The missing of components shall be less than 0.1%

3-4. Components shall not stick to fixing tape.

3-5. When removing the cover tape, there shall not be difficulties by unfitting clearance gap, burrs and crushes of cavities. Also the sprocket holes shall not be covered by absorbing dust into the suction nozzle.

Appendix 3

Plastic Tape



(Unit : mm)

Symbol	A	B	C	D	E	F
Case size						
CGA6 (CC1210)	(2.90)	(3.60)	12.0 ± 0.30	5.50 ± 0.05	1.75 ± 0.10	4.00 ± 0.10

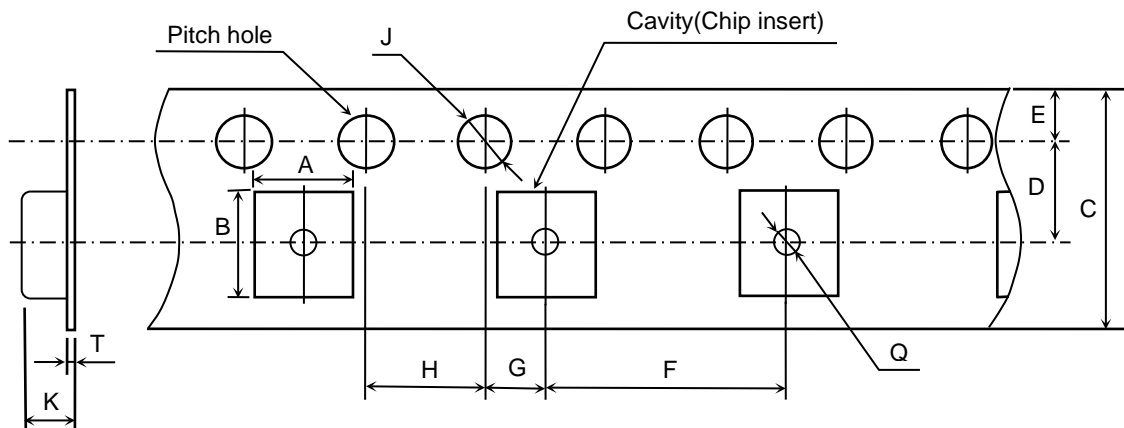
Symbol	G	H	J	K	T	Q
Case size						
CGA6 (CC1210)	2.00 ± 0.05	4.00 ± 0.10	∅1.50 ^{+0.10} ₀	3.40 max.	0.60 max.	∅ 0.50 min.

() Reference value.

Exceptionally no hole in the cavity is applied. Please inquire if hole in cavity is mandatory.

Appendix 4

Plastic Tape



(Unit : mm)

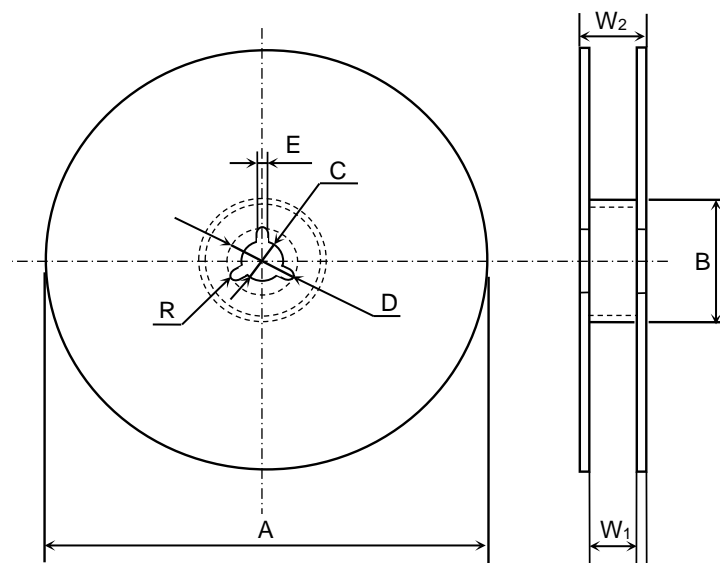
Symbol	A	B	C	D	E	F
Case size						
CGA7 (CC1808)	(2.50)	(5.10)	12.0 ± 0.30	5.50 ± 0.05	1.75 ± 0.10	8.00 ± 0.10
CGA8 (CC1812)	(3.60)	(4.90)				
Symbol	G	H	J	K	T	Q
Case size						
CGA7 (CC1808)	2.00 ± 0.05	4.00 ± 0.10	∅ 1.5 $\begin{matrix} +0.10 \\ 0 \end{matrix}$	6.50 max.	0.60 max.	∅ 1.50 min.
CGA8 (CC1812)						

() Reference value.

Exceptionally no hole in the cavity is applied. Please inquire if hole in cavity is mandatory.

Appendix 5

Dimensions of reel (Material : Polystyrene)

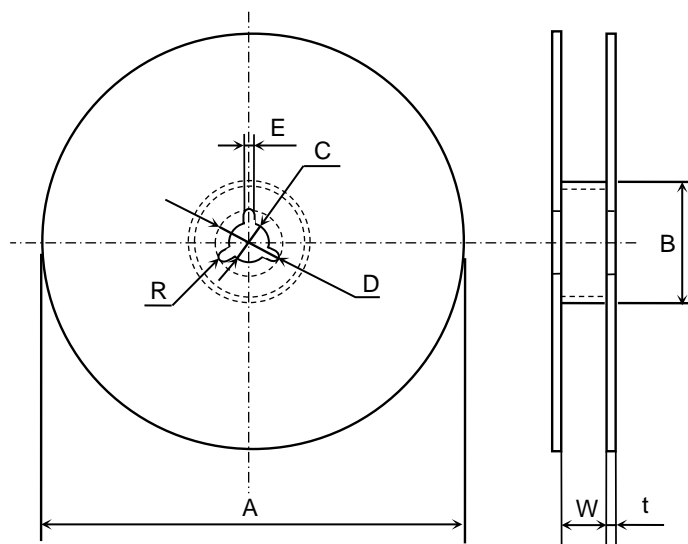


(Unit : mm)

Symbol	A	B	C	D	E	W ₁
Dimension	∅178 ± 2.0	∅60 ± 2.0	∅13 ± 0.5	∅21 ± 0.8	2.0 ± 0.5	13.0 ± 0.3
Symbol	W ₂	R				
Dimension	17.0 ± 1.4	1.0				

Appendix 6

Dimensions of reel (Material : Polystyrene)



(Unit : mm)

Symbol	A	B	C	D	E	W
Dimension	∅382 max. (Nominal ∅330)	∅50 min.	∅13 ± 0.5	∅21 ± 0.8	2.0 ± 0.5	14.0 ± 1.5
Symbol	t	R				
Dimension	2.0 ± 0.5	1.0				