EVALUATION DATA

DWG No. : CA711-53-01							
APPD	APPD	СНК	DWG				
Эжигаугто 31. Ang 99	K.T. alg 10. Aug. 99	9239 Aug, 10. 199	d shang Aug. 06.99				

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Terminology used

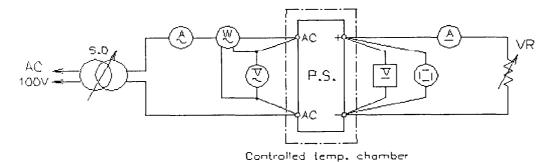
	Definition	
Vin		Input voltage
Vout		Output voltage
Iin		Input current
Iout		Output current
Ta		Ambient temperature

1. Evaluation Method

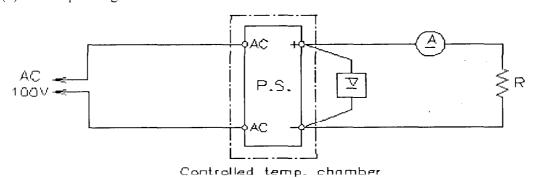
VS30C

1.1 Circuit used for determination

(1)Steady state data

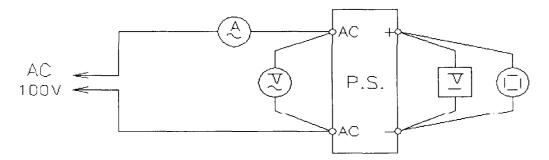


(2) Warm up voltage drift characteristics

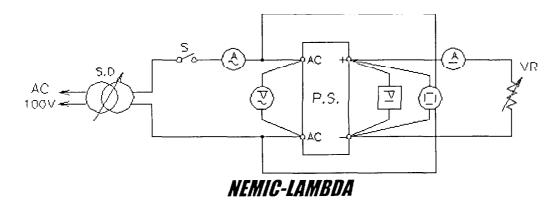


(3) Over current protection (O.C.P) characteristics Same as Steady state data.

(4) Over voltage protection (O.V.P) characteristics



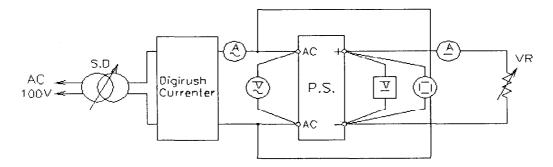
(5) Output rise characteristics



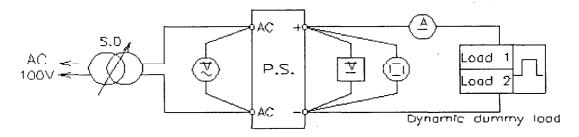
(6) Output fall characteristics

Same as output rise characteristics.

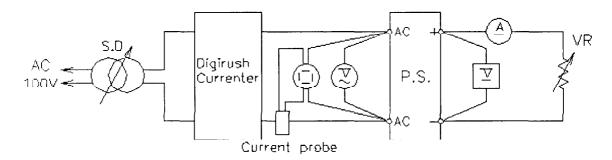
(7) Dynamic line response characteristics



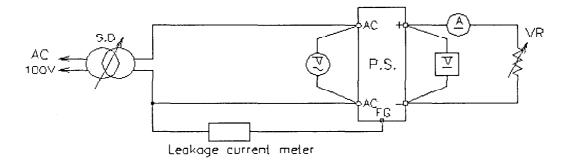
(8) Dynamic load response characteristics



(9) Inrush current characteristics

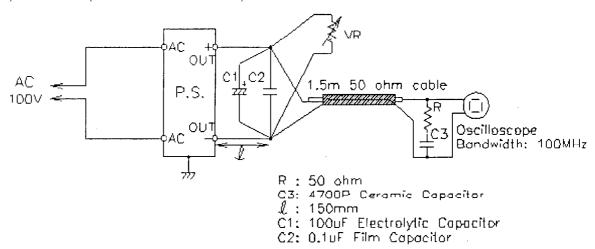


(10) Leakage current characteristics

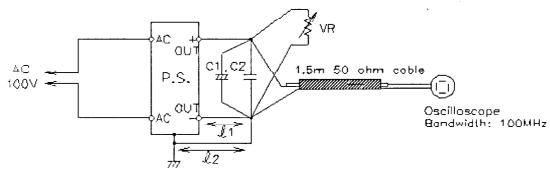


11)Output ripple and noise waveform

a)Normal Mode (EIAJ Standard RC - 9002A)



b)Normal + Common Mode

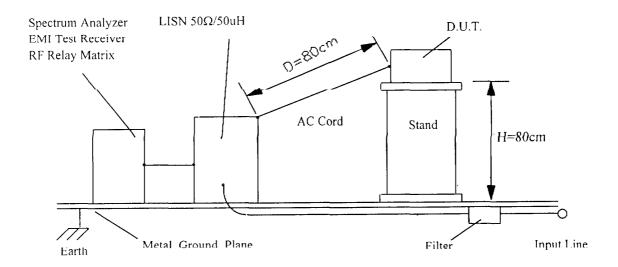


£1: 150mm £2: 282.5mm

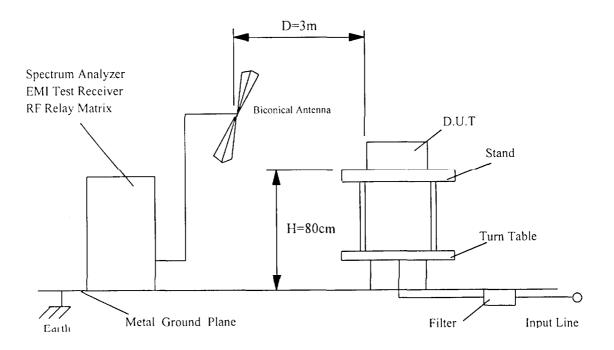
C1: 100uF Electrolytic Capacitor C2: 0.1uF Film Capacitor

12)Electro-Magnetic Interference characteristics

(a) Conducted Emission Noise



(b) Radiated Emission Noise



1.2 LIST OF EQUIPMENT USED

	EQUIPMENT USED	MANUFACTURER	MODEL NO.
1	Oscilloscope	cilloscope HITACHI	
2	Digital storage oscilloscope	TEKTRONIX	TDS-540A
3	Digital volt meter	LEADER	856
4	Digital watt/current/volt meter	НІОКІ	3186
5	DC ampere meter	YOKOGAWA	2051
6	Dynamic dummy load	KIKUSUI	PLZ152W
7	Current probe/amplifier	TEKTRONIX	A6303/AM503B
8	Controlled temperature chamber	TABAI-ESPEC	SU-240
9	Leakage current meter	SIMPSON	228
10	Digirush currenter	TAKAMISAWA CYBERNETICS	PSA-200

2. Characteristics

- 2.1 Steady state data
- (1) Regulation line and load, temperature drift

5V

1. Regulation - line and load

Condition Ta: 25°C

Į i	out\Vin	85 V	100V	132V	Line re	gulation
	0%	5.038	5.038	5.037	1 mV	0.02%
	50%	5.036	5.036	5.036	0 mV	0.00%
	100%	5.033	5.034	5.034	1 mV	0.02%
Load		5 mV	4 mV	3 mV		
	Regulation	0.10%	0.08%	0.06%		

2. Temperature drift

Conditions Vin=100Vac

Io =100%

Ta(°C)	-10°C	+25°C	+50°C	Tempera	ture drift
Vo(Vdc)	5.024	5.034	5.029	10 mV	0.20%

12V

1. Regulation - line and load

Condition Ta: 25°C

Id	out\Vin	85V	100V	132V	Line regulation	
	0%	12.056	12.055	12.051	5 mV	0.042%
	50%	12.057	12.057	12.057	0 mV	0.000%
	100%	12.054	12.055	12.055	1 mV	0.008%
Load		3 mV	2 mV	6 mV		<u> </u>
	Regulation	0.025%	0.017%	. 0.050%		

2. Temperature drift

Conditions Vin=100Vac

Io =100%

Ta(°C)	-10°C	+25°C	+50°C	Tempera	ture drift
Vo(Vdc)	12.061	12.055	12.077	22 mV	0.183%

24V

1. Regulation - line and load

Condition Ta: 25°C

Iout\Vin	85V	100V	132V	Line re	gulation
0%	24.082	24.065	24.048	34 mV	0.142%
50%	24.063	24.062	24.061	2 mV	0.008%
100%	24.056	24.055	24.054	2 mV	0.008%
Load	26 mV	10 mV	13 mV		
Regulation	0.108%	0.042%	0.054%		

2. Temperature drift

Conditions Vin=100Vac

Io = 100%

Ta(°C)	-10°C	+25°C	+50°C	Tempera	ture drift
Vo(Vdc)	24.090	24.055	24.056	35 mV	0.146%

2.1. (2) Output voltage and Ripple voltage v.s. Input voltage Conditions Iout: 100% -10°C Ta: 25°C 5V 50°C Output voltage Ripple voltage (mV) Output voltage (V) Ripple voltage 6Ü 7U Input voltage (V) 12V Output voltage Ripple voltage (mV) Output voltage (V) Ripple voltage Input voltage (V) 24V Output voltage Ripple voltage (mV) Output voltage (V) Ripple voltage

Input voltage (V)

2.1. (3) Efficiency and Input current v.s. Output current Conditions Ta: 25°C Vin: 85Vac 100Vac 5V 132Vac Input current (A) 0 100 Output current (%) 12V 80 Input current (A) 0.5 0 0 60 100 Output current (%) 24V 80 Injut current (A) 0

Output current (%)

80

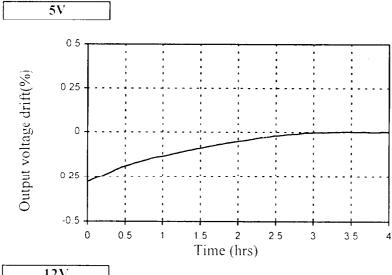
100

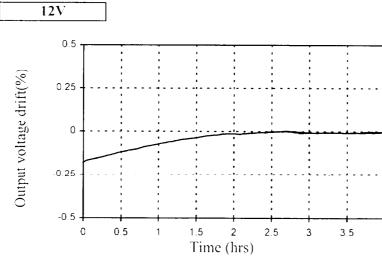
20

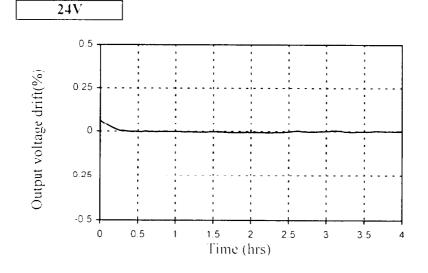
VS30C

Conditions Vin: 100VAC

lout : 100% Ta : 25°C





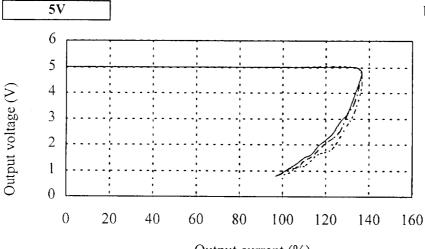


Conditions Ta: 25°C

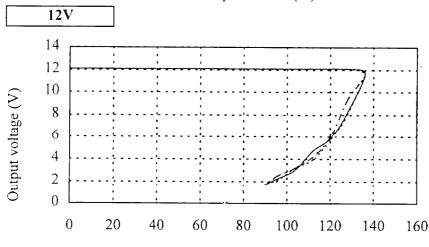
Vin: 85Vac

100Vac

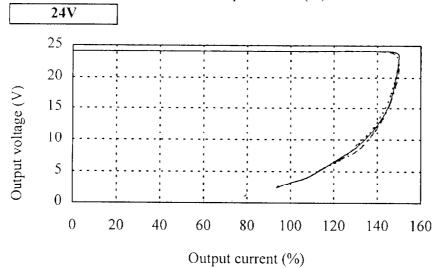




Output current (%)



Output current (%)

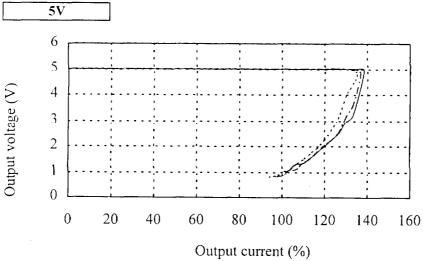


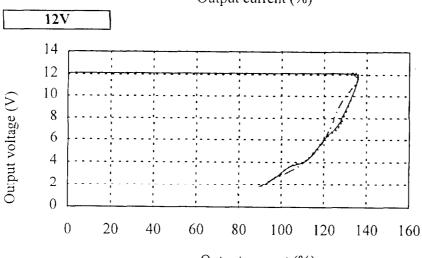
Conditions Vin: 100VAC

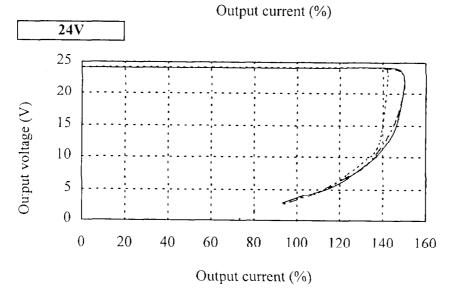
Ta:-10°C ...

25°C

50°C ____





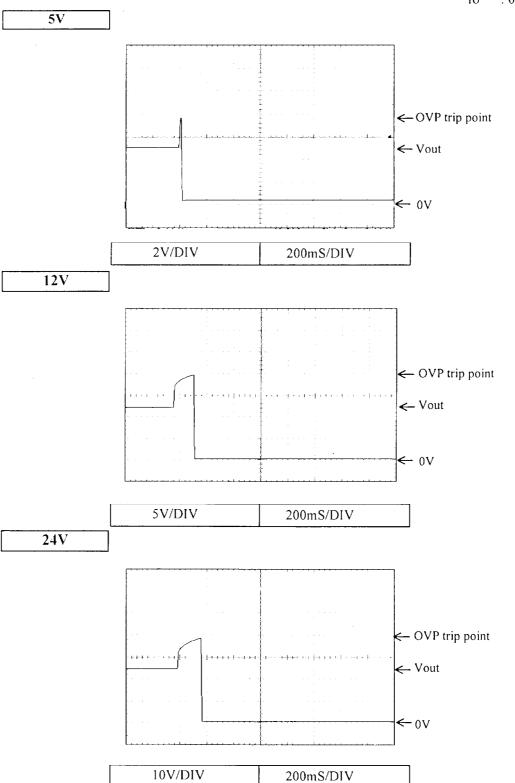


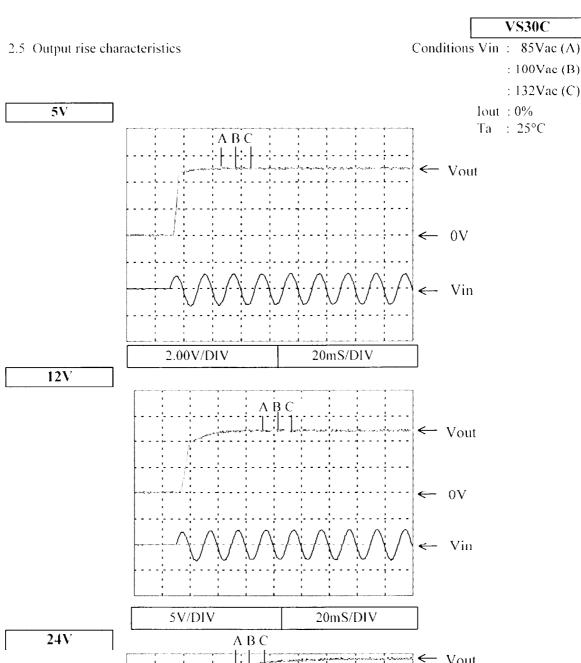
2.4 Over voltage protection (OVP) characteristics

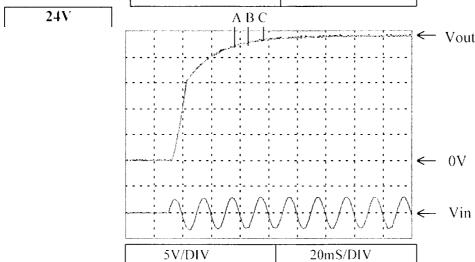
Conditions Ta: 25°C

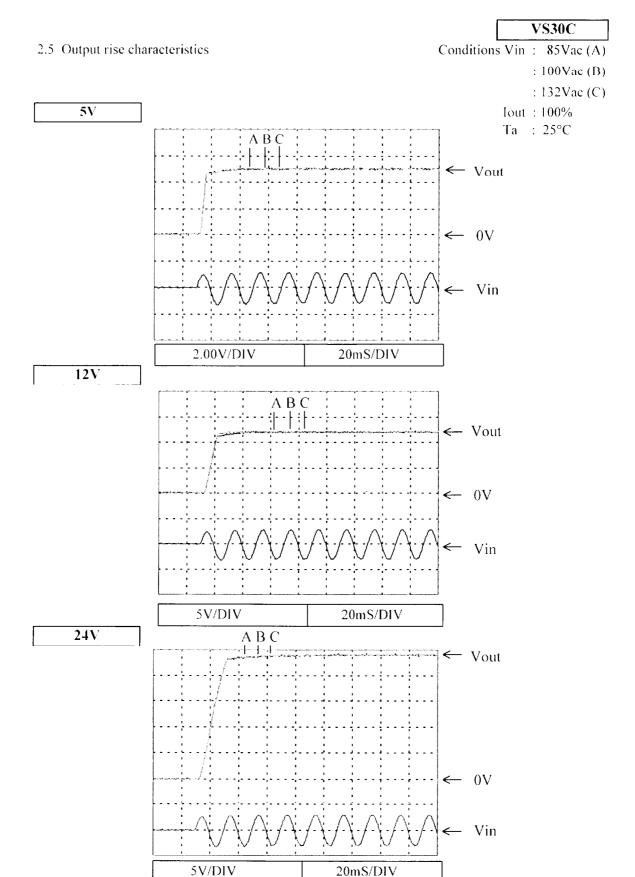
Vin: 100Vac

lo : 0%





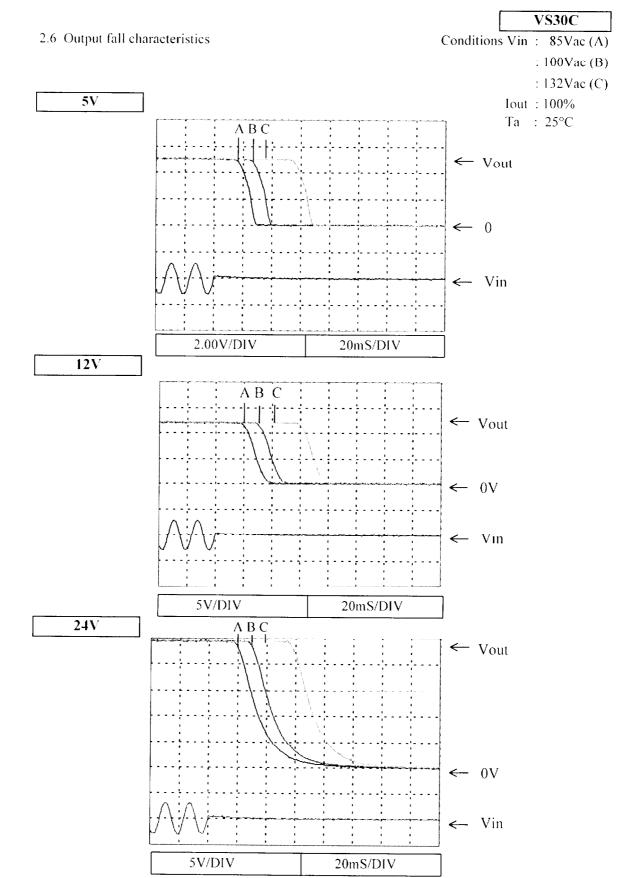




1000mS/DIV

5V/DIV

← Vin



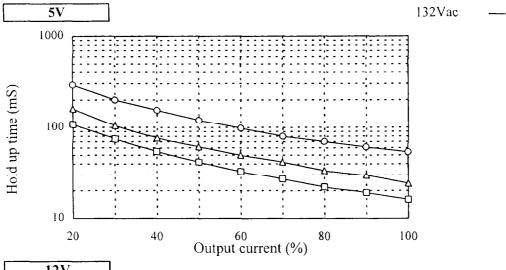


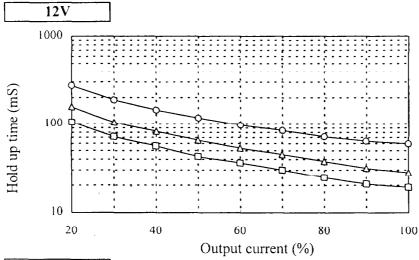
2.7 Hold up time characteristics

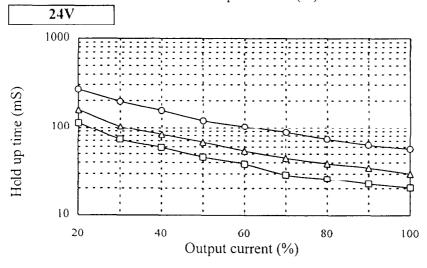
Conditions Ta: 25°C

Vin: 85Vac -

100Vac —___



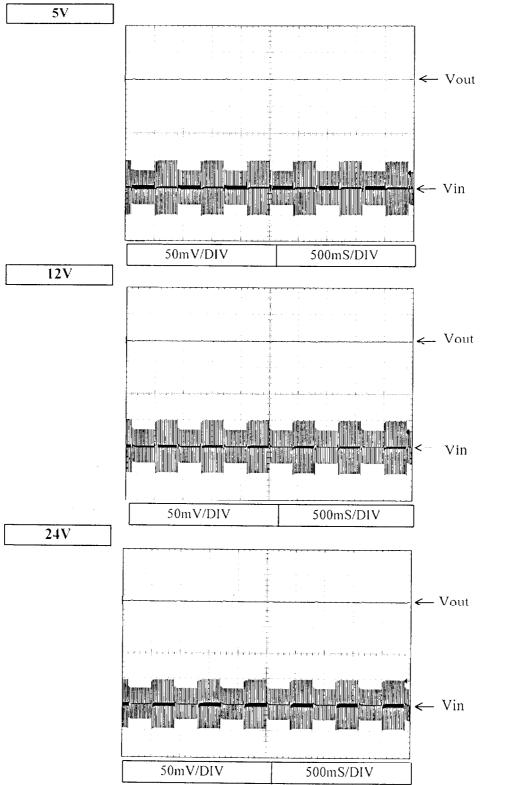




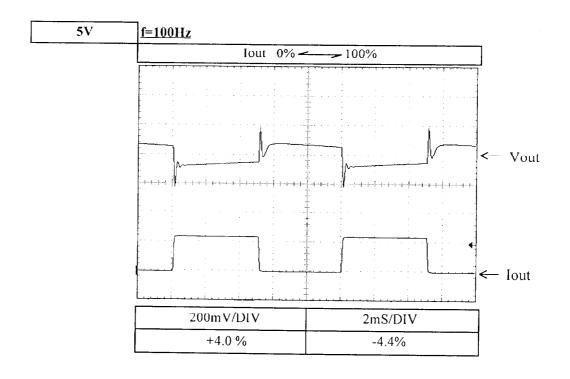
2.8 Dynamic line response characteristics

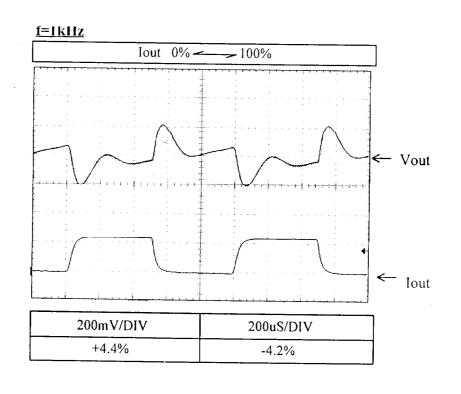
Conditions Vin: 85Vac -- 132Vac

Iout : 100% Ta : 25°C



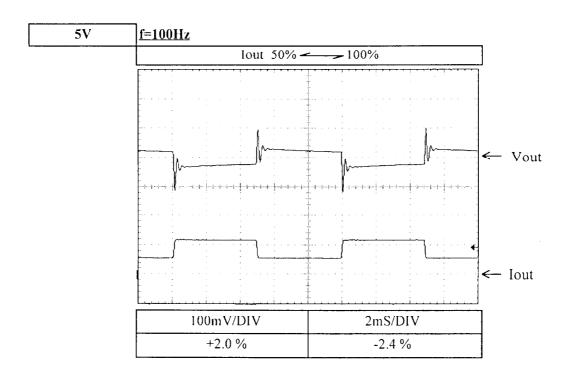
Conditions Vin: 100Vac

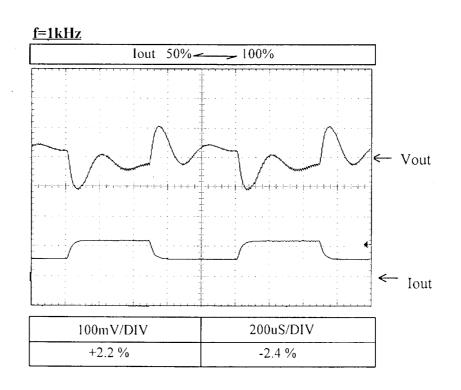




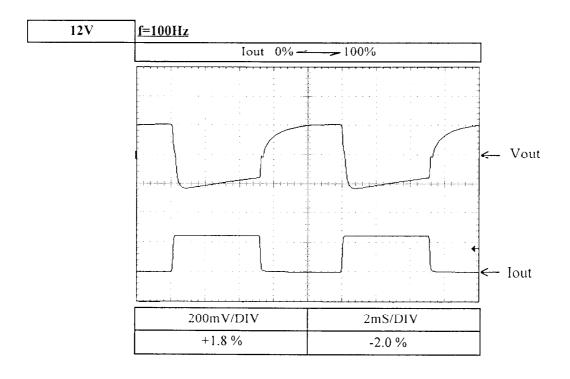
VS30C

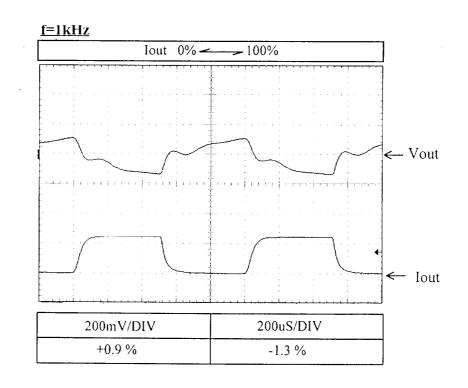
Conditions Vin: 100Vac





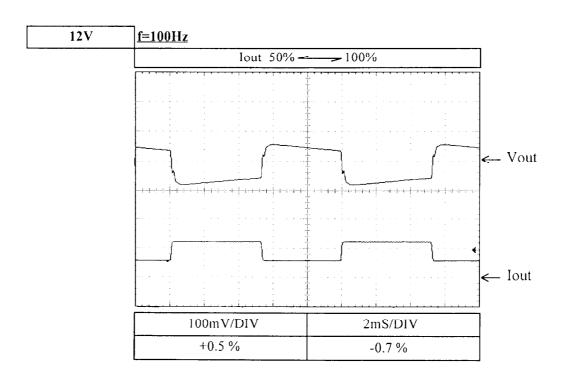
Conditions Vin: 100Vac

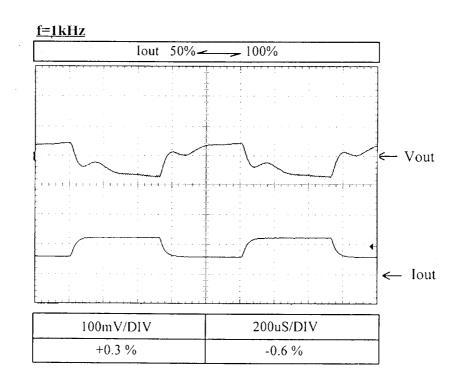




VS30C

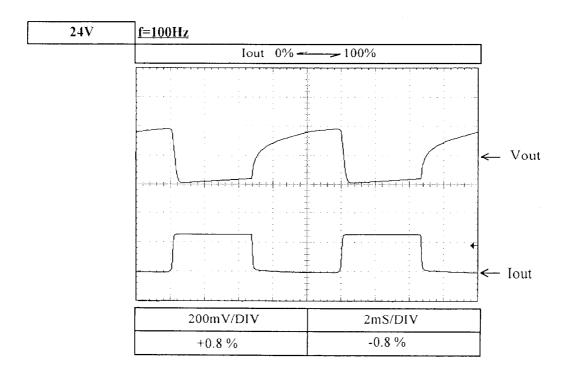
Conditions Vin: 100Vac

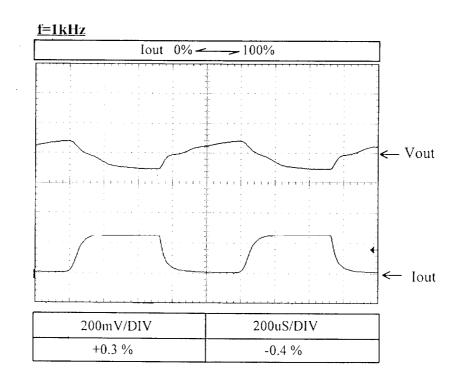




2.9 Dynamic load response characteristics

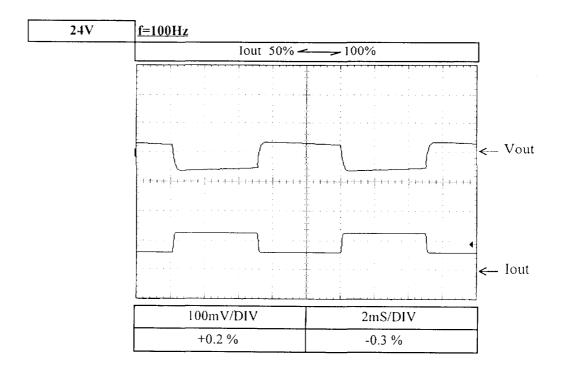
Conditions Vin: 100Vac

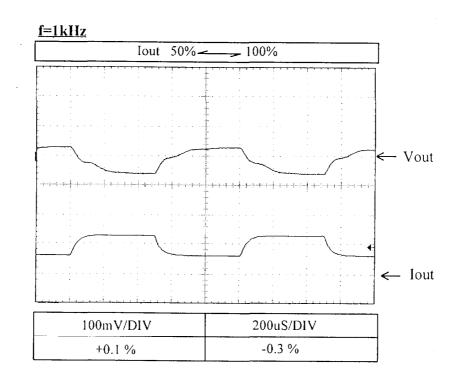




VS30C

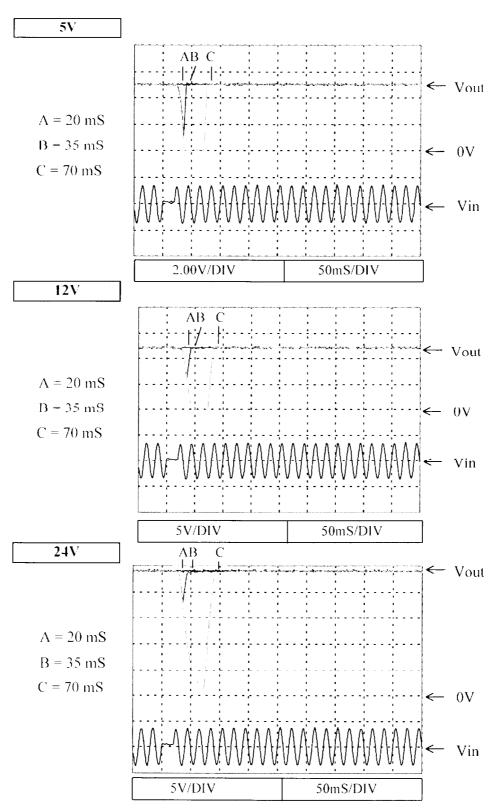
Conditions Vin: 100Vac





Conditions Vin: 100Vac

Iout : 100% Ta : 25°C



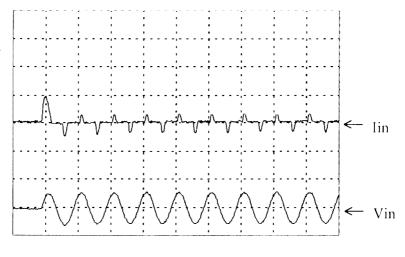
Conditions Vin: 100Vac

Iout : 100% Ta : 25°C

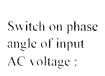


Switch on phase angle of input AC voltage:

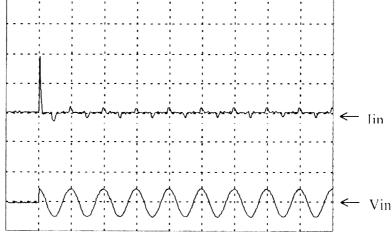












20mS/DIV

10.0A/DIV

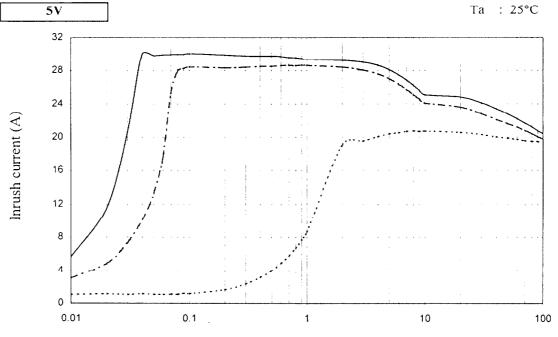
VS30C

Conditions Vin : 100Vac

Iout : 0%

50%

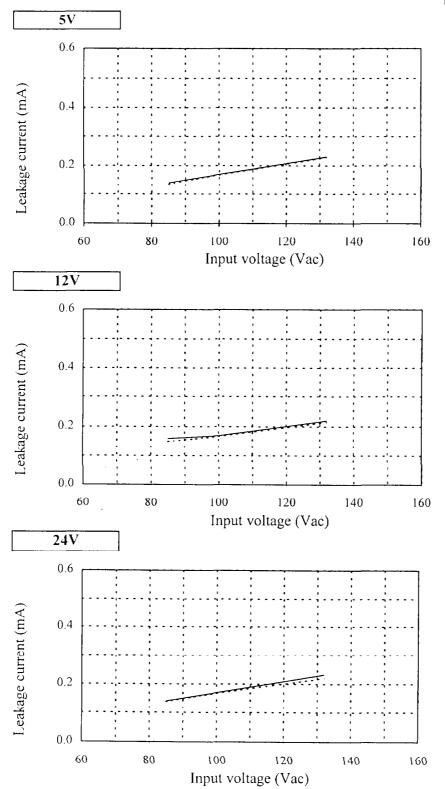
100%



Conditions Ta: 25°C

Vin: 0% -

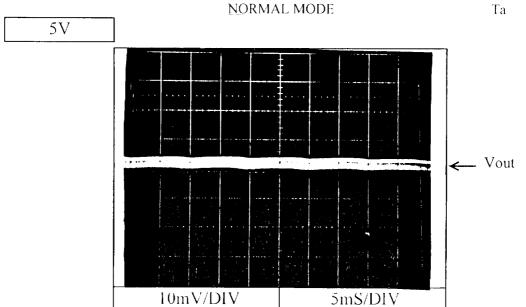
100%



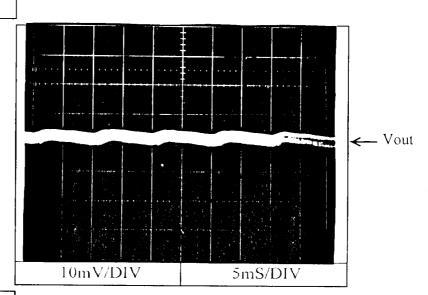
2.14 Output ripple and noise waveform

Conditions Vin: 100VAC

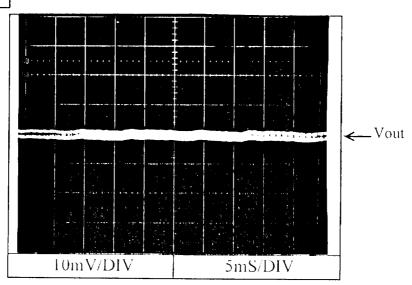
Iout : 100% Ta : 25°C



12V



24V



NEMIC-LAMBDA

2.14 Output ripple and noise waveform

Conditions Vin: 100VAC

Iout : 100% Ta : 25°C

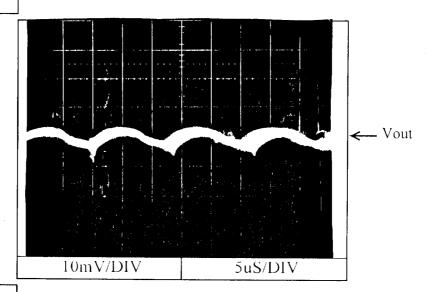
NORMAL MODE

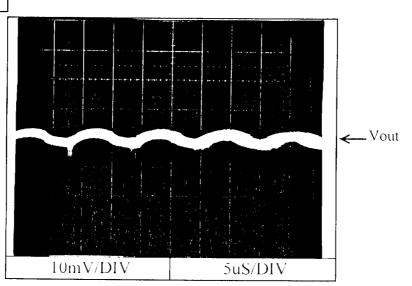
Vout

10mV/DIV

5uS/DIV

12V





NEMIC-LAMBDA

2.14 Output ripple and noise waveform

Conditions Vin: 100VAC

Iout : 100%

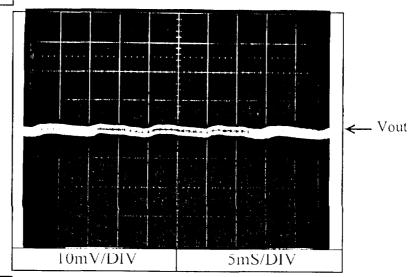
NORMAL + COMMON MODE Ta : 25°C

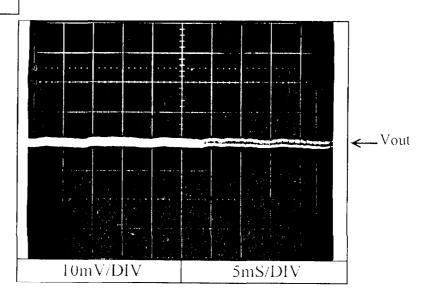
Vout

10mV/DIV

5mS/DIV

12V





2.14 Output ripple and noise waveform

Conditions Vin: 100VAC

Iout : 100%

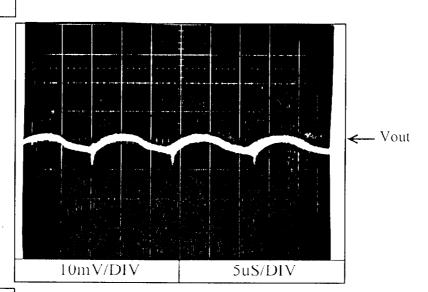
NORMAL + COMMON MODE Ta : 25°C

Vout

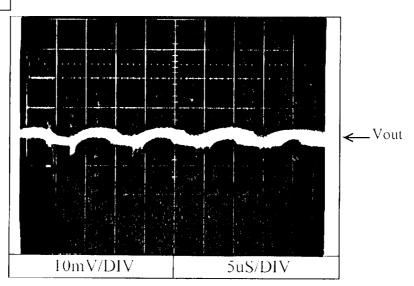
10mV/DIV

5uS/DIV

12V



24V



NEMIC-LAMBDA

2.15 Electro Magnetic Interference characteristics

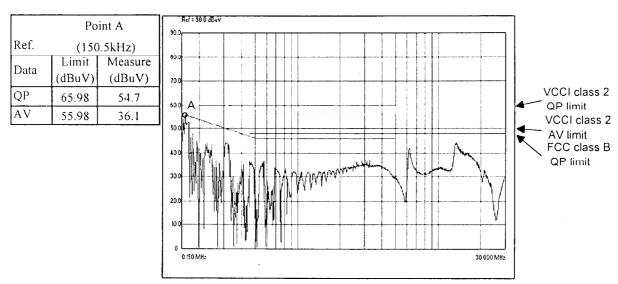
Conditions Vin: 100Vac

Iout: 100%

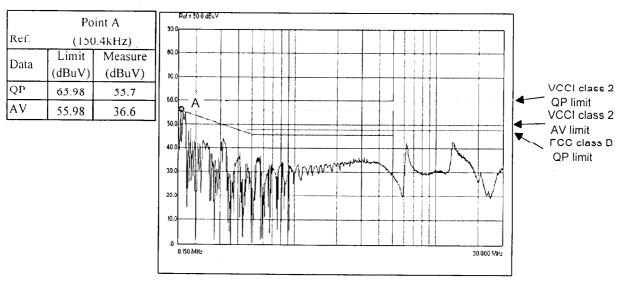
Ta : 25°C

Conducted Emission

5V



Phase: L



Phase: N

2.15 Electro Magnetic Interference characteristics

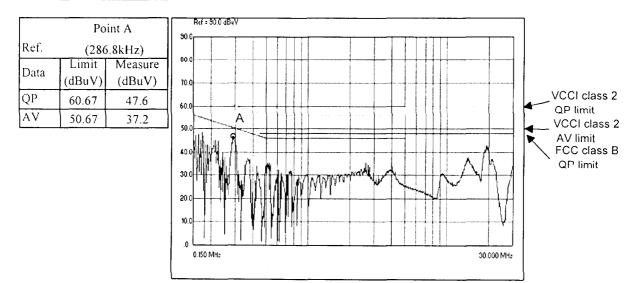
VS30C Conditions Vin: 100Vac

Iout: 100%

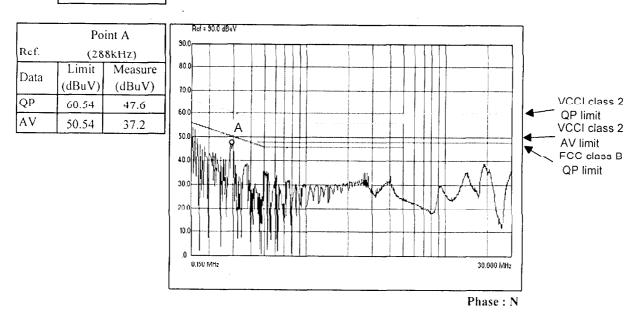
Ta : 25°C

Conducted Emission

12V



Phase: L



2.15 Electro Magnetic Interference characteristics

Conducted Emission

Conditions Vin: 100Vac

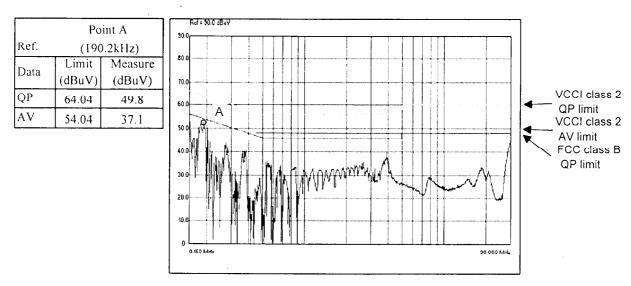
lout: 100%

Ta : 25°C

24V

	Poi	nt A	Rd = 900 dBuV	7
Ref.	(170	.5kHz)		
Data	Limit (dBuV)	Measure (dBuV)	70.0	
QP	65.00	42.9	600	VCCI class
AV	55.00	28.5	500 8	QP limit VCCI class
			40 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	AV limit FCC class QP limit
			0.150 MHz 30.000 MHz	

Phase: L



Phase: N

2.15 Electro Magnetic Interference characteristics

Radiated Emission Noise

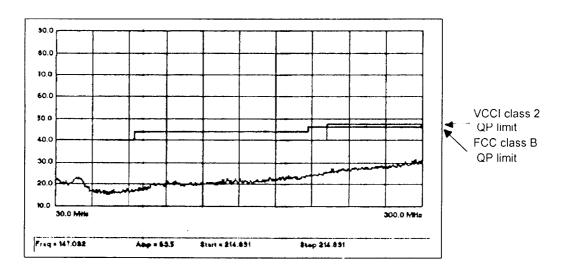
Conditions Vin: 100Vac

lout: 100%

Ta : 25°C

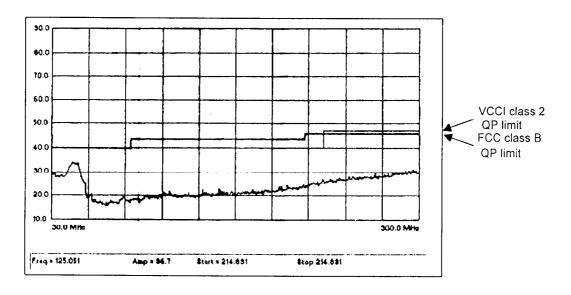
HORIZONTAL:

5V



5V

VERTICAL:



2.15 Electro Magnetic Interference characteristics

Conditions Vin: 100Vac

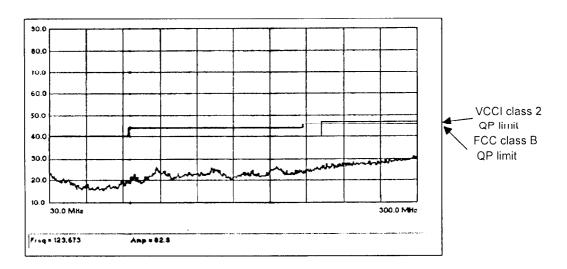
Iout: 100%

Ta : 25°C

Radiated Emission Noise

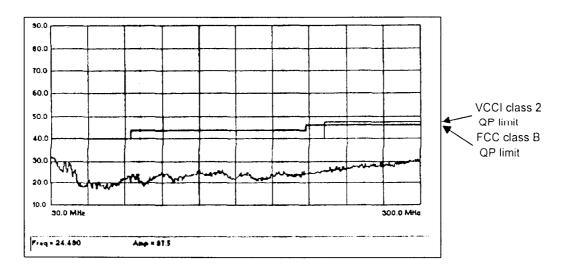
12V

HORIZONTAL:



12V

VERTICAL:



2.15 Electro Magnetic Interference characteristics

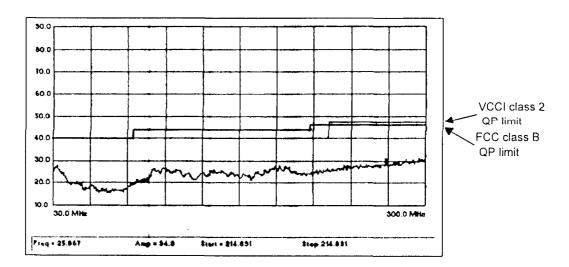
Conditions Vin: 100Vac

Iout : 100% Ta : 25°C

Radiated Emission Noise

24V

HORIZONTAL:



24V

VERTICAL:

