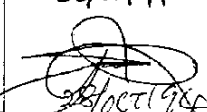



# LWT15H-\*

## RELIABILITY DATA

DWG. NO : PA785-79-01				
APPD.(NLJ QA)	APPD.(NLM QA)	APPROVED	CHECKED	ENGR.
<i>J. Murayama</i> 26/JAN./95	<i>M.K.HO</i> 28/10/94 	<i>CCNGO</i> 27/10/94	<i>Y. Adli</i> 27/10/94	 26-OCT-1994

 **NEMIC-LAMBDA**

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The above data is typical value. As all units have nearly the same characteristics, the data to be considered as ability value.

## M. T. B. F.

MODEL : LWT15H -5FF

### 1. Method of calculation

This calculation is by the components count method laid down by the DC Stabilized Power Supplies (Switching Mode) Committee of EIAJ.

The MTBF is determined by means of a fixed component failure rate  $\lambda_G$  given to each component and the number of component count, of each type of component.  $\lambda_G$  is determined based on MIL-HDBK-217F.

Formula :

$$\begin{aligned} \text{MTBF} &= \frac{1}{\lambda_{\text{equip}}} \\ &= \frac{1}{\sum_{i=1}^n N_i (\lambda_G \pi_Q)_i} \times 10^6 \quad (\text{HOURS}) \end{aligned}$$

where :

$\lambda_{\text{equip}}$  = Total Equipment Failure Rate ( Failures /  $10^6$  Hours )

$\lambda_G$  = Generic Failure Rate For The  $i$ th Generic Part ( Failure /  $10^6$  Hours )

$N_i$  = Quantity of  $i$ th Generic Part

$n$  = Number of Different Generic Part Categories

$\pi_Q$  = Generic Quality Factor for the  $i$ th Generic Part (  $\pi_Q = 1$  )

### 2. MTBF Values

1.  $G_F$  : ( GROUND, FIXED)

$$\text{MTBF} = 457770 \quad (\text{HOURS})$$

## COMPONENTS DERATING

( At Nominal Line and Rated Load , Ambient Temperature 40°C )

### Calculation Method

#### A . Semiconductors

The derating factor is taken as the ratio of the actual operating junction temperature taking into consideration operating ambient temperature , power loss and thermal resistance to the maximum rated junction temperature specifications of the components .

#### B . IC , Resistors , Capacitors etc .

Operating ambient temperature , operating condition , power loss for each individual component are all designed to meet the requirements of Nemic-Lambda's design standard .

#### C . Thermal Resistance Calculation

$$\theta_{j-c} = \frac{T_{j(max)} - T_c}{P_{c(max)}} \quad , \quad \theta_{j-a} = \frac{T_{j(max)} - T_a}{P_{c(max)}}$$

$T_c$  : Case Temperature ( Normally 25°C )

$T_a$  : Ambient Temperature ( Normally 25°C )

$P_{c(max)}$  : Maximum Power Loss

$T_{j(max)}$  : Maximum Junction Temperature

$\theta_{j-c}$  : Junction To Case Thermal Resistance

$\theta_{j-a}$  : Junction To Ambient Thermal Resistance

**SEMICONDUCTOR DERATING**

DATE : 28-FEB-94

MODEL : LWT15H-5FF

VIN = AC 100V      LOAD = 100%      Ta = 40°C

Q1 2SK1943 FUJI	Tchmax = 150 °C	$\Theta_{ch-c} = 1.56$ °C/W	Pdmax = 80.0 W
	Pd = 0.99 W	$\Delta T_c = 36.4$ °C	Tc = 76.4 °C
	Tch = Tc + ( $\Theta_{ch-c}$ ) * Pd = 77.9 °C		
	D.F. = 52.0 %		
Q2 2SC3425 TOSHIBA	Tjmax = 150 °C	$\Theta_{j-c} = 12.5$ °C/W	Pdmax = 10 W
	Pd = 0.1 W	$\Delta T_c = 33.0$ °C	Tc = 73.0 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd = 74.3 °C		
	D.F. = 49.5 %		
A2 TA7815S TOSHIBA	Tjmax = 150 °C	$\Theta_{j-c} = 5$ °C/W	Pdmax = 15 W
	Pd = 2.4 W	$\Delta T_c = 32.6$ °C	Tc = 72.6 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd = 84.6 °C		
	D.F. = 56.4 %		
A3 TA7815S TOSHIBA	Tjmax = 150 °C	$\Theta_{j-c} = 5$ °C/W	Pdmax = 15 W
	Pd = 1.6 W	$\Delta T_c = 55.6$ °C	Tc = 95.6 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd = 103.6 °C		
	D.F. = 69.1 %		
A4 AN1431T MATSUSHITA	Tjmax = 150 °C	$\Theta_{j-c} = 156$ °C/W	Pdmax = 0.55 W
	Pd = 0.006 W	$\Delta T_c = 33.0$ °C	Tc = 73.0 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd = 73.9 °C		
	D.F. = 49.3 %		
A50 UC2842ADW UNITRODE	Tjmax = 150 °C	$\Theta_{j-c} = 70$ °C/W	Pdmax = 0.725 W
	Pd = 0.4 W	$\Delta T_c = 40.9$ °C	Tc = 80.9 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd = 108.9 °C		
	D.F. = 72.6 %		
D1 D2SB60 SHINDENGEN	Tjmax = 150 °C	$\Theta_{j-l} = 10$ °C/W	Pdmax = 12.5 W
	Pd = 0.35 W	$\Delta T_l = 30.0$ °C	T(lead) = 70.0 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 73.5 °C		
	D.F. = 49.0 %		

## SEMICONDUCTOR DERATING

DATE : 28-FEB-94

MODEL : , LWT15H-5FF

VIN = AC 100V

LOAD - 100%

Ta = 40°C

D2 1SS193 TOSHIBA	Tjmax = 125 °C	$\Theta_{j-l} = 666$ °C/W	Pdmax = 0.15 W
	Pd = 0 W	$\Delta T_l = 27.4$ °C	T(lead) = 67.4 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 67.4 °C		
	D.F. = 53.9 %		
D3 1NU41 TOSHIBA	Tjmax = 150 °C	$\Theta_{j-l} = 34$ °C/W	Pdmax = 3.7 W
	Pd = 0.03 W	$\Delta T_l = 25.6$ °C	T(lead) = 65.6 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 66.6 °C		
	D.F. = 44.4 %		
D4 EGP10G G.I.	Tjmax = 150 °C	$\Theta_{j-l} = 50$ °C/W	Pdmax = 2.5 W
	Pd = 0.005 W	$\Delta T_l = 29.8$ °C	T(lead) = 69.8 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 70.1 °C		
	D.F. = 46.7 %		
D7 D10SC6M SHINDENGEN	Tjmax = 125 °C	$\Theta_{j-l} = 3.3$ °C/W	Pdmax = 5.8 W
	Pd = 0.25 W	$\Delta T_l = 36.6$ °C	T(lead) = 76.6 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 77.4 °C		
	D.F. = 61.9 %		
D9 5FL2CZ41A TOSHIDA	Tjmax = 150 °C	$\Theta_{j-l} = 3.8$ °C/W	Pdmax = 33 W
	Pd = 0.26 W	$\Delta T_l = 38.2$ °C	T(lead) = 78.2 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 79.2 °C		
	D.F. = 52.8 %		
D10 V19E HITACHI	Tjmax = 150 °C	$\Theta_{j-l} = 50$ °C/W	Pdmax = 12 W
	Pd = 0 W	$\Delta T_l = 32.8$ °C	T(lead) = 72.8 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 72.8 °C		
	D.F. = 48.5 %		
D11 S2L40 SHINDENGEN	Tjmax = 150 °C	$\Theta_{j-l} = 12$ °C/W	Pdmax = 12.5 W
	Pd = 0.36 W	$\Delta T_l = 36.0$ °C	T(lead) = 76.0 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 80.3 °C		
	D.F. = 53.5 %		

**SEMICONDUCTOR DERATING**

DATE : 28-FEB-94

MODEL : LWT15H-5FF

VIN = AC 100V      LOAD = 100%      Ta = 40°C

D13 V19E HITACHI	Tjmax = 150 °C	$\Theta_{j-l} = 50$ °C/W	Pdmax = 12 W
	Pd = 0 W	$\Delta T_l = 34.2$ °C	T(lead) = 74.2 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 74.2 °C		
	D.F. = 49.5 %		
7D1 1N4746A MOTOROLA	Tjmax = 200 °C	$\Theta_{j-l} = 175$ °C/W	Pdmax = 1 W
	Pd = 0 W	$\Delta T_l = 30.8$ °C	T(lead) = 70.8 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 70.8 °C		
	D.F. = 35.4 %		
ZD2 1N4746A MOTOROLA	Tjmax = 200 °C	$\Theta_{j-l} = 175$ °C/W	Pdmax = 1 W
	Pd = 0 W	$\Delta T_l = 29.1$ °C	T(lead) = 69.1 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 69.1 °C		
	D.F. = 34.6 %		
ZD3 1N4735A MOTOROLA	Tjmax = 200 °C	$\Theta_{j-l} = 175$ °C/W	Pdmax = 1 W
	Pd = 0 W	$\Delta T_l = 29.5$ °C	T(lead) = 69.5 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 69.5 °C		
	D.F. = 34.8 %		
ZD4 HZS5.1NB2-TA HITACHI	Tjmax = 150 °C	$\Theta_{j-l} = 438$ °C/W	Pdmax = 0.4 W
	Pd = 0 W	$\Delta T_l = 35.0$ °C	T(lead) = 75.0 °C
	Tj = Tl + ( $\Theta_{j-l}$ ) * Pd = 75.0 °C		
	D.F. = 50.0 %		
PC1 (SCR) TLP647G TOSHIBA	Tjmax = 100 °C	$\Theta_{j-c} = 500$ °C/W	Pdmax = 0.15 W
	Pd = 0 W	$\Delta T_c = 30.4$ °C	Tc = 70.4 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd = 70.4 °C		
	D.F. = 70.4 %		
PC1 (LED) TLP647G TOSHIBA	Tjmax = 125 °C	$\Theta_{j-c} = -$ °C/W	Pdmax = - W
	If = 0 mA	$\Delta T_c = 30.4$ °C	Tc = 70.4 °C
	If (max) = 38 mA (at Ta = 70.4 °C)		
	D.F. = 0 %		

SEMICONDUCTOR DERATING

DATE : 28-FEB-94

MODEL : LWT15H-5FF

VIN = AC 100V      LOAD = 100%      Ta = 40°C

PC2 (TRANSISTOR) TLP732 TOSHIBA	Tjmax = 125 °C	$\Theta_{j-c} = 667$ °C/W	Pdmax = 0.15 W
	Pd = 0.004 W	$\Delta T_c = 29.9$ °C	Tc = 69.9 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd = 72.6 °C		
	D.F. = 58.1 %		
PC2 (LED) TLP732 TOSHIBA	Tjmax = 125 °C	$\Theta_{j-c} = -$ °C/W	Pdmax = - W
	If = 1.4 mA	$\Delta T_c = 29.9$ °C	Tc = 69.9 °C
	If (max) = 38 mA (at Ta = 69.9 °C)		
	D.F. = 3.7 %		



## SEMICONDUCTOR DERATING

DATE : 28-FEB-94

MODEL : LWT15H-5FF

VIN = AC 200V		LOAD = 100%		Ta = 40°C		
Q1 2SK1943 FUJI	Tchmax = 150 °C		Θch-c = 1.56 °C/W		Pdmax = 80.0 W	
	Pd = 0.91 W		ΔTc = 45.3 °C		Tc = 85.3 °C	
	Tch = Tc + (Θch-c)*Pd =		86.7 °C			
	D.F. = 57.8 %					
Q2 2SC3425 TOSHIBA	Tjmax = 150 °C		Θj-c = 12.5 °C/W		Pdmax = 10 W	
	Pd = 0.1 W		ΔTc = 34.1 °C		Tc = 74.1 °C	
	Tj = Tc + (Θj-c)*Pd =		75.4 °C			
	D.F. = 50.2 %					
A2 TA7815S TOSHIBA	Tjmax = 150 °C		Θj-c = 5 °C/W		Pdmax = 15 W	
	Pd = 2.4 W		ΔTc = 34.3 °C		Tc = 74.3 °C	
	Tj = Tc + (Θj-c)*Pd =		86.3 °C			
	D.F. = 57.5 %					
A3 TA7815S TOSHIBA	Tjmax = 150 °C		Θj-c = 5 °C/W		Pdmax = 15 W	
	Pd = 1.6 W		ΔTc = 57.0 °C		Tc = 97.0 °C	
	Tj = Tc + (Θj-c)*Pd =		105 °C			
	D.F. = 70.0 %					
A4 AN1431T MATSUSHITA	Tjmax = 150 °C		Θj-c = 156 °C/W		Pdmax = 0.55 W	
	Pd = 0.006 W		ΔTc = 36.7 °C		Tc = 76.7 °C	
	Tj = Tc + (Θj-c)*Pd =		77.6 °C			
	D.F. = 51.8 %					
A50 UC2842ADW UNITRODE	Tjmax = 150 °C		Θj-c = 70 °C/W		Pdmax = 0.725 W	
	Pd = 0.4 W		ΔTc = 42.7 °C		Tc = 82.7 °C	
	Tj = Tc + (Θj-c)*Pd =		110.7 °C			
	D.F. = 73.8 %					
D1 D2SB60 SHINDENGEN	Tjmax = 150 °C		Θj-l = 10 °C/W		Pdmax = 12.5 W	
	Pd = 0.21 W		ΔTl = 26.1 °C		T(lead) = 66.1 °C	
	Tj = Tl + (Θj-l)*Pd =		68.2 °C			
	D.F. = 45.5 %					

**SEMICONDUCTOR DERATING**

DATE : 28-FEB-94

MODEL : .LWT15H-5FF

		VIN = AC 200V	LOAD = 100%	Ta = 40°C
D2 1SS193 TOSHIBA	Tjmax =	125 °C	$\Theta_{j-l} =$	666 °C/W Pdmax = 0.15 W
	Pd =	0 W	$\Delta T_l =$	29.0 °C T(lead) = 69.0 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd =	69.0 °C		
	D.F. =	55.2 %		
D3 1NU41 TOSHIBA	Tjmax =	150 °C	$\Theta_{j-l} =$	34 °C/W Pdmax = 3.7 W
	Pd =	0.02 W	$\Delta T_l =$	25.8 °C T(lead) = 65.8 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd =	66.5 °C		
	D.F. =	44.3 %		
D4 EGP10G G.I.	Tjmax =	150 °C	$\Theta_{j-l} =$	50 °C/W Pdmax = 2.5 W
	Pd =	0.004 W	$\Delta T_l =$	32.4 °C T(lead) = 72.4 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd =	72.6 °C		
	D.F. =	48.4 %		
D7 D10SC6M SHINDENGEN	Tjmax =	125 °C	$\Theta_{j-l} =$	3.3 °C/W Pdmax = 5.8 W
	Pd =	0.25 W	$\Delta T_l =$	38.6 °C T(lead) = 78.6 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd =	79.4 °C		
	D.F. =	63.5 %		
D9 5FL2CZ41A TOSHIBA	Tjmax =	150 °C	$\Theta_{j-l} =$	3.8 °C/W Pdmax = 33 W
	Pd =	0.26 W	$\Delta T_l =$	44.8 °C T(lead) = 84.8 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd =	85.8 °C		
	D.F. =	57.2 %		
D10 V19E HITACHI	Tjmax =	150 °C	$\Theta_{j-l} =$	50 °C/W Pdmax = 12 W
	Pd =	0 W	$\Delta T_l =$	35.4 °C T(lead) = 75.4 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd =	75.4 °C		
	D.F. =	50.3 %		
D11 S2L40 SHINDENGEN	Tjmax =	150 °C	$\Theta_{j-l} =$	12 °C/W Pdmax = 12.5 W
	Pd =	0.24 W	$\Delta T_l =$	39.4 °C T(lead) = 79.4 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd =	82.3 °C		
	D.F. =	54.9 %		

SEMICONDUCTOR DERATING

DATE : 28-FEB-94

MODEL : LWT15H-5FF

VIN = AC 200V      LOAD = 100%      Ta = 40°C

D13 V19E HITACHI	Tjmax = 150 °C	$\Theta_{j-l} = 50$ °C/W	Pdmax = 12 W
	Pd = 0 W	$\Delta T_l = 38.0$ °C	T(lead) = 78.0 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd = 78.0 °C		
	D.F. = 52.0 %		
ZD1 1N4746A MOTOROLA	Tjmax = 200 °C	$\Theta_{j-l} = 175$ °C/W	Pdmax = 1 W
	Pd = 0 W	$\Delta T_l = 32.8$ °C	T(lead) = 72.8 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd = 72.8 °C		
	D.F. = 36.4 %		
ZD2 1N4746A MOTOROLA	Tjmax = 200 °C	$\Theta_{j-l} = 175$ °C/W	Pdmax = 1 W
	Pd = 0 W	$\Delta T_l = 33.2$ °C	T(lead) = 73.2 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd = 73.2 °C		
	D.F. = 36.6 %		
ZD3 1N4735A MOTOROLA	Tjmax = 200 °C	$\Theta_{j-l} = 175$ °C/W	Pdmax = 1 W
	Pd = 0 W	$\Delta T_l = 32.9$ °C	T(lead) = 72.9 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd = 72.9 °C		
	D.F. = 36.5 %		
ZD4 11Z35.1NB2-TA HITACHI	Tjmax = 150 °C	$\Theta_{j-l} = 438$ °C/W	Pdmax = 0.4 W
	Pd = 0 W	$\Delta T_l = 39.0$ °C	T(lead) = 79.0 °C
	Tj = Tl + ( $\Theta_{j-l}$ )*Pd = 79.0 °C		
	D.F. = 52.7 %		
PC1 (SCR) TLP647G TOSHIBA	Tjmax = 100 °C	$\Theta_{j-c} = 500$ °C/W	Pdmax = 0.15 W
	Pd = 0 W	$\Delta T_c = 32.8$ °C	Tc = 72.8 °C
	Tj = Tc + ( $\Theta_{j-c}$ )*Pd = 72.8 °C		
	D.F. = 72.8 %		
PC1 (LED) TLP647G TOSHIBA	Tjmax = 125 °C	$\Theta_{j-c} = -$ °C/W	Pdmax = - W
	If = 0 mA	$\Delta T_c = 32.8$ °C	Tc = 72.8 °C
	If (max) = 36 mA (at Ta = 72.8 °C)		
	D.F. = 0 %		

SEMICONDUCTOR DERATING

DATE : 28-FEB-94

MODEL : LWT15H-5FF

VIN = AC 200V      LOAD = 100%      Ta = 40°C

PC2 (TRANSISTOR) TLP732 TOSHIBA	Tjmax = 125 °C	$\Theta_{j-c} = 667$ °C/W	Pdmax = 0.15 W
	Pd = 0.004 W	$\Delta T_c = 32.2$ °C	Tc = 72.2 °C
	Tj = Tc + ( $\Theta_{j-c}$ ) * Pd =		74.9 °C
	D.F. = 59.9 %		
PC2 (LED) TLP732 TOSHIBA	Tjmax = 125 °C	$\Theta_{j-c} = -$ °C/W	Pdmax = - W
	If = 1.4 mA	$\Delta T_c = 32.2$ °C	Tc = 72.2 °C
	If (max) = 37 mA (at Ta = 72.2 °C)		
	D.F. = 3.8 %		

# LWT15H-5FF


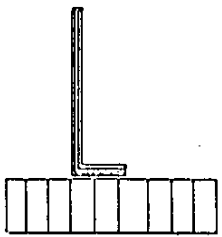
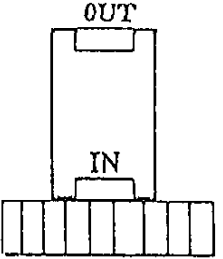
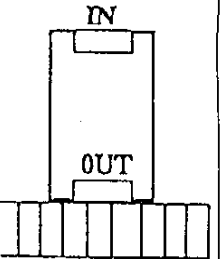
## ΔT TEMPERATURE RISE

MODEL : LWT15H-5FF

Ta : 40°C

Sym.	Parts Name	ΔT Temperature Rise (°C)			
		Mounting A	Mounting B	Mounting C	Mounting D
Q1	MOSFET	36.4	35.1	15.1	12.2
D1	BRIDGE DIODE	30.0	29.3	14.2	17.0
D7	S.B.D	36.6	25.8	13.4	15.0
T1	TRANS., PULSE	34.6	31.9	17.3	17.0
L1	BALUN COIL	35.3	35.5	15.7	17.8
C7	E.CAP	24.0	22.5	10.9	13.0
C14	E.CAP	31.3	23.5	14.2	14.6

### Conditions

	(A)	(B)	(C)	(D)
Mounting Method (Standard Mounting : A)				
Input Voltage	100VAC	100VAC	100VAC	100VAC
Output Voltage	5FF	5FF	5FF	5FF
Output Current	100%	100%	50%	50%

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : LWT15H-5FF

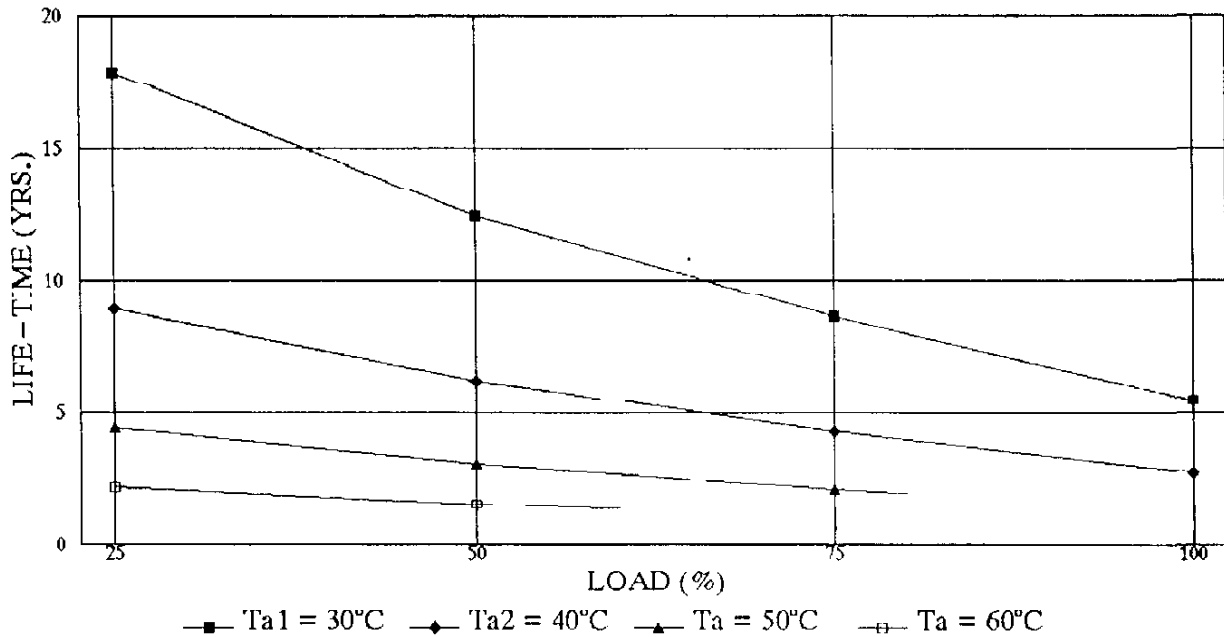
MOUNTING POSITION : A

Vin : 100VAC

Iout (100%) = CH1 : 2A  
 CH2 : 0.28A  
 CH3 : 0.19A

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	17.9	8.9	4.5	2.2
50	12.5	6.2	3.1	1.6
75	8.6	4.3	2.2	—
100	5.5	2.8	—	—

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

L : Elec. capacitor computed life  
 (24 hours per day, 365 days operation)

$$L = L_o \times 2^{\frac{105 - T_c}{10}} \quad (\text{Yrs})$$

L<sub>o</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : LWT15H-5FF

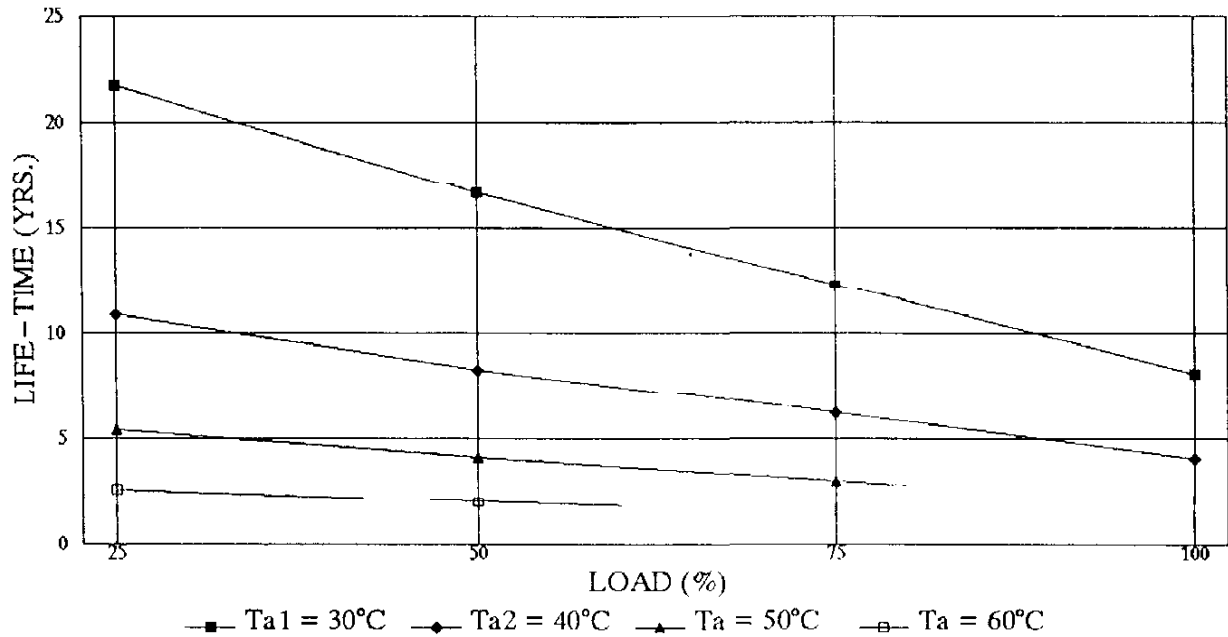
MOUNTING POSITION: B

V<sub>in</sub> : 100VAC

I<sub>out</sub> (100%) = CH1 : 2A  
 CH2 : 0.20A  
 CH3 : 0.19A

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	21.7	10.8	5.4	2.7
50	16.7	8.3	4.2	2.1
75	12.4	6.2	3.1	—
100	8.2	4.1	—	—

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

L : Elec. capacitor computed life  
 (24 hours per day, 365 days operation)

$$L = L_0 \times 2^{\frac{105 - T_c}{10}} \quad (\text{Yrs})$$

L<sub>0</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

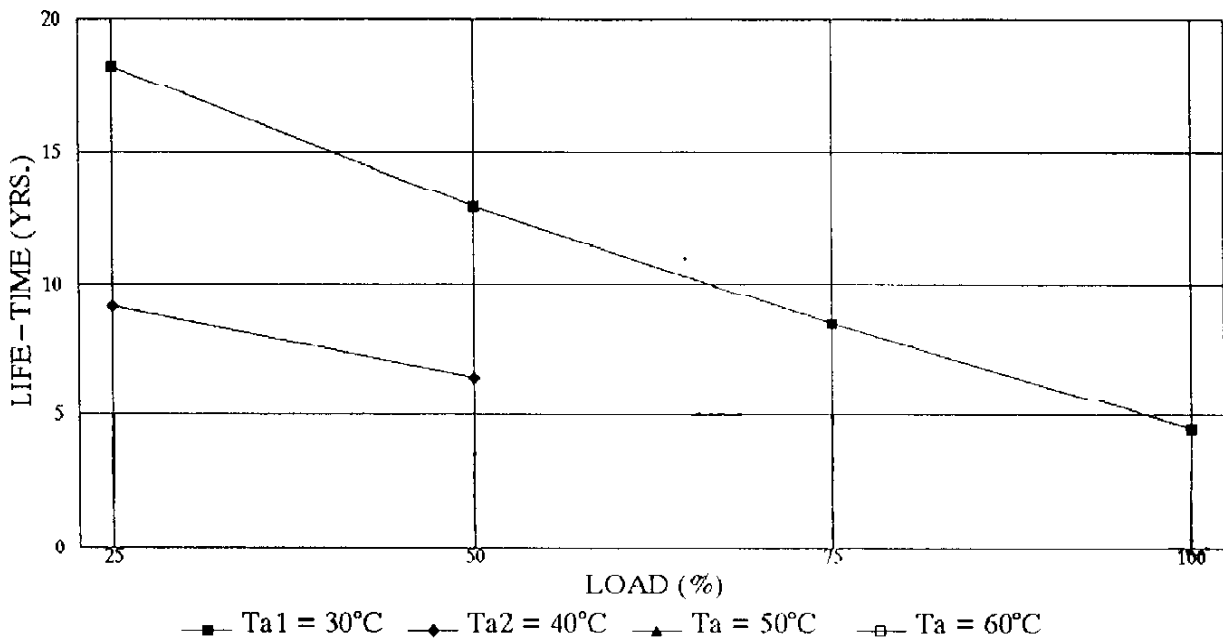
MODEL : LWT15H-5FF

MOUNTING POSITION: C  
 I<sub>out</sub> (100%) = CH1 : 2A  
 CH2 : 0.28A  
 CH3 : 0.19A

V<sub>in</sub> : 100VAC

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	18.3	9.1	-	-
50	12.9	6.5	-	-
75	8.5	-	-	-
100	4.5	-	-	-

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

$$L = L_0 \times 2^{\frac{106 - T_c}{10}} \quad (\text{Yrs})$$

L : Elec. capacitor computed life  
 (24 hours per day, 365 days operation)

L<sub>0</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor



## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : LWT15H-5FF

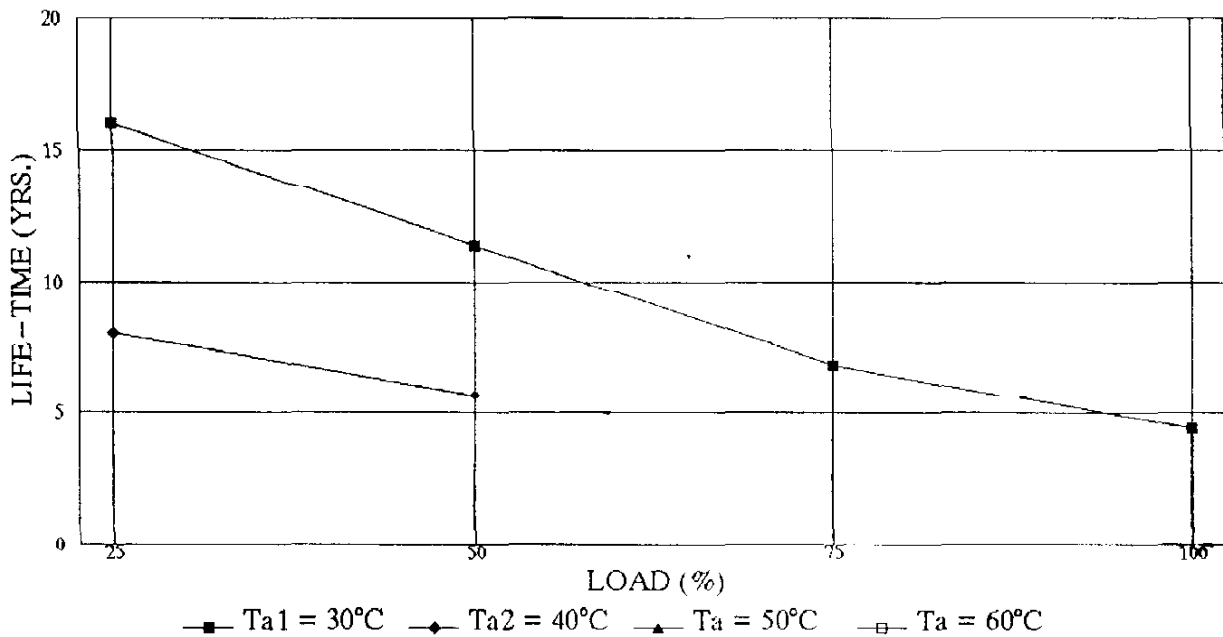
MOUNTING POSITION : D

Vin : 100VAC

Iout (100%) = CH1 : 2A  
 CH2 : 0.28A  
 CH3 : 0.19A

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	16.0	8.0	—	—
50	11.4	5.7	—	—
75	6.8	—	—	—
100	4.4	—	—	—

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

L : Elec. capacitor computed life  
 (24 hours per day, 365 days operation)

$$L = L_0 \times 2^{\frac{105 - T_c}{10}} \quad (\text{Yrs})$$

L<sub>0</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : LWT15H-5FF

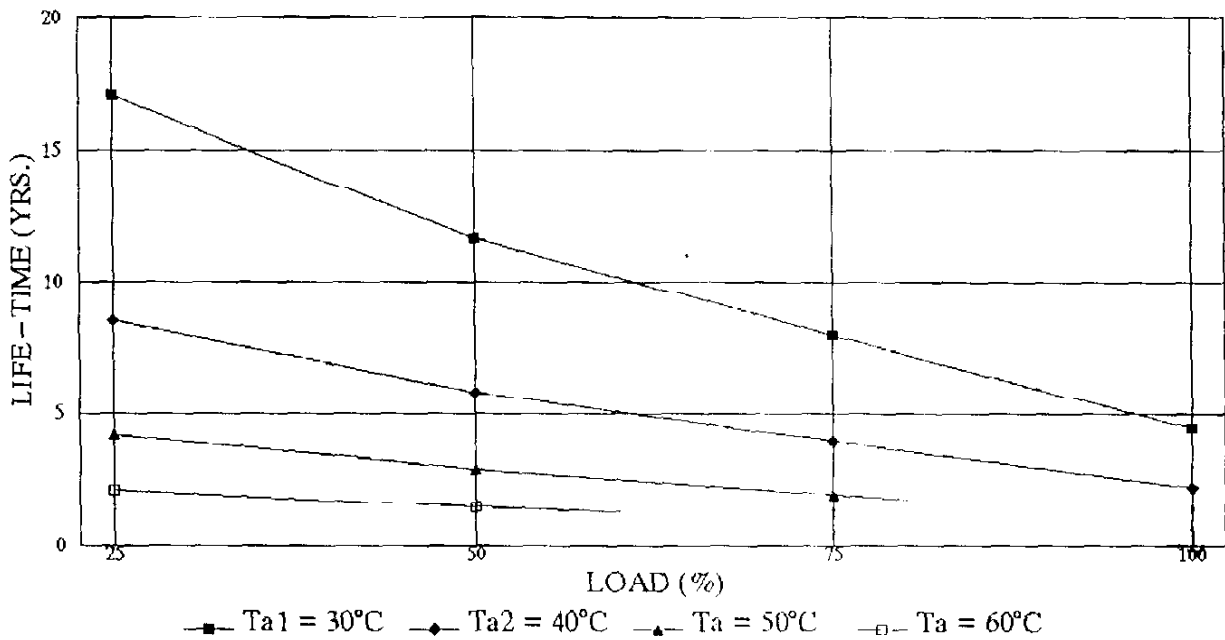
MOUNTING POSITION : A

Vin : 200VAC

Iout (100%) = CH1 : 2A  
 CH2 : 0.28A  
 CH3 : 0.19A

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	17.0	8.5	4.3	2.1
50	11.6	5.8	2.9	1.5
75	8.0	4.0	2.0	-
100	4.5	2.2	-	-

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

$$L = L_0 \times 2^{\frac{105 - T_c}{10}} \quad (\text{Yrs})$$

L : Elec. capacitor computed life  
(24 hours per day, 365 days operation)

L<sub>0</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : LWT15H-5FF

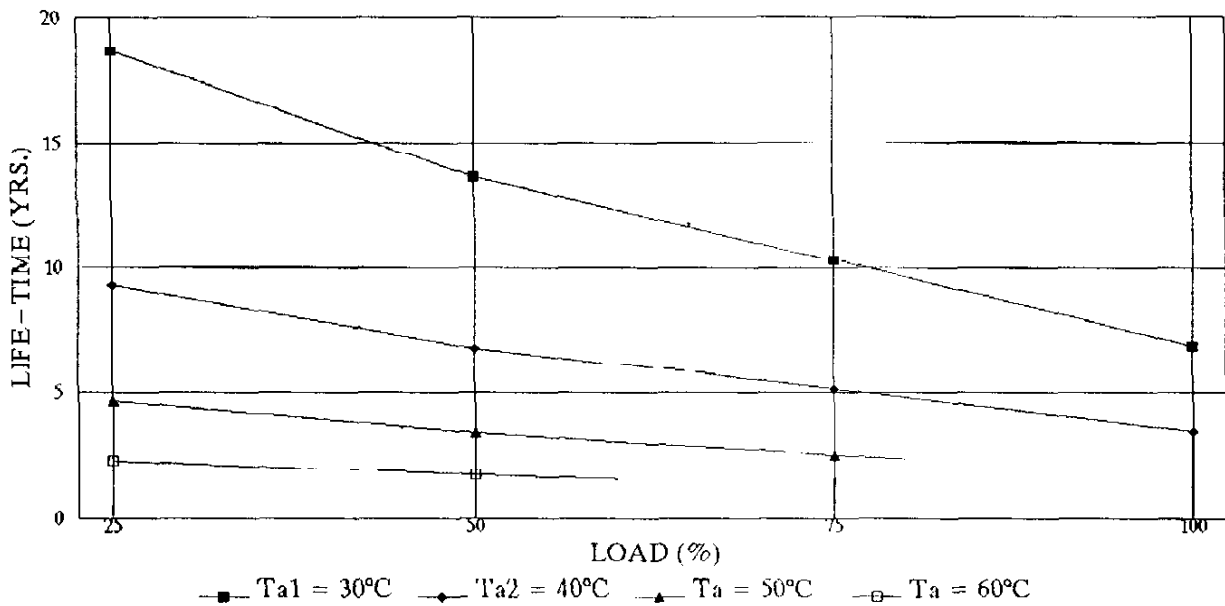
MOUNTING POSITION : B

Vin : 200VAC

Iout (100%) = CH1 : 2A  
 CH2 : 0.28A  
 CH3 : 0.19A

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	18.6	9.3	4.7	2.3
50	13.6	6.8	3.4	1.7
75	10.3	5.2	2.6	-
100	6.9	3.5	-	-

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

$$L = L_o \times 2^{\frac{105 - T_c}{10}} \quad (\text{Yrs})$$

L : Elec. capacitor computed life  
 (24 hours per day, 365 days operation)

L<sub>o</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : LWT15H-5FF

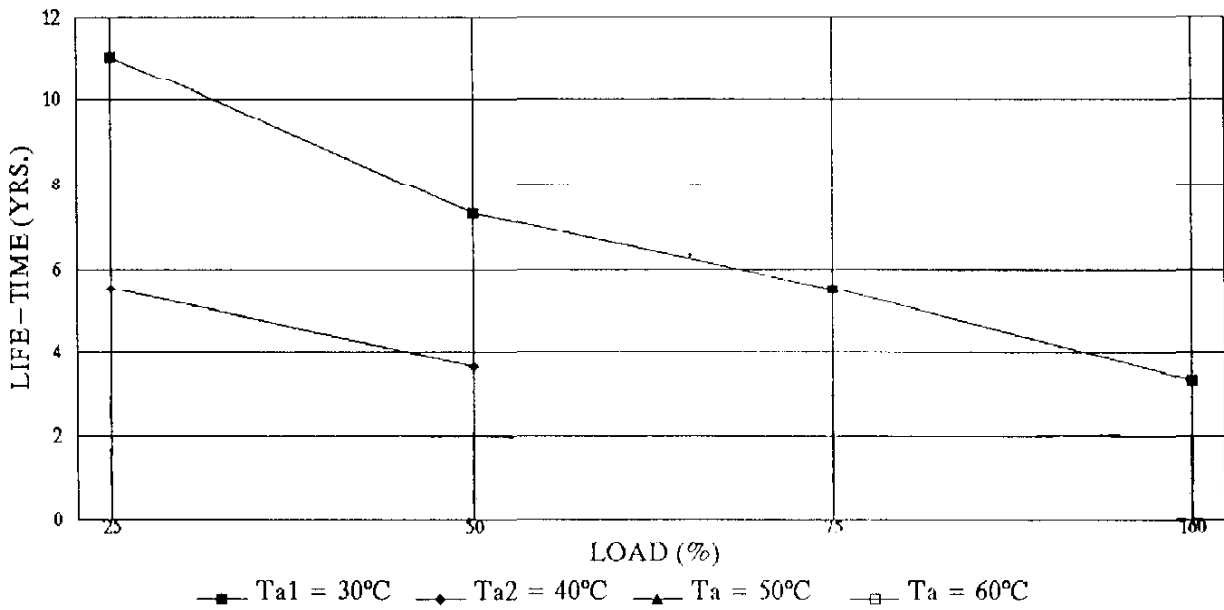
MOUNTING POSITION : C

Vin : 200VAC

Iout (100%) = CH1 : 2A  
 CH2 : 0.28A  
 CH3 : 0.19A

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	11.1	5.5	--	--
50	7.3	3.7	--	--
75	5.5	--	--	--
100	3.3	--	--	--

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

$$L = L_o \times 2^{\frac{105 - T_c}{10}} \quad (\text{Yrs})$$

L : Elec. capacitor computed life  
 (24 hours per day, 365 days operation)

L<sub>o</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor

## ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : LWT15H-5FF

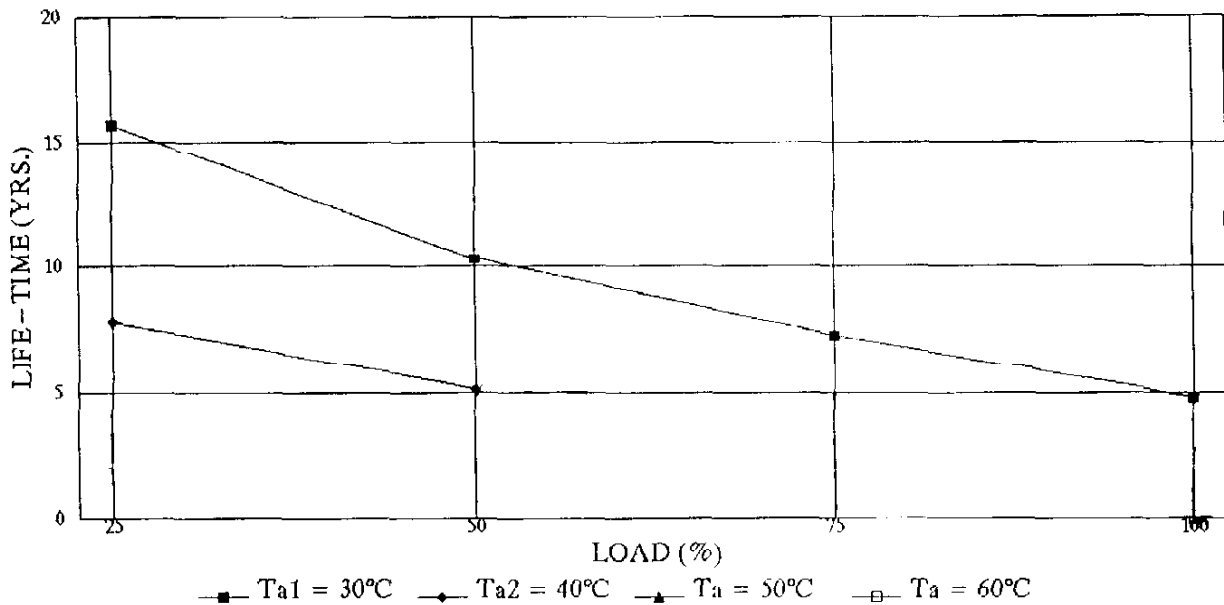
MOUNTING POSITION : D

Vin : 200VAC

I<sub>out</sub> (100%) = CH1 : 2A  
 CH2 : 0.28A  
 CH3 : 0.19A

LOAD (%)	LIFETIME (YRS.)			
	Ta1 = 30°C	Ta2 = 40°C	Ta3 = 50°C	Ta4 = 60°C
25	15.7	7.8	—	—
50	10.3	5.2	—	—
75	7.3	—	—	—
100	4.8	—	—	—

GRAPH OF ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD



**FORMULA :**

1. AL. Elec. capacitor

L : Elec. capacitor computed life  
 (24 hours per day, 365 days operation)

$$L = L_0 \times 2^{\frac{105 - T_c}{10}} \quad (\text{Yrs})$$

L<sub>0</sub> : Guarantee life for Elec. capacitor

T<sub>c</sub> : Case temperature of Elec. capacitor



MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS				APPROVED BY		TESTED BY					
		TESTMODE										LOAD = 100% TYPICAL				Vin = 200VAC Ta = 25°C		Ramona H		CTLEE			
PARTS NAME	PART NO.	TESTMODE		OPEN	SMOKE A	SMOKE B	BURST	SMELL	RED HOT	DAMAGE	FUSE	BLCWN	O.C.P.	O.V.P.	NO OUTPUT	NO CHANGE	OTHERS	NOTE	OK	TEST	GOOD		
		SHORT	SMOKE																				
1	HIC																						
2	AB712																						
3																							
4																							
5																							
6																							
7																							
8																							
9																							
10																							
11																							
12																							
13																							
14																							
15	3T-REGULATOR																						
16	TA7815S																						
17																							
18																							
19																							
20																							
21																							
22																							
23																							
24																							
25																							

\*\*\* A : SLIGHT B : PROLONGED







MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS				APPROVED BY	TESTED BY							
		TEST MODE										LOAD = 100% TYPICAL				Vin = 230VAC Ta = 25°C	Ramsey, R.	CTLEE						
PARTS NAME	PART NO.	TEST MODE	OPEN	FIRE	S M O K E A	S M O K E B	B U R S T	S M E L L	R E D H O T	D A M A G E	F U S E B L O W N	O . V . P .	O . N O U T P U T	N O C H A N G E	O T H E R S				NOTE		O K T E S T	N O G O O D		
1	CHIP DIODE																							
2	1SS193		Y											Y							Y			
3																								
4	F.R.D		Y																					
5	1NU41		Y											Y								Y		
6																								
7	F.R.D		Y																				Y	
8	EGP10G		Y																				Y	
9																								
10	NOT ASSIGNED																							
11																								
12																								
13	NOT ASSIGNED																							
14																								
15																								
16	S.B.D		Y																					Y
17	D10SC6M		Y																					Y
18																								
19	NOT ASSIGNED																							
20																								
21	F.R.D		Y																					Y
22	5FL2CZ41A		Y																					Y
23																								
24	F.R.D		Y																					Y
25	V19E		Y																					Y

\*\*\* A : SLIGHT B : PROLONGED

MODEL : LWT15H-5FF		ABNORMAL TESTING												TEST CONDITIONS				APPROVED BY TESTED BY						
PARTS NAME	PART NO.	TEST MODE	OPEN	SHORT	FIRE	SMOKE A	SMOKE B	BURST	SMELL	RED HOT	DAMAGE	FUSE	BLOWN	O.C.P.	O.V.P.	NO OUTPUT	NO CHANGE	OTHERS	LOAD = 100% TYPICAL		NOTE	OK	TEST	
																			IN = 200VAC	TA = 25°C				
1 F.R.D	D11	A-K	Y																		HICCUP	Y		
2 S2L40		A-K	Y																		HICCUP	Y		
3																								
4 NOT ASSIGNED	D12																							
5																								
6 F.R.D	D13	A-K	Y											Y								Y		
7 V19E		A-K	Y																			Y		
8																								
9 F.R.D	D50	A-K	Y																			Y		
10 1SS193-TE85L		A-K	Y																			Y		
11																								
12 ZENER DIODE	ZD1	A-K	Y													Y						Y		
13 1N4746A		A-K	Y																			Y		
14																								
15 ZENER DIODE	ZD2	A-K	Y													Y						Y		
16 1N4746A		A-K	Y																			Y		
17																								
18 ZENER DIODE	ZD3	A-K	Y							Y											Q1 DAMAGED	Y		
19 1N4735A		A-K	Y																			Y		
20																								
21 ZENER DIODE	ZD4	A-K	Y												Y							Y		
22 HZS5.1NB2		A-K	Y																			Y		
23																								
24 CAP. FILM	C1		Y										Y									Y		
25 ECQ-U2A474MX			Y																			Y		

\*\*\* A : SLIGHT B : PROLONGED

MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS			APPROVED BY		TESTED BY								
PARTS NAME		PART NO.	TEST MODE	S H O R T	O P E N	F I R E	S M O K E A	S M O K E B	B U R S T	S M E L L	R E D H O T	D A M A G E	F U S E B L O W N	O . C . P .	O . V . P .	N O O U T P U T	N O C H A N G E	O T H E R S	N O T E	L O A D = 100% TYPICAL	V <sub>in</sub> = 200VAC	T <sub>a</sub> = 25°C	RANGE	CTLEE	
	NOT ASSIGNED	C2																							
	CAP., CERAMIC	C3		Y													Y								
	CD-19E2GA32MYAS				Y												Y								
	NOT ASSIGNED	C4																							
	CAP., CERAMIC	C5		Y													Y								
	CD-19E2GA472MYAS				Y												Y								
	CAP., CERAMIC	C6		Y													Y								
	CD-19E2GA472MYAS				Y												Y								
	CAP., ELECT	C7		Y									Y												
	LGE2G101MHSB				Y												Y								
	NOT ASSIGNED	C8																							
	CAP., ELECT	C9		Y																					
	UPR2G010MPH				Y																				
	CAP., FILM	C10		Y																					
	MMHF-103K630				Y																				

\*\*\* A : SLIGHT B : PROLONGED



MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS				APPROVED BY	TESTED BY										
		PARTS NAME										LOAD = 100% TYPICAL				$V_{in} = 200VAC$ $T_a = 25^{\circ}C$	<i>Ramphal</i>	<i>cT Lee</i>									
PARTS NAME		PART NO.	TEST MODE										O . V . P .	O . C . P .	F U S E B L O W N	O T H E R S	N O C H A N G E	N O O U T P U T	O K	R E T E S T							
			S H O R T	O P E N	F I R E	S M O K E A	S M O K E B	B U R S T	S M E L L	R E D H O T	D A M A G E	S M O K E A	S M O K E B	S M E L L	R E D H O T	D A M A G E	F U S E B L O W N	O . V . P .	O . C . P .	N O O U T P U T	N O C H A N G E	O T H E R S	N O C H A N G E	O T H E R S	O K	R E T E S T	
1	CAP., FILM	C20	Y	Y																	Y				Y		
2	MMHF-223630																				Y					Y	
3																											
4	CHIP CAP., CERAMIC	C21	Y	Y															Y						Y		
5	GRM42-6R104K50PT																				Y					Y	
6																											
7	CHIP CAP., CERAMIC	C22	Y	Y																	Y				Y		
8	CM21W5R331K200BT																				Y				Y		
9																											
10	CAP., ELECT	C23	Y	Y															Y						Y		
11	LXF35VB220																				Y				Y		
12																											
13	CAP., ELECT	C24	Y	Y															Y						Y		
14	LXF35VB150																				Y				Y		
15																											
16	CHIP CAP., CERAMIC	C25	Y	Y															Y						Y		
17	GRM42-6R104K50PT																				Y				Y		
18																											
19	CAP., ELECT	C26	Y	Y															Y						Y		
20	LXF35VB220																				Y				Y		
21																											
22	CAP., ELECT	C27	Y	Y															Y						Y		
23	LXF35VB150																				Y				Y		
24																											
25																											

\*\*\* A : SLIGHT B : PROLONGED

MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS				APPROVED BY TESTED BY				
												LOAD = 100% TYPICAL		V <sub>in</sub> = 200VAC T <sub>a</sub> = 25°C		Rampson CT LEE				
		TEST MODE														NOTE		R E T E S T		
PARTS NAME	PART NO.	S H C R T	O P E N	F I R E	S M O K E A	S M O K E B	B U R S T	S M E L L	R E D H O T	D A M A G E	F U S E B L O W N	O . V . P .	O . C . P .	N O O U T P U T	N O C H A N G E	O T H E R S				
1	CAP., FILM		Y											Y			Y			
2	MMHF-333K630		Y											Y			Y			
3																				
4	CHIP CAP., CERAMIC		Y									Y					Y			
5	GRM42-6R104K50PT		Y									Y					Y			
6																				
7	CHIP CAP., CERAMIC		Y									Y					Y			
8	GRM42-6R104K50PT		Y									Y					Y			
9																				
10	CHIP CAP., CERAMIC		Y									Y					Y			
11	GRM42-6R104K50PT		Y									Y					Y			
12																				
13	CHIP CAP., CERAMIC		Y									Y					Y			
14	GRM42-6R104K50PT		Y									Y					Y			
15																				
16	CHIP CAP., CERAMIC		Y									Y					Y			
17	GRM42-6R104K50PT		Y									Y					Y			
18																				
19	CHIP CAP., CERAMIC		Y									Y					Y			
20	GRM42-6R104K50PT		Y									Y					Y			
21																				
22	CHIP CAP., CERAMIC		Y									Y					Y			
23	GRM42-6R104K50PT		Y									Y					Y			
24																				
25																				

\*\*\* A : SLIGHT B : PROLONGED

MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS				APPROVED BY	TESTED BY							
												LOAD = 100% TYPICAL	Vin = 200VAC	Ta = 25°C	Rawayhan	CTLEE								
PARTS NAME	PART NO.	TEST MODE										O.C.P.	O.V.P.	N O U T P U T	N O C H A N G E	O T H E R S	NOTE	O K F E S T	R E T E S T	N O G O O D				
		OPEN	SHORT	FIRE	SMOKE A	SMOKE B	BURST	SMELL	RED HOT	DAMAGE	FUSE LOW													
1	CHIP CAP., CERAMIC		Y											Y							Y			
2	GRM40SLJ101K50PT		Y	Y																		Y		
3																								
4	CHIP CAP., CERAMIC													Y								Y		
5	GRM40R222J50PT		Y	Y										Y								Y		
6																								
7	CHIP CAP., CERAMIC													Y								Y		
8	GRM40SLJ101K50PT			Y																		Y		
9																								
10	CHIP CAP., CERAMIC													Y								Y		
11	GRM42-6R104K50PT		Y	Y																		Y		
12																								
13																								
14	CHIP RESISTOR													Y										
15	RK73B2HTE244J		Y	Y																		Y		
16																								
17	CHIP RESISTOR													Y										
18	RK73B2HTE244J		Y	Y																		Y		
19																								
20	CHIP RESISTOR													Y										
21	RK73B2HTE394J		Y	Y																		Y		
22																								
23	CHIP RESISTOR													Y										
24	RK73B2HTE244J		Y	Y																		Y		
25																								

\*\*\* A : SLIGHT B : PROLONGED



MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS				APPROVED BY TESTED BY					
		TESTMODE										LOAD = 100% TYPICAL				Vin = 200VAC Ta = 25°C		Approved by: <i>Ramona</i> CTLEE			
PARTS NAME	PART NO.	SHORT	OPEN	FIRE	SMOKE A	SMOKE B	BURSTS	SMELL	RED HOT	DAMAGE	FUSE BLOWN	O.V.P.	O.C.P.	NO OUTPUT	NO CHANGE	OTHERS	NOTE	OK	RETEST	NO GOOD	
1	CHIP RESISTOR R5	Y	Y											Y	Y			Y			
2	RK73B2HTE244J														Y				Y		
3																					
4	CHIP RESISTOR R6	Y	Y									Y					HICCUP		Y		
5	RK73B2HTE244J													Y					Y		
6																					
7	CHIP RESISTOR R7	Y	Y																Y		
8	RK73B2HTE244J																		Y		
9																					
10	RES., FUSE R8	Y	Y						Y	Y							A50		Y		
11	FMR2B472J														Y				Y		
12																					
13	CHIP RESISTOR R9	Y	Y											Y					Y		
14	CR1/10W273JV																		Y		
15																					
16	CHIP RESISTOR R10	Y	Y																Y		
17	RK73B3ATE203J																		Y		
18																					
19	CHIP RESISTOR R11	Y	Y																Y		
20	CR1/10W273JV																		Y		
21																					
22	CHIP RESISTOR R12	Y	Y																Y		
23	RK73B2HTE240J																		Y		
24																					
25																					

\*\*\*\* A : SLIGHT B : PROLONGED





MODEL : LWT15H-5FF		ABNORMAL TESTING										TEST CONDITIONS				APPROVED BY	TESTED BY		
		TEST MODE		TEST MODE		TEST MODE		TEST MODE		TEST MODE		TEST MODE		TEST MODE		TEST MODE			
PARTS NAME	PART NO.	OPEN	SHORT	FIRE	SMOKE A	SMOKE B	BURST	SMELL	RED HOT	DAMAGE	FUSE BLOWN	O.C.P.	O.V.P.	NO OUTPUT	NO CHANGE	OTHERS	NOTE	O.K.	TESTED BY
1	CHIP RESISTOR CR1/10W682JV		Y											Y				Y	CTLee
2														Y				Y	
3															Y			Y	
4	CHIP RESISTOR ERJ8GEYJ100V		Y											Y				Y	
5																		Y	
6																		Y	
7	CHIP RESISTOR ERJ8GEYJ300V		Y											Y				Y	
8																		Y	
9																		Y	
10	CHIP RESISTOR CR1/10W102JV		Y									Y						Y	
11																		Y	
12																		Y	
13	CHIP RESISTOR CR1/10W182JV		Y															Y	
14																		Y	
15																		Y	
16	CHIP RESISTOR CR1/10W362JV		Y															Y	
17																		Y	
18																		Y	
19	RES., VARIABLE RJ-4W20		Y										Y					Y	
20			Y										Y					Y	
21			Y															Y	
22																		Y	
23																		Y	
24																		Y	
25																		Y	

\*\*\* A : SLIGHT B : PROLONGED



MODEL : LWT15H-SFF		ABNORMAL TESTING										TEST CONDITIONS			APPROVED BY	TESTED BY					
												LOAD = 100% TYPICAL	ViN = 200VAC Ta = 25°C	<i>Ramsey, R</i>	<i>CT LEE</i>						
PARTS NAME	PART NO.	TEST MODE	OPEN	SHORT	FIRE	SMOKE A	SMOKE B	BURST	SMELL	RED HOT	DAMAGE	FUSE BLOWN	O.V.P.	O.C.P.	NO OUTPUT	NO CHANGE	OTHERS	NOTE	O.K.	RETEST	NO GOOD
1	BALUN COIL	L1		Y												Y			Y		
2	PLA1530R5A020	L2		Y												Y			Y		
3		34		Y								Y							Y		
4		14		Y								Y							Y		
5		23		Y								Y							Y		
6		1	Y												Y				Y		
7		2	Y												Y				Y		
8		3	Y												Y				Y		
9		4	Y												Y				Y		
10	NOT ASSIGNED	L2																			
11																					
12	CHOKE COIL	L3		Y											Y				Y		
13	RD810A3F			Y											Y				Y		
14																					
15	FUSE	F1		Y											Y				Y		
16	FGLMT250V2.5A			Y											Y				Y		
17																					
18																					
19																					
20																					
21																					
22																					
23																					
24																					
25																					

\*\*\* A : SLIGHT B : PROLONGED

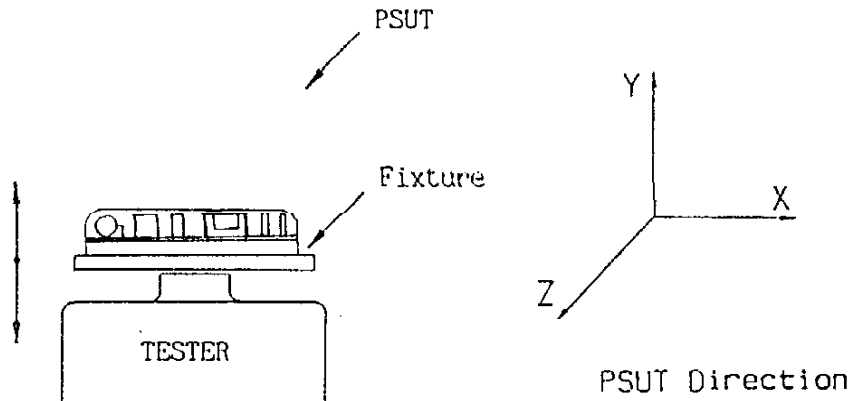
VIBRATION TEST

TYPES OF VIBRATION TEST :

A) OSCILLATOR FREQUENCY SWEEP

EQUIPMENT : EMIC CORPORATION VIBRATION TEST SYSTEM F-400-BM-E47  
 VIBRATION GENERATOR 905-FN

PROCEDURE :



A) VIBRATION TEST WITH FREQUENCY SWEEP

FREQUENCY	10 ~ 55 Hz
SWEEP TIME	1 min.
ACCELERATION	Fixed (2G)
DIRECTION	X, Y, Z
DURATION	1 hr. for each direction.

TEST POINT :

1. Output voltage (Apply some shock when checking the o/p voltage, and observe any abnormalities.)
2. Ripple voltage (At 100VAC and output)
3. Mechanical Condition (No breakage)

認 APPD	RCNEO 15.8.94	設 計 ENGR	Ramex.m 1.8.94	圖面番号 DWG-No.	- <span style="border: 1px dashed black; display: inline-block; width: 20px; height: 20px; vertical-align: middle;"></span>
檢 閱 CHK	Radson 1.8.94	製 圖 DWG	Ramex.m 1.8.94		

TEST RESULTS :  
(after vibration)

A)

TEST POINT	OUTPUT VOLTAGE (V)			RIPPLE VOLTAGE (mV)			MECHANICAL CONDITION	NOTE
	CH1	CH2	CH3	CH1	CH2	CH3		
BEFORE TEST DIRECTION	5.005	12.026	11.980	70	60	60	O.K	
X	5.005	12.026	11.980	70	60	60	O.K	
Y	5.005	12.026	11.980	70	60	60	O.K	
Z	5.005	12.026	11.980	70	60	60	O.K	

EVALUATION RESULT :

PASS /  FAIL

VISUAL INSPECTION RESULT :

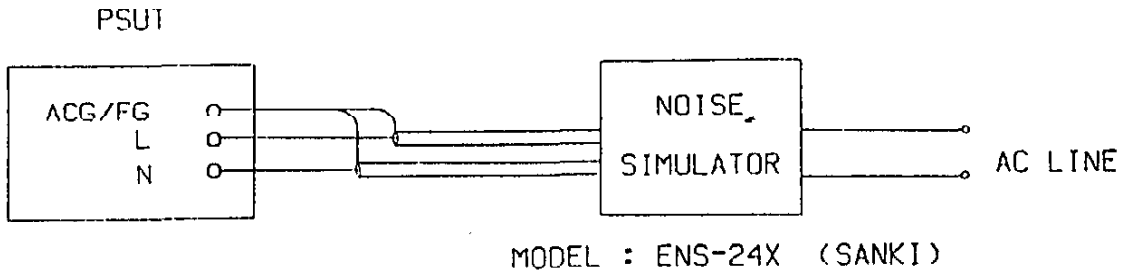
PASS /  FAIL

認 APPD	RCNBO 15.8.94	設 計 ENGR	Rameth.m 1.8.94	図面番号 DWG - No.
検 図 CHK	Gaku 1.8.94	製 図 DWG	Rameth.m 1.8.94	



NOISE SIMULATION TEST

Circuit for measurement and equipment used :



Testing Conditions :

- Input Voltage : 100VAC
- Output Voltage : Rated
- Output Current : ML, 100%
- Ambient Temp. : 25 °C

Settings :

- MODE ..... Normal , Common
- TRIG SELECT .... Line or Ext (Line)
- PULSE WIDTH .... 50, 200, 800, 1000ns
- PHASE SHIFT .... 0 ~ 360 Degree
- POLARITY ..... + , -
- NOISE LEVEL .... 0 ~ 2KV

Acceptance Criteria :

- 1) No damage of PSUT
- 2) No output failure
- 3) Check any abnormalities (eg. noise)

Evaluation Result :

(PASS) / FAIL

認 APPD	CCNEO 15.8.94	設 計 ENGR	Ramesh.M 1.8.94	圖面番号 DWG-No.
檢 閱 CHK	Rajadhin 1.8.94	製 閱 DWG	Ramesh.M 1.8.94	- <span style="border: 1px dashed black; display: inline-block; width: 20px; height: 20px; vertical-align: middle;"></span>

ELECTROSTATIC DISCHARGE TEST

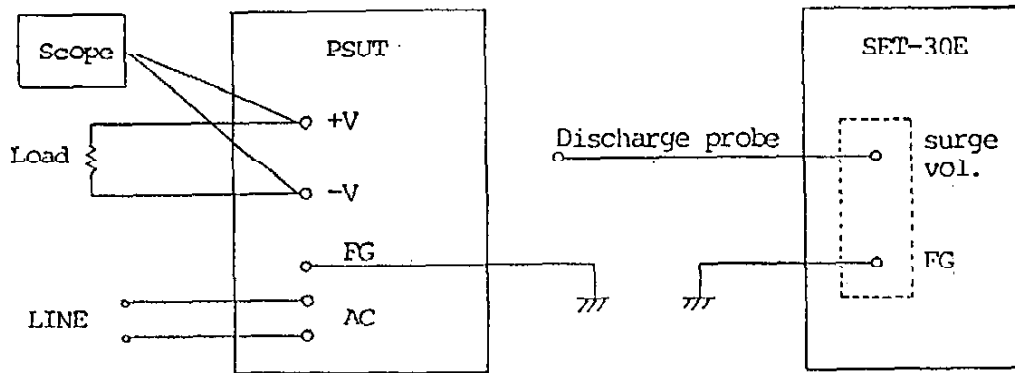
EQUIPMENT : SET-30E (SANKI ELECTRONIC)

Discharge Resistor : 250 ohm  
Capacitor unit : 200 nF

CONDITIONS : Ambient Temperature : 25°C  
Input Voltage : 100 VAC  
Output Voltage : Rated  
Output Current : Rated  
Applied Voltage : ±3kV, ±5kV, ±10kV, ±15kV

PROCEDURE : The PSUT should be in a good working condition. Discharge the applied voltage to the touchable parts of the PSUT (Chassis, Input Terminal, Output Terminal, FG Terminal, ACG Terminal) and check any abnormalities.

Each point to be tested 3 times with different polarity. Voltage should be applied from 3kV to 15kV.



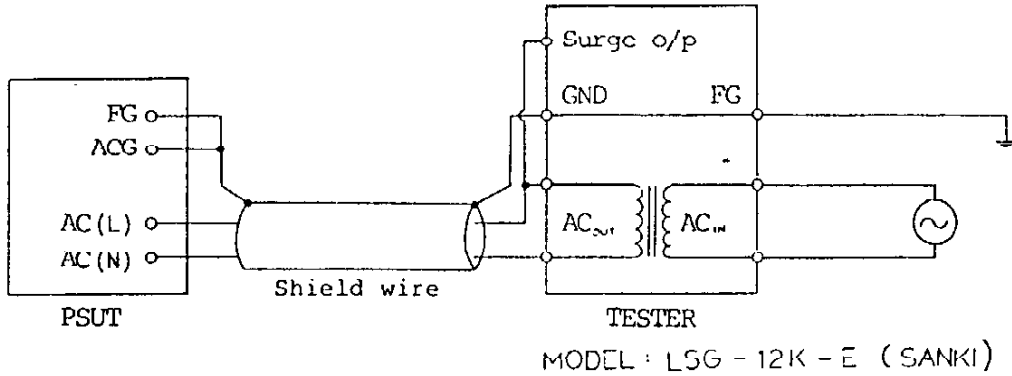
ACCEPTANCE CRITERIA : 1. No damage of PSUT  
2. No output failure  
3. No abnormalities

EVALUATION RESULT : (PASS) / FAIL

認 APPD	RANGE 15.8.94	設 計 ENGR	Ramesh.n 1.8.94	図面番号 DWG-No.	-
検 査 CHK	1.8.94	製 図 DWG	Ramesh.n 1.8.94		

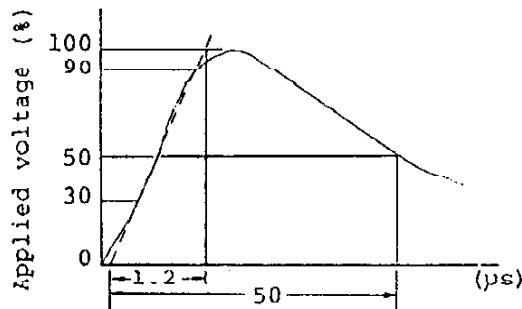
LIGHTNING SURGE TEST

TEST CIRCUIT, TEST EQUIPMENT



- CONDITIONS :
- Input Voltage : AC100V
  - Output Voltage : Rated
  - Output Current : Rated
  - Applied Voltage : From 3kV in steps of 0.5kV  
Check the max. withstand voltage
  - Applied Point : Between FG - AC
  - Number of Test : Each voltage 3 times
  - Polarity : + , -
  - Ambient Temp. : 25°C

APPLIED VOLTAGE WAVEFORM :



- ACCEPTANCE CRITERIA :
1. No damage to the PSUT
  2. No output failure
  3. No abnormalities

EVALUATION RESULT : 3.0KV **PASS** / FAIL

認・ APPD	CCNED 16.08.94	設 計 ENGR	Rameth.m 1.8.94	図面番号 DWG-No.
検 査 CHK	Raddin 15.8.94	製 図 DWG	Rameth.m 1.8.94	