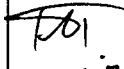




VS15C

RELIABILITY DATA

DWG No. : CA710-57-01B		
APPD	CHK	DWG
 20-Nov-07	 20/Nov/07	 10-Nov-07

I N D E X

	PAGE
1. Calculated Values of MTBF	R-1
2. Component Derating	R-2~3
3. Main Components Temperature Rise ΔT List	R-4
4. Electrolytic Capacitor Life	R-5~9
5. Abnormal Test	R-10~11
6. Vibration Test	R-12
7. Noise Simulate Test	R-13

The above data is typical value. As all units have nearly the same characteristics, the data to be considered as ability value.

1. CALCULATED VALUES OF MTBF

MODEL : VS15C-5

(1) Calculating method

Calculated based on part count reliability projection of JEITA (RCR-9102).

Individual failure rates λ_G is given to each part and MTBF is calculated by the count of each part.

<Formula> :

$$MTBF = \frac{1}{\lambda_{\text{equip}}} = \frac{1}{\sum_{i=1}^n N_i (\lambda_G \pi_Q)_i} \times 10^6 \text{ (Hours)}$$

λ_{equip} : Total Equipment Failure Rate (Failure/10⁶ Hours)

λ_G : Generic Failure Rate for The ith Generic Part (Failure/10⁶ Hours)

N_i : Quantity of ith Generic Part

n : Number of Different Generic Part Categories

π_Q : Generic Quality Factor for The ith Generic Part ($\pi_Q = 1$)

(2) MTBF Values

GF : (Ground , Fixed)

MTBF ≒ 845,780 (Hours)

2. COMPONENT DERATING

MODEL: VS15C-5

(1) Calculating Method

(a) Measuring conditions

Input : 100VAC , Ambient temperature : 50°C
 Output : 5V 3A(100%) , Mounting method : Standard Mounting

(b) Semiconductors

Compared with maximum junction temperature and actual one which is calculated based on case temperature, power dissipation and thermal impedance.

(c) IC, Resistors, Capacitors, etc.

Ambient temperature, operating condition, power dissipation and so on are within derating criteria.

(d) Calculating method of thermal impedance

$$\theta_{j-c} = \frac{T_{j(max)} - T_c}{P_{c(max)}} \quad \theta_{j-a} = \frac{T_{j(max)} - T_a}{P_{c(max)}} \quad \theta_{j-l} = \frac{T_{j(max)} - T_l}{P_{c(max)}}$$

T_c : Case Temperature at Start Point of Derating ; 25°C in General

T_a : Ambient Temperature at Start Point of Derating ; 25°C in General

T_l : Lead Temperature at Start Point of Derating ; 25°C in General

$P_{c(max)}$
 $(P_{ch(max)})$: Maximum Collector(Channel) Dissipation

$T_{j(max)}$
 $(T_{ch(max)})$: Maximum Junction(Channel) Temperature

θ_{j-c}
 (θ_{ch-c}) : Thermal Impedance between Junction(Channel) and Case

θ_{j-a} : Thermal Impedance between Junction and Air

θ_{j-l} : Thermal Impedance between Junction and lead

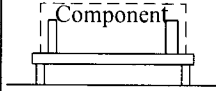
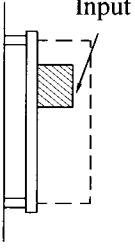
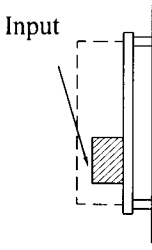
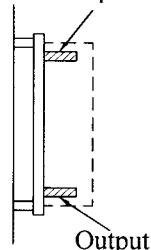
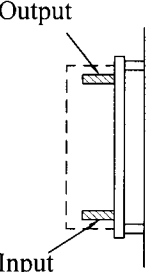
(2) Component Derating List

Location No.	Vin = 100VAC Load = 100% Ta = 50°C		
A1 UPC1944J NEC	Tjmax = 125 °C, Pd = 0.012 W, Tj = Ta + ((θj - a) x Pd) = 73.7 °C D.F. = 59.0 %	θ j-a = 178.6 °C /W, Δ Ta = 21.6 °C,	Pj(max) = 0.56 W Ta = 71.6 °C
D1 S1WB(A)60B -4101 SHINDENGEN	Tjmax = 150 °C, Pd = 0.347 W, Tj = Tl + ((θj - l) x Pd) = 88.7 °C D.F. = 59.1 %	θ j-l = 10 °C /W, Δ Tl = 35.2 °C,	Pj(max) = - W Tl = 85.2 °C
D2 D10SC4M SHINDENGEN	Tjmax = 150 °C, Pd = 1.65 W, Tj = Tc + ((θj - c) x Pd) = 123.5 °C D.F. = 82.3 %	θ j-c = 3.3 °C /W, Δ Tc = 68.1 °C,	Pj(max) = - W Tc = 118.1 °C
D3 ISS178 TOSHIBA	Tjmax = 175 °C, Pd = 0.01 W, Tj = Ta + ((θj - a) x Pd) = 81.4 °C D.F. = 46.5 %	θ j-a = 500 °C /W, Δ Ta = 26.4 °C,	Pj(max) = 0.3 W Ta = 76.4 °C
Q1 2SK2185 SHINDENGEN	Tchmax = 150 °C, Pd = 0.64 W, Tch = Tc + ((θ ch - c) x Pd) = 102.1 °C D.F. = 68.1 %	θ ch-c = 4.17 °C /W, Δ Tc = 49.4 °C,	Pch(max) = - W Tc = 99.4 °C
Q2 2SD467C HITACHI	Tjmax = 150 °C, Pd = 0.015 W, Tj = Ta + ((θj - a) x Pd) = 86.8 °C D.F. = 57.8 %	θ j-a = 250 °C /W, Δ Ta = 33 °C,	Pc(max) = 0.5 W Ta = 83 °C
PC1 PS2561-1-V-L (LED) NEC	Tjmax = 125 °C, If = 2.5 mA, If (max) = 53 mA (at Ta = 75 °C) D.F. = 4.7 %	Δ Pj /°C = -1.5 mW /°C, Δ Ta = 25 °C,	Pj(max) = 0.15 W Ta = 75 °C
PC1 PS2561-1-V-L (Transistor) NEC	Tjmax = 125 °C, Pd = 0.007 W, Tj = Ta + ((θj - a) x Pd) = 79.7 °C D.F. = 63.8 %	θ j-a = 667 °C /W, Δ Ta = 25 °C,	Pc(max) = 0.15 W Ta = 75 °C
ZD1 HZS24NB3 HITACHI	Tjmax = 200 °C, Pd = 0.017 W, Tj = Ta + ((θj - a) x Pd) = 94.8 °C D.F. = 47.4 %	θ j-a = 437.5 °C /W, Δ Ta = 37.4 °C,	Pj(max) = 0.4 W Ta = 87.4 °C
ZD2 HZS15NB2 HITACHI	Tjmax = 200 °C, Pd = 0 W, Tj = Ta + ((θj - a) x Pd) = 94.3 °C D.F. = 47.2 %	θ j-a = 437.5 °C /W, Δ Ta = 44.3 °C,	Pj(max) = 0.4 W Ta = 94.3 °C
ZD3 HZS18NB2 HITACHI	Tjmax = 200 °C, Pd = 0 W, Tj = Ta + ((θj - a) x Pd) = 80.7 °C D.F. = 40.4 %	θ j-a = 437.5 °C /W, Δ Ta = 30.7 °C,	Pj(max) = 0.4 W Ta = 80.7 °C
ZD4 HZ6.2CP HITACHI	Tjmax = 175 °C, Pd = 0 W, Tj = Ta + ((θj - a) x Pd) = 100.3 °C D.F. = 57.3 %	θ j-a = 187.5 °C /W, Δ Ta = 50.3 °C,	Pj(max) = 0.8 W Ta = 100.3 °C

3. MAIN COMPONENTS TEMPERATURE RISE ΔT LIST

MODEL : VS15C-5

Measuring Conditions

Mounting Method (Standard Mounting Method:(A))	(A)	(B)	(C)	(D)	(E)
	Horizontal mounting 	Vertical mounting 	Vertical mounting 	Vertical mounting 	Vertical mounting 
Input Voltage (VAC)	100	100	100	100	100
Output Voltage (VDC)	5	5	5	5	5
Output Current (A)	3	3	3	3	2.55

*Condition $T_a = 50^\circ\text{C}$, Convection cooling .

Output Derating (%) $T_a = 50^\circ\text{C}$		ΔT Temperature rise ($^\circ\text{C}$)				
		100%	100%	100%	100%	85%
Location No.	Parts Name	Mounting A	Mounting B	Mounting C	Mounting D	Mounting E
Q1	MOSFET	49.4	47.4	42.3	52.2	43.5
D1	BRIDGE DIODE	35.2	32.2	37.0	46.6	29.8
D2	OUTPUT DIODE	68.1	64.2	62.4	63.6	63.9
T1	X'MER.	50.8	46.7	45.6	51.8	48.7
C3	E. CAP.	26.2	21.2	28.7	29.8	21.4
C7	E. CAP.	36.1	32.1	39.6	33.4	35.6
C12	E. CAP.	19.1	23.5	18.5	17.8	30.1
C14	E. CAP.	26.4	33.1	16.1	23.4	24.4

4. ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : VS15C - 5

Mounting A

Input : 100VAC

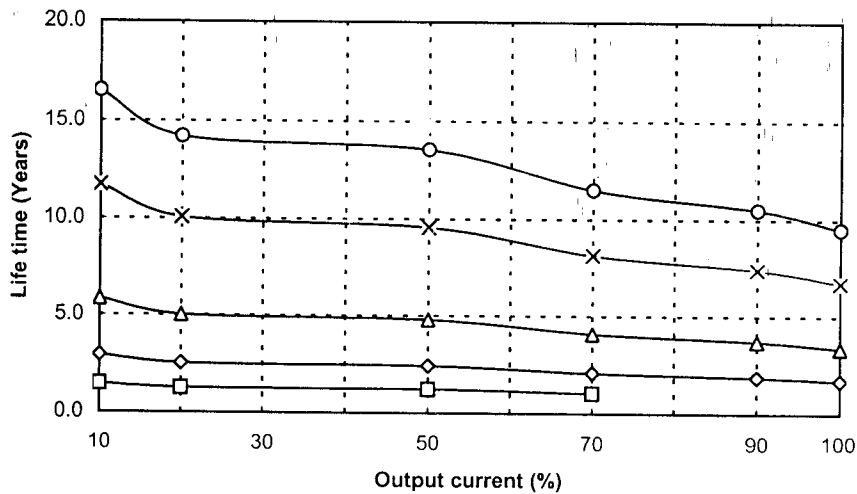
LOAD %	Life time (years)				
	Ta (°C)				
	25.0	30.0	40.0	50.0	60.0
10	16.6	11.7	5.9	2.9	1.5
20	14.2	10.1	5.0	2.5	1.3
50	13.5	9.6	4.8	2.4	1.2
70	11.5	8.2	4.1	2.0	1.0
90	10.5	7.4	3.7	1.9	—
100	9.5	6.7	3.4	1.7	—

$$L = L_o \cdot 2^{(105 - T_c) / 10} \quad (\text{Yrs})$$

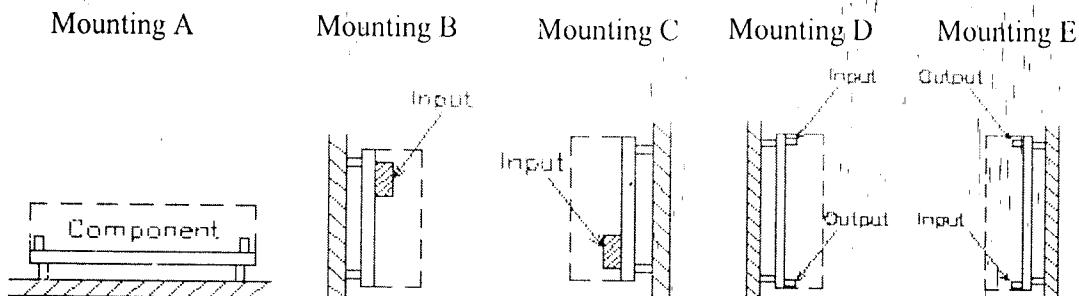
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L_o : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C --○-- Ta=30°C --X-- Ta=40°C --△-- Ta=50°C --◇-- Ta=60°C --□--



4. ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : VS15C - 5

Mounting B

Input : 100VAC

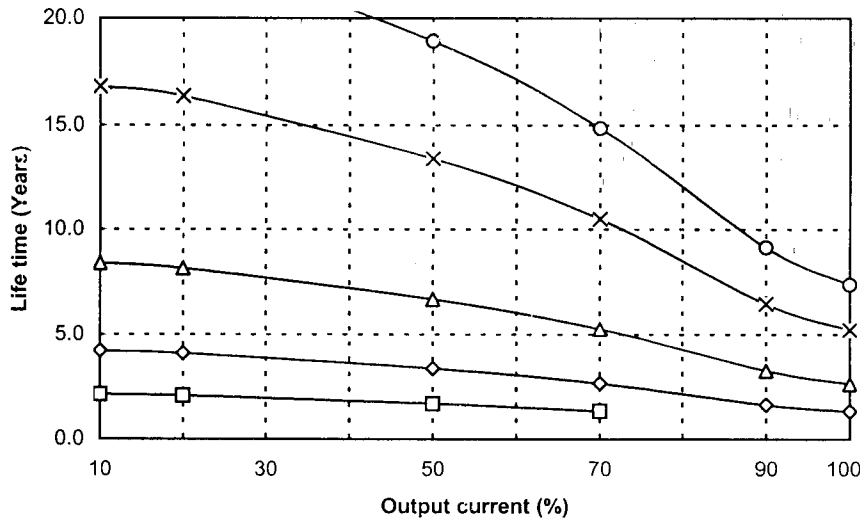
LOAD %	Life time (years)				
	Ta (°C)				
	25.0	30.0	40.0	50.0	60.0
10	23.7	16.8	8.4	4.2	2.1
20	23.1	16.3	8.2	4.1	2.0
50	18.9	13.4	6.7	3.3	1.7
70	14.8	10.5	5.3	2.6	1.3
90	9.1	6.5	3.2	1.6	—
100	7.4	5.2	2.6	1.3	—

$$L = L_o \cdot 2^{(105 - T_c) / 10} \quad (\text{Yrs})$$

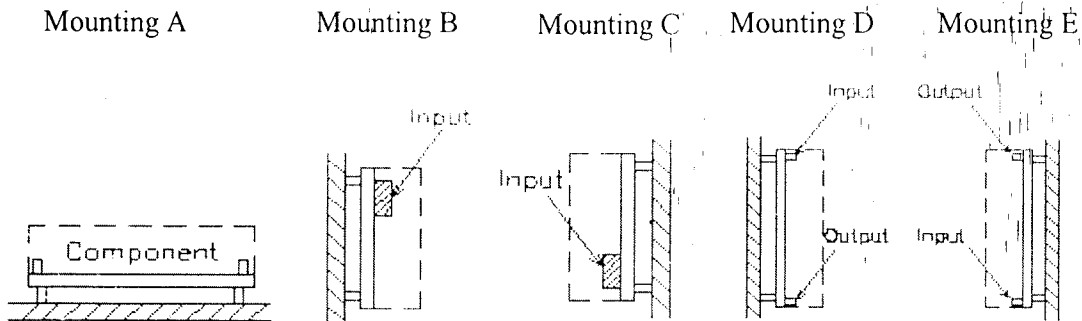
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L_o : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C -○- Ta=30°C -×- Ta=40°C -△- Ta=50°C -◇- Ta=60°C -□-



4. ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

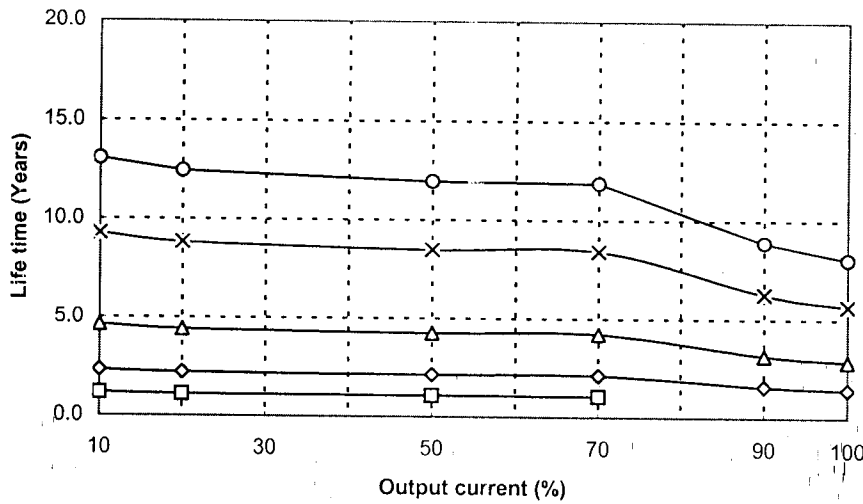
MODEL : VS15C - 5

Mounting C
Input : 100VAC

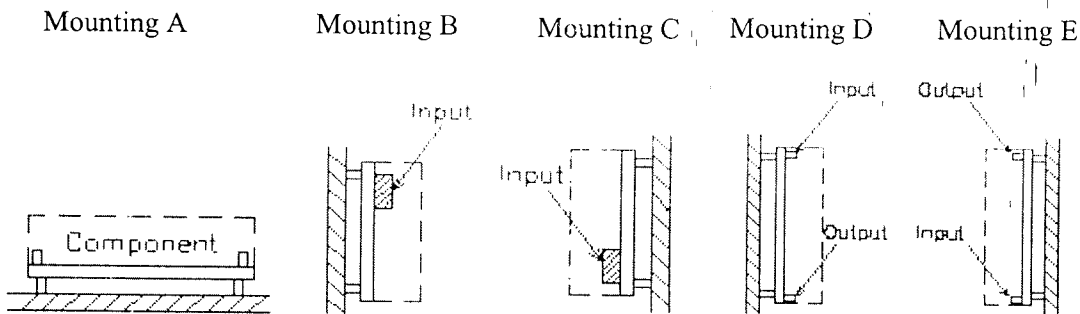
LOAD %	Life time (years)				
	Ta (°C)				
	25.0	30.0	40.0	50.0	60.0
10	13.1	9.3	4.6	2.3	1.2
20	12.5	8.8	4.4	2.2	1.1
50	12.0	8.5	4.2	2.1	1.1
70	11.9	8.4	4.2	2.1	1.1
90	8.9	6.3	3.1	1.6	—
100	8.0	5.7	2.8	1.4	—

$$L = L_o \cdot 2^{(105 - T_c) / 10} \quad (\text{Yrs})$$

- L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)
- L_o : Guarantee life for Elec. Capacitor
- T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C -○- Ta=30°C -X- Ta=40°C -△- Ta=50°C -◇- Ta=60°C -□-



4. ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : VS15C - 5

Mounting D
Input : 100VAC

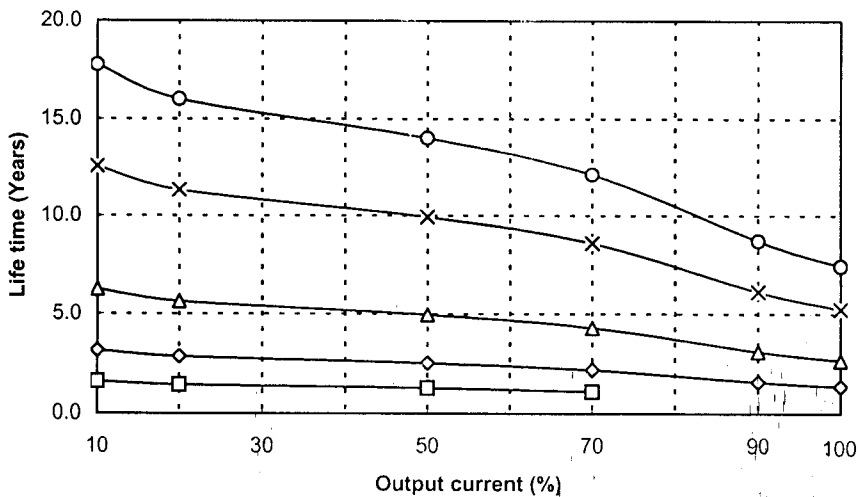
LOAD %	Life time (years)				
	Ta (°C)				
	25.0	30.0	40.0	50.0	60.0
10	17.7	12.6	6.3	3.1	1.6
20	16.0	11.3	5.7	2.8	1.4
50	14.0	9.9	5.0	2.5	1.2
70	12.1	8.6	4.3	2.1	1.1
90	8.7	6.1	3.1	1.5	—
100	7.4	5.2	2.6	1.3	—

$$L = L_0 \cdot 2^{(105 - T_c) / 10} \quad (\text{Yrs})$$

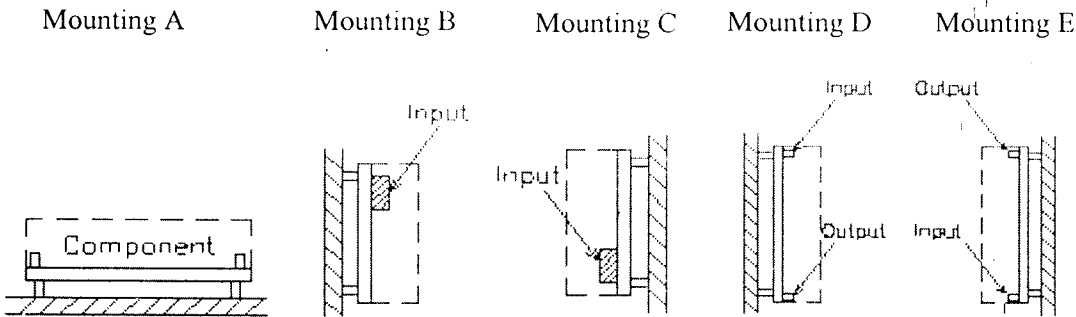
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year,)

L₀ : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C —○— Ta=30°C —×— Ta=40°C —△— Ta=50°C —◇— Ta=60°C —□—



4. ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

MODEL : VS15C - 5

Mounting E

Input : 100VAC

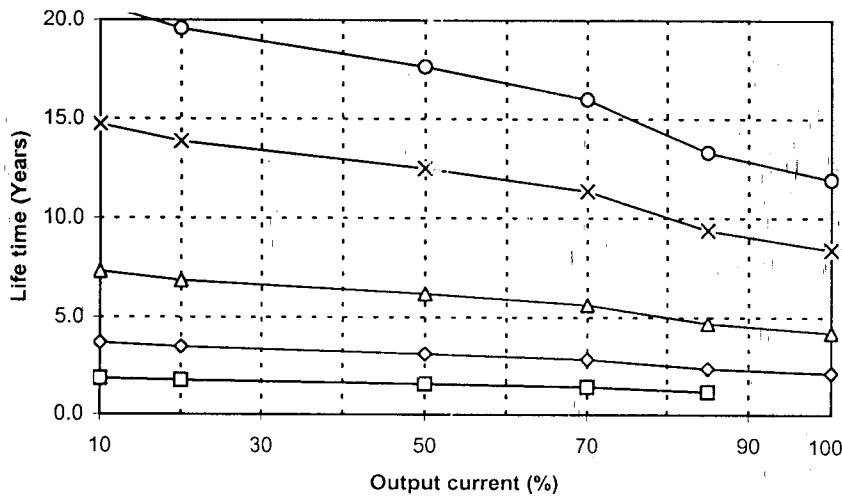
LOAD %	Life time (years)				
	Ta (°C)				
	25.0	30.0	40.0	50.0	60.0
10	20.8	14.7	7.4	3.7	1.8
20	19.6	13.8	6.9	3.5	1.7
50	17.6	12.5	6.2	3.1	1.6
70	16.0	11.3	5.7	2.8	1.4
85	13.3	9.4	4.7	2.3	1.2
100	11.9	8.4	4.2	2.1	—

$$L = L_o \cdot 2^{(105 - T_c) / 10} \quad (\text{Yrs})$$

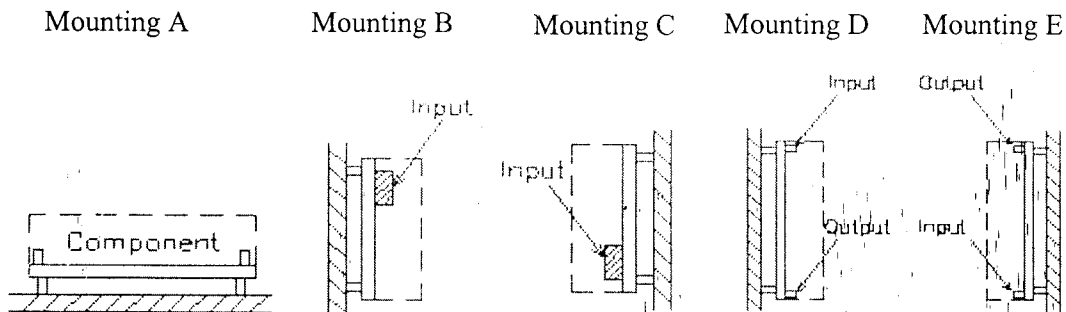
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L_o : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C —○— Ta=30°C —X— Ta=40°C —Δ— Ta=50°C —◇— Ta=60°C —□—



5. ABNORMAL TEST

MODEL: VS15C-5

(1) Test Conditions

Input : 132VAC

Output : 5V / 3A

Ta : 25°C , 70%RH

(2) Test Results

No.	Test position		Test Mode		Test Results												Note
	Location No.	Test Point	S H O R T	O P E N	1	2	3	4	5	6	7	8	9	10	11	12	
					F I R E	S M O K E	B U R S T	S M E L	Red Hot	D A M A G E	Fuse Blown	O V P	O C P	No Out - put	No Change	O T H E R S	
1	Q1	D-G	Y								Y			Y			Q1,ZD2 SHORTED
2		D-S	Y								Y			Y			
3		G-S	Y											Y			
4		D		Y										Y			
5		S		Y										Y			
6		G		Y							Y			Y			Q1 SHORTED
7	Q2	C-E	Y											Y			
8		B-C	Y											Y			
9		B-E	Y								Y			Y			Q1 SHORTED
10		E		Y							Y			Y			Q1 SHORTED
11		C		Y							Y			Y			Q1 SHORTED
12		B		Y							Y			Y			Q1 SHORTED
13	A1	K-A	Y													Y	O/P LOW
14		K-R	Y													Y	O/P LOW
15		R-A	Y									Y		Y			ZD4 SHORTED
16		K		Y								Y		Y			ZD4 SHORTED
17		A		Y								Y		Y			ZD4 SHORTED
18		R		Y								Y		Y			ZD4 SHORTED
19	PC1	1-2	Y									Y		Y			ZD4 SHORTED
20		3-4	Y											Y			
21		1		Y								Y		Y			ZD4 SHORTED
22		2		Y								Y		Y			ZD4 SHORTED
23		3		Y								Y		Y			ZD4 SHORTED
24		4		Y								Y		Y			ZD4 SHORTED
25	D1		Y								Y			Y			
26				Y										Y			
27	D2		Y								Y			Y			Q1 SHORTED
28				Y										Y			
29	D3		Y													Y	O/P LOW
30				Y								Y		Y			ZD4 SHORTED

No.	Test position		Test Mode		Test Results												Note
	Location No.	Test Point	S H O R T	O P E N	1 F I R E	2 S M O K E	3 B U R S T	4 S M E L L	5 R e d H o t	6 D A M A G E	7 F u s e B l o w n	8 O V E R P	9 O C C U r e n c e	10 N o O u t - p u t	11 N o C h a n g e	12 O T H E R S	
31	ZD1		Y												Y		
32				Y											Y		
33	ZD2		Y											Y			
34				Y											Y		
35	ZD3		Y													Y	O/P LOW
36				Y											Y		
37	ZD4		Y											Y			
38				Y											Y		
39	T1	1-2	Y								Y			Y			
40		2-3	Y								Y			Y			Q1 SHORTED
41		6-7	Y												Y		
42		7-8	Y												Y		
43		8-9	Y											Y			
44		9-10	Y												Y		
45		1		Y										Y			
46		2		Y										Y			
47		3		Y										Y			
48		5		Y										Y			
49		6		Y											Y		
50		7		Y											Y		
51		8		Y											Y		
52		9		Y											Y		
53		10		Y											Y		
54																	
55																	
56																	
57																	
58																	
59																	
60																	
61																	
62																	
63																	
64																	
65																	
66																	
67																	
68																	
69																	
70																	

6. VIBRATION TEST

MODEL : VS15C-5

(1) Vibration test class

Frequency variable endurance test

(2) Equipment used

EMIC CORP Controller : F-400-BM-DCS-7800 Vibrator 905-FN

(3) Test Conditions

Sweep frequency 10 ~ 55Hz

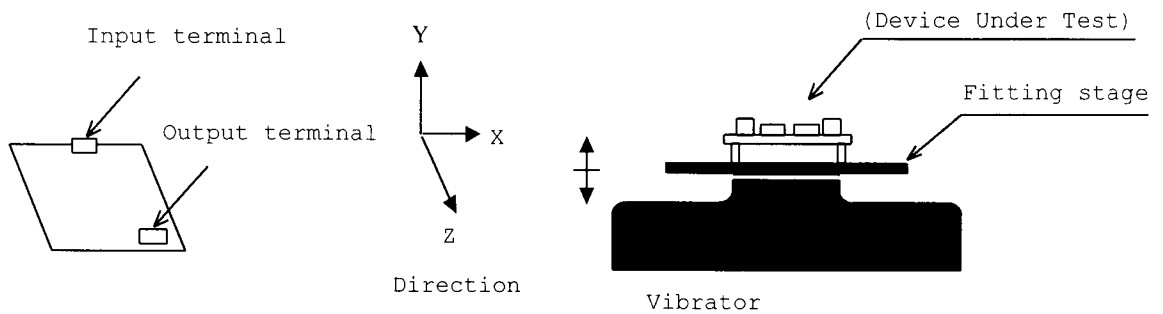
Sweep time 1 minute

Acceleration Constant (2G)

Direction X, Y, Z.

Test time 1 hour each

(4) Test method



(5) Test Results

OK

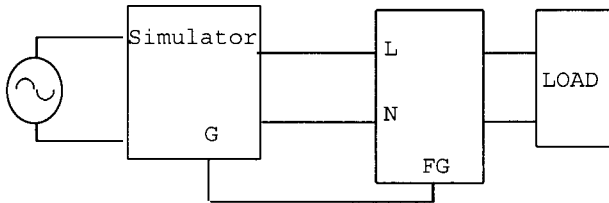
Check item	Output voltage (V)	Ripple voltage(mVp-p)	Visual Check
Before Test	5.016	8	O.K.
After Test	X	5.020	O.K.
	Y	5.022	O.K.
	Z	5.021	O.K.

Check conditions: Vin: 100Vac, Iout: 100%

7. NOISE SIMULATE TEST

MODEL : VS15C-5

(1) Test circuit and equipment



Simulator : INS-400L Noise Laboratory Co.,LTD

(2) Test Conditions

- | | | | |
|-----------------------|-----------------|---------------|--------------------|
| * Input voltage | : 100VAC | * Noise level | : 0V~2KV |
| * Output voltage | : Rated | * Phase shift | : 0° ~ 360° |
| * Output current | : 0% , 100% | * Polarity | : + , - |
| * Ambient temperature | : 25°C | * Mode | : Normal
Common |
| * Pulse width | : 50ns ~ 1000ns | * Trig select | : Line |

(3) Acceptable conditions

1. Not to be broken
2. Not to be shut down output
3. No other out of orders

(4) Test Result

OK

ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

FORCED AIR COOLING

(air velocity: 0.7m/s, measured at component side of power supply, air must flow through component side.)

MODEL : VS15C - 5

Mounting A

Input : 100VAC

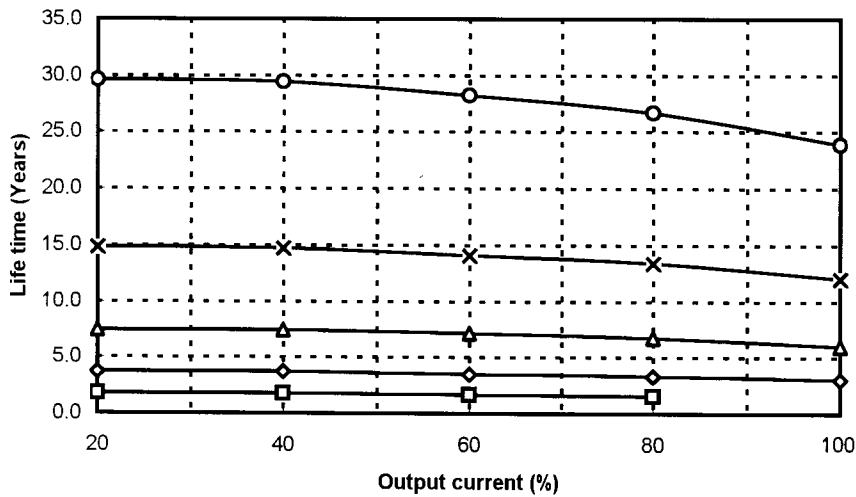
LOAD %	Life time (years)				
	Ta (° C)				
	30.0	40.0	50.0	60.0	70.0
20	29.6	14.8	7.4	3.7	1.8
40	29.4	14.7	7.4	3.7	1.8
60	28.2	14.1	7.1	3.5	1.7
80	26.7	13.4	6.7	3.3	1.6
100	23.9	12.0	6.0	3.0	----

$$L = L_0 * 2^{(105 - T_c) / 10} / (24 * 365) \quad (\text{Yrs})$$

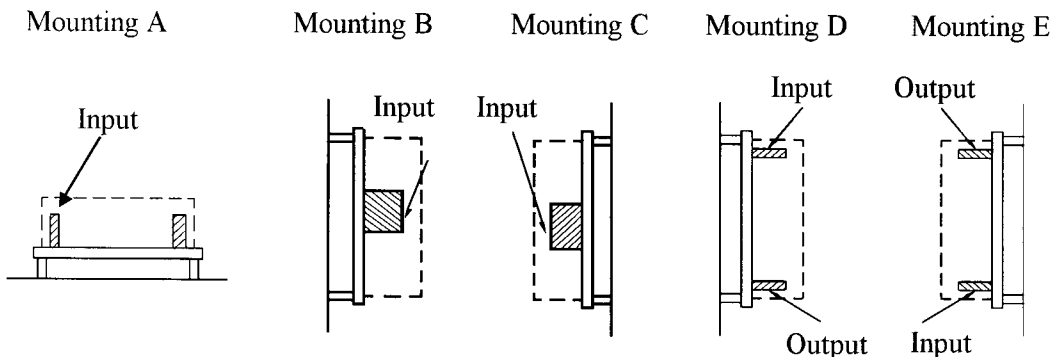
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L₀ : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C -O- Ta=30°C -X- Ta=40°C -△- Ta=50°C -◇- Ta=60°C -□-



ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

FORCED AIR COOLING

(air velocity: 0.7m/s, measured at component side of power supply, air must flow through component side.)

MODEL : VS15C - 5

Mounting B

Input : 100VAC

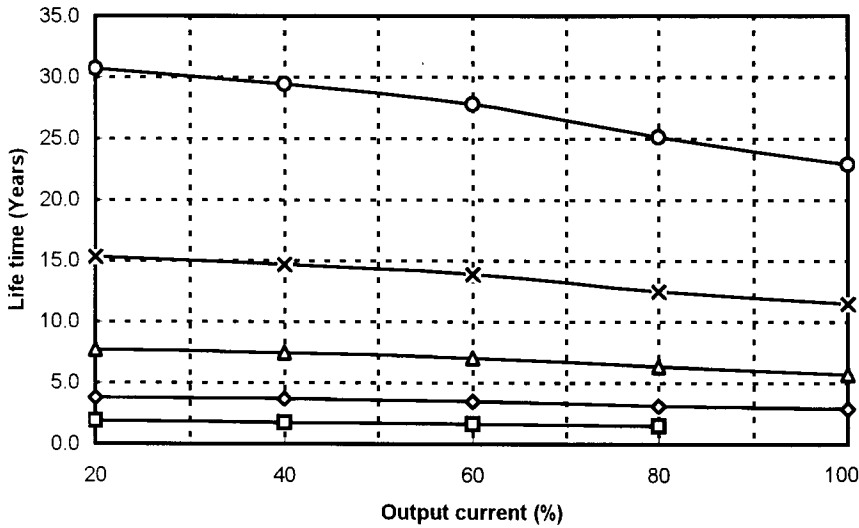
LOAD %	Life time (years)				
	Ta (° C)				
	30.0	40.0	50.0	60.0	70.0
20	30.7	15.3	7.7	3.8	1.9
40	29.4	14.7	7.4	3.7	1.8
60	27.8	13.9	7.0	3.5	1.7
80	25.1	12.5	6.3	3.1	1.5
100	22.9	11.5	5.7	2.9	---

$$L = L_o * 2^{(105 - T_c) / 10} / (24 * 365) \quad (\text{Yrs})$$

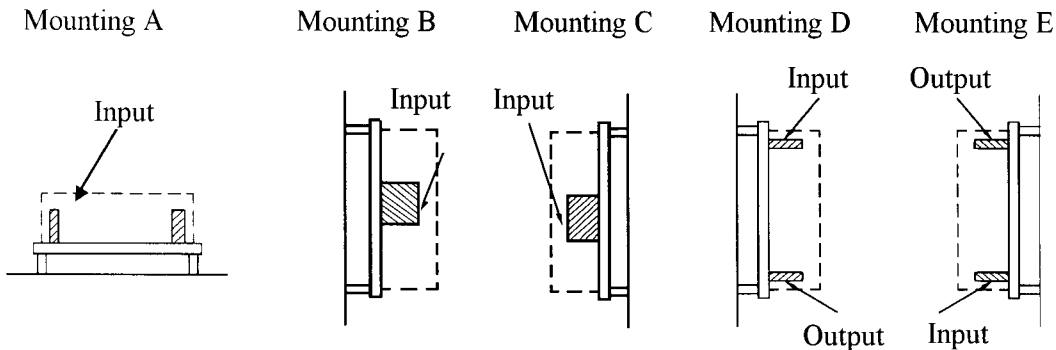
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L_o : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C —○— Ta=30°C —×— Ta=40°C —△— Ta=50°C —◇— Ta=60°C —□—



ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

FORCED AIR COOLING

(air velocity: 0.7m/s, measured at component side of power supply, air must flow through component side.)

MODEL : VS15C - 5

Mounting C

Input : 100VAC

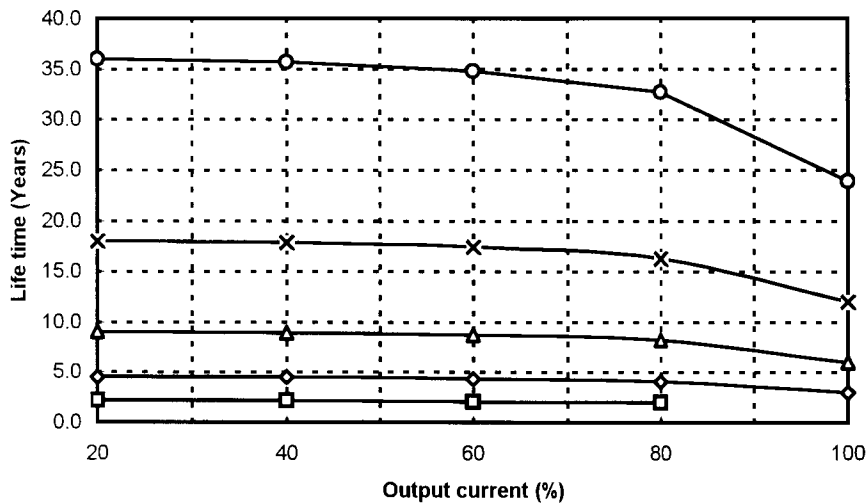
LOAD %	Life time (years)				
	Ta (° C)				
	30.0	40.0	50.0	60.0	70.0
20	36.0	18.0	9.0	4.5	2.2
40	35.7	17.9	8.9	4.5	2.2
60	34.8	17.4	8.7	4.3	2.1
80	32.7	16.3	8.2	4.1	2.0
100	23.9	12.0	6.0	3.0	----

$$L = L_o * 2^{(105 - T_c) / 10} / (24 * 365) \quad (\text{Yrs})$$

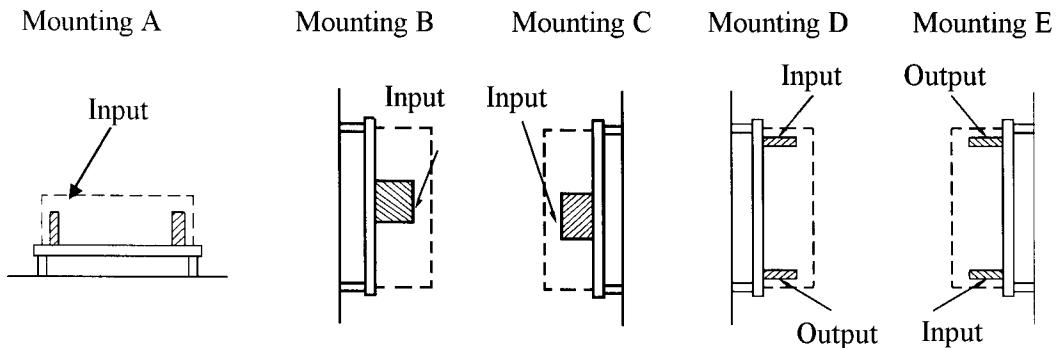
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L_o : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C —○— Ta=30°C —×— Ta=40°C —△— Ta=50°C —◇— Ta=60°C —□—



ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

FORCED AIR COOLING

(air velocity: 0.7m/s, measured at component side of power supply, air must flow through component side.)

MODEL : VS15C - 5

Mounting D

Input : 100VAC

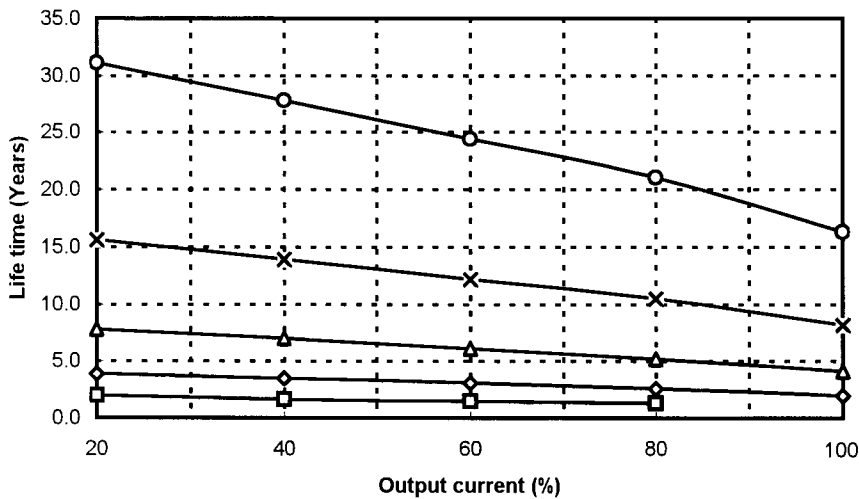
LOAD %	Life time (years)				
	Ta (° C)				
	30.0	40.0	50.0	60.0	70.0
20	31.1	15.6	7.8	3.9	2.0
40	27.8	13.9	7.0	3.5	1.7
60	24.4	12.2	6.1	3.1	1.5
80	21.0	10.5	5.2	2.6	1.3
100	16.3	8.2	4.1	2.0	----

$$L = L_o * 2^{(105 - T_c) / 10} / (24 * 365) \quad (\text{Yrs})$$

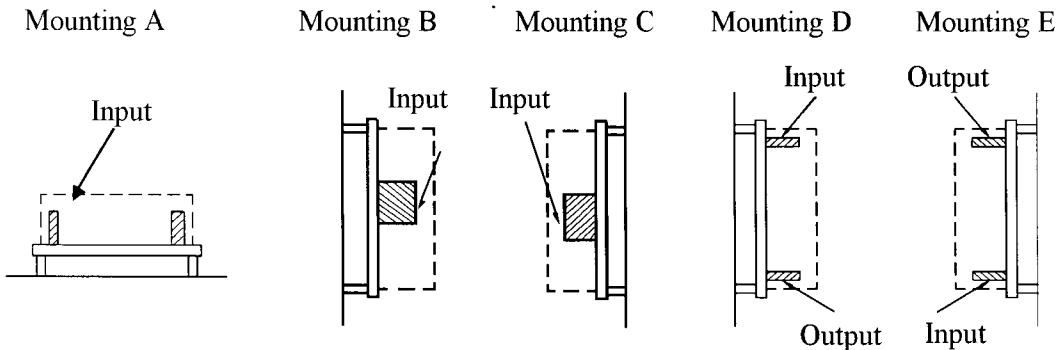
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L_o : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C —○— Ta=30°C —×— Ta=40°C —△— Ta=50°C —◇— Ta=60°C —□—



ELECTROLYTIC CAPACITOR LIFETIME VERSUS LOAD

FORCED AIR COOLING

(air velocity: 0.7m/s, measured at component side of power supply, air must flow through component side.)

MODEL : VS15C - 5

Mounting E

Input : 100VAC

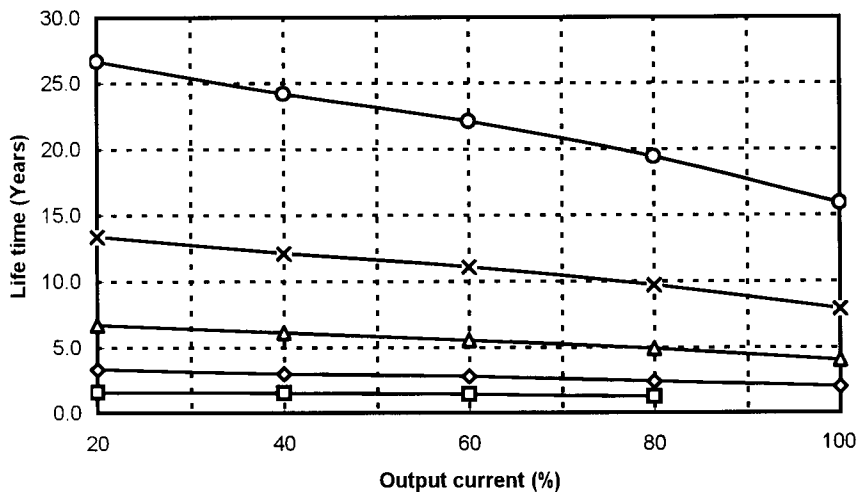
LOAD %	Life time (years)				
	Ta (° C)				
	30.0	40.0	50.0	60.0	70.0
20	26.7	13.4	6.7	3.3	1.6
40	24.2	12.1	6.1	3.0	1.5
60	22.1	11.1	5.5	2.8	1.4
80	19.4	9.7	4.9	2.4	1.2
100	15.9	7.9	4.0	2.0	----

$$L = L_o * 2^{(105 - T_c) / 10} / (24 * 365) \quad (\text{Yrs})$$

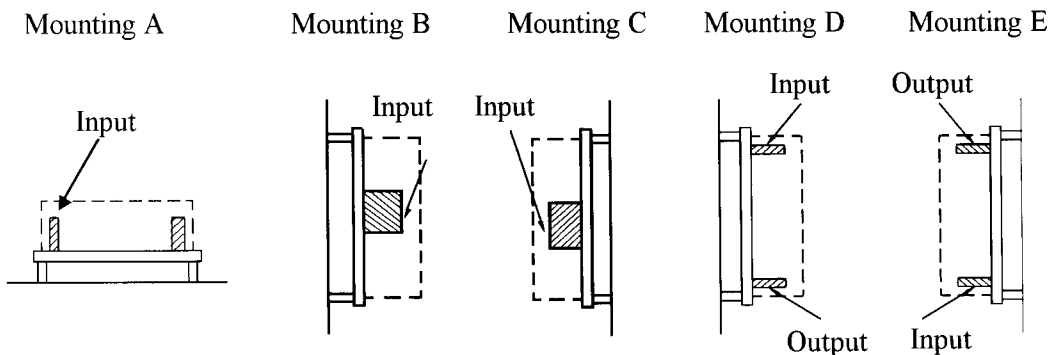
L : Elec. Capacitor computed life (24 Hrs / day , 365 days / year)

L_o : Guarantee life for Elec. Capacitor

T_c(ΔT+T_a) : Case temperature of Elec. Capacitor



Ta=25°C -○- Ta=30°C -X- Ta=40°C -△- Ta=50°C -◇- Ta=60°C -□-



DENSEI-LAMBDA