

MAXETA iFB SERIES DC-DC POWER MODULES 48V Input, 9.6V/58A/557W, Full Brick

The Maxeta™ iFB Series power modules are ideally suited for extremely high-density distributed power architectures where the demands of voltage and substantial load mandate the creation of a robust local intermediate 9.6Vdc distribution bus. With a typical 92% full load efficiency (93% at 70% load), a power density >100W per cubic inch and a total power and current output capability of 557W and 58A respectively, Maxeta™ iFB Series offers the highest efficiency, power density and usable output power in a full brick package currently available. A wide output voltage trim range, -40% to +10%, remote sensing, and isolated remote on/off control are standard features enhancing versatility. An optional droop load current share feature is also available upon request.

Standard Features:

- RoHS 6/6 Complaint
- Industry Standard 557W full brick
- Fully regulated 9.6V bus voltage
- Power density: > 100 W/cubic inch
- High efficiency: up to 95%
- Full load efficiency: 92% at nominal input
- 70% load efficiency: 93% at nominal input
- Up to 556W of output power
- Base-plate for improved thermal performance
- Single board design with high usable power 42A at 65C, 200LFM (with base-plate) 36A at 65C, 200LFM (No base-plate)
- Meets basic insulation requirements
- Start-up into pre-biased output bus
- Voltage foldback constant current limit
- Isolated remote on/off control
- Wide output voltage adjustment range
- Auto-recovery protection for input UV
- Auto-recovery over-load protection

- Auto-recovery over-temperature protection
- Latched output over-voltage protection
- Constant switching frequency
- UL 60950 (US and Canada), VDE 0805, CB scheme (IEC950)
- CE Mark (EN60950)
- EMI: Class A or B with external EMI filters
- Multiple patents.
- ISO Certified manufacturing facilities

Optional Features:

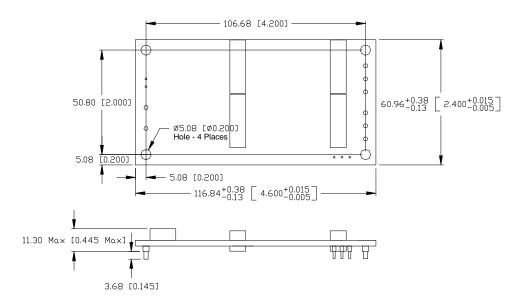
- Droop Load Current Share
- Latched fault protections
- No base-plate option

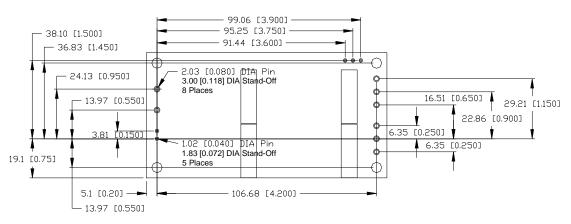


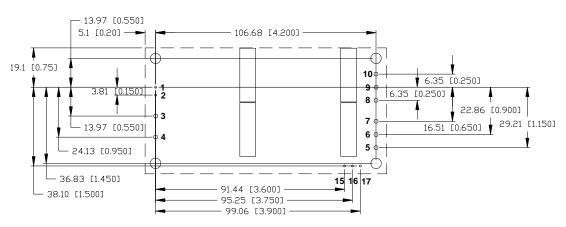
Data Sheet: Maxeta™ iFB Series	S
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Mechanical Specification (open frame without base-plate):

Dimensions are in mm [in]. Unless otherwise specified, tolerances are: x.x ± 0.5 [0.02], x.xx and x.xxx ± 0.25 [0.010].



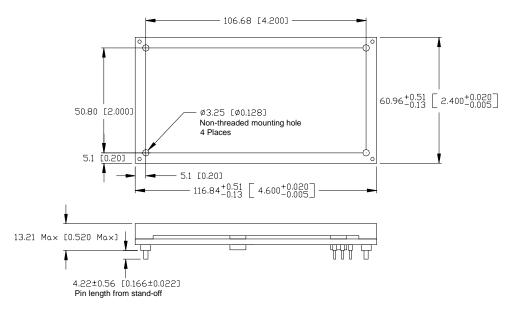


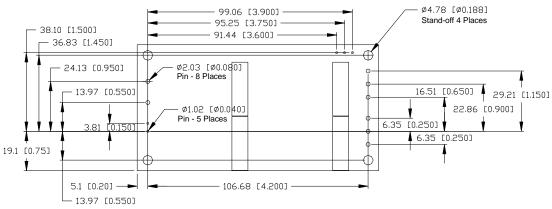


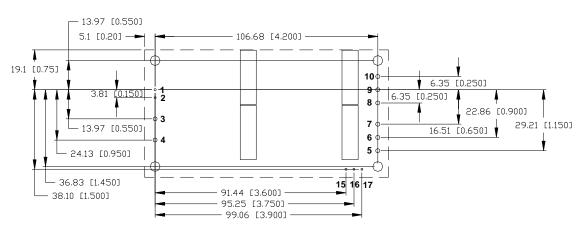
Recommended hole pattern (top view)

Mechanical Specification (with base-plate):

Dimensions are in mm [in]. Unless otherwise specified, tolerances are: x.x ± 0.5 [0.02], x.xx and x.xxx ± 0.25 [0.010].







Recommended hole pattern (top view)



Pin Assignment:

PIN	FUNCTION	PIN	FUNCTION	PIN	FUNCTION
1	ON/OFF (+)	7	Vout (-)	13	Not Available
2	ON/OFF (-)	8	Vout (+)	14	Not Available
3	Vin (+)	9	Vout (+)	15	TRIM
4	Vin (-)	10	Vout (+)	16	SENSE (+)
5	Vout (-)	11	Not Available	17	SENSE (-)
6	Vout (-)	12	Not Available	18	

^{*} Pin base material is brass or copper with gold plating. The maximum module weight with base-plate is 225g (116g open frame).



Absolute Maximum Ratings:

Stress in excess of Absolute Maximum Ratings may cause permanent damage to the device.

Characteristic	Min	Max	Unit	Notes & Conditions
Continuous Input Voltage	-0.5	80	Vdc	
Transient Input Voltage		100	Vdc	100mS max.
Isolation Voltage Input to Output Input to Base-plate Output to Base-plate	 	1500 1500 500	Vdc Vdc Vdc	Basic Insulation Basic Insulation Operational Insulation
Storage Temperature	-55	125	°C	
Operating Temperature Range (Tc)	-40	125	°C	Measured at the location specified in the thermal measurement figure; maximum temperature varies with output current and module orientation – see curve in the thermal performance section of the data sheet.

Input Characteristics:

Unless otherwise specified, specifications apply over all rated input voltage, resistive load, and temperature conditions.

Characteristic	Min	Тур	Max	Unit	Notes & Conditions
Operating Input Voltage	36	48	75	Vdc	When $36V \le Vin < 37V$, the modules will continue to operate, but the output voltage regulation may be out of spec at load $\ge 90\%$ of full load
Maximum Input Current			18.7*	Α	Vin = 0 to Vin,max
Input Low End Turn-on Voltage		34.4	36	Vdc	
Input Low End Turn-off Voltage		32.5		Vdc	
Hysteresis		1.9		Vdc	
Startup Delay Time from application of input voltage		10		mS	Vo = 0 to 0.1*Vo,nom; on/off =on, lo=lo,max, Tc=25°C
Startup Delay Time from on/off		3		mS	Vin = Vin,nom, Io=Io,max, Tc=25°C
Output Voltage Rise Time		30		mS	lo=lo,max, Vo=0.1 to 0.9*Vo,nom, Tc=25°C
Inrush Transient			1	A ² S	
Input Reflected Ripple		6		mApp	Vin=Vin,nom, Io=Io,max (0 to 20MHz) See input/output ripple measurement figure; BW = 20 MHz
Input Ripple Rejection		50		dB	@120Hz

^{*} Engineering Estimate

Caution: The power modules are not internally fused. An external input line fuse with a maximum value of 20A is required. See the Safety Considerations section of the data sheet.



Electrical Data:

iFB48058A096V- 000 through – 1xx: 9.6V, 58A Output

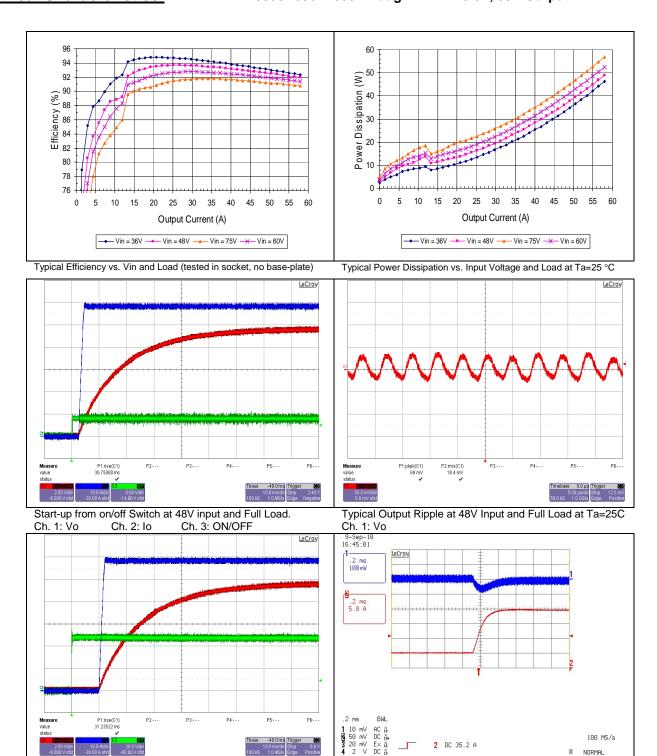
Characteristic	Min	Тур	Max	Unit	Notes & Conditions
Output Voltage Initial Set-point	9.38	9.6	9.82	Vdc	Vin=Vin,nom; Io=Io,max; Tc = 25°C
Output Voltage Tolerance	9.31		9.89	Vdc	Over all rated input voltage, load, and temperature conditions to end of life
Efficiency		92		%	Vin=Vin,nom; lo=lo,max; Tc = 25°C
Efficiency at 70% Load		93		%	Vin=Vin,nom; Io=70% of Io,max; Tc = 25°C
Line Regulation		3	10	mV	Vin=Vin,min to Vin,max, Io and Tc fixed
Load Regulation		2	10	mV	Io=Io,min to Io,max, Vin and Tc fixed
Temperature Regulation		40	100*	mV	Tc=Tc,min to Tc,max, Vin and Io fixed
Output Current	0		58	А	At loads less than lo,min the module will continue to regulate the output voltage, but the output ripple may increase
Output Current Limiting Threshold		64		А	Vo = 0.9*Vo,nom, Tc <tc,max, tc="25°C</td"></tc,max,>
Short Circuit Current		2		А	Vo = 0.25V, Tc = 25°C
Output Ripple and Noise Voltage		70	140*	mVpp	Vin=48V, lo≥lo,min, Tc = 25°C, with a 0.1μF, a 10μF ceramic, two 470μF low esr aluminum
		18.5		mVrms	capacitors located 2 inch away. See input & output ripple measurement figure; BW = 20MHz
Output Voltage Adjustment Range	60		110	%Vo,nom	Po≤Po,max, refer to "Output Voltage Adjustment" figure for Vin,min requirement
Remote Output Voltage Sense Range				Vdc	
Dynamic Response: Settling Time to 10% Peak Deviation		200		μS	di/dt = 0.1A/uS, Vin=Vin,nom; Tc = 25°C, load step from 50% to 75% of lo,max. With at least a 10uF ceramic capacitor and two
Peak Voltage Deviation		80		mV	470uF low esr aluminum or tantalum capacitor across the output terminals.
Output Voltage Overshoot during Startup	0	0		mV	lo=lo,max,Tc=25°C
Switching Frequency		110		kHz	Fixed
Output Over Voltage Protection	11.1	11.7	12.5	V	
External Load Capacitance	950		10,000 **	uF	Cext,min required for the 100% load dump. Minimum ESR $> 2m\Omega$
Isolation Capacitance		1000		pF	
Isolation Resistance	15			ΜΩ	
Load Share Accuracy				%	50% to 100% rated load current
Power Good Pin Max Applied Voltage	N/A	N/A	N/A	Vdc	
Auxiliary Output Voltage	N/A	N/A	N/A	Vdc	

^{*} Engineering Estimate

^{**} Contact TDK - Lambda Americas for applications that require additional capacitance or capacitors with very low esr

Electrical Characteristics:

iFB48058A096V- 000 through - 1xx: 9.6V, 58A Output

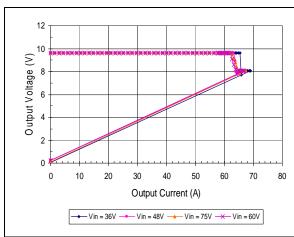


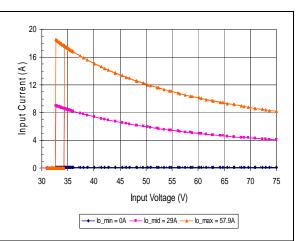
Start-up from Input Voltage Application at Full Load. Ch. 1: Vo Ch. 2: Io Ch. 3: Vin

Load Transient Response. Load step from 50% to 75% of full load. Di/dt=0.1A/us. Ch. 1: Vo Ch.2: lo

Electrical Characteristics:

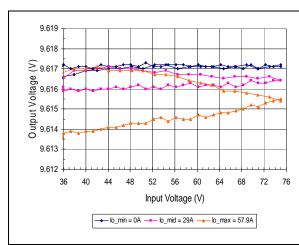
iFB48058A096V- 000 through - 1xx: 9.6V, 58A Output

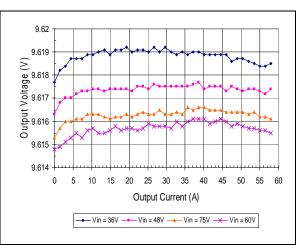




Output Current Limit Characteristics vs. Input Voltage

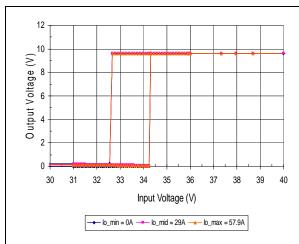
Typical Input Current vs. Input Voltage Characteristics

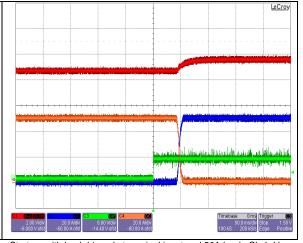




Typical Output Voltage vs. Input Voltage (Line Regulation)

Typical Output Voltage vs. Load Current (Load Regulation)



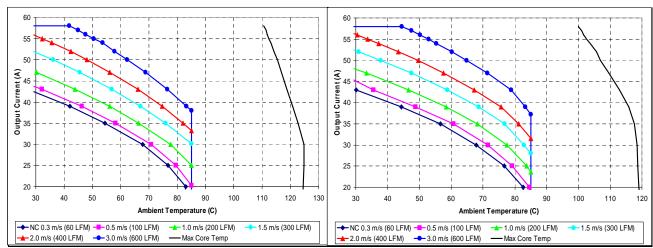


Typical Output Voltage vs. Input Voltage During Start-up

Start-up with back-biased at nominal input and 50A load. Ch.1: Vo Ch.2: lo2 (starting) Ch. 3: ON/OFF Ch. 4: lo1 (running)

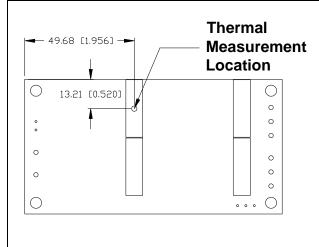
Thermal Performance:

iFB48058A096V- 000 through - 1xx: 9.6V, 58A Output



Maximum output current vs. ambient temperature at nominal input voltage **with no base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 4 to pin 1.

Maximum output current vs. ambient temperature at nominal input voltage **with no base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 1 to pin 4.

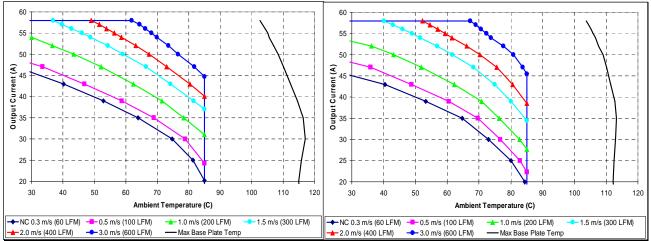


Thermal measurement location - top view

The thermal curves provided are based upon measurements made in TDK - Lambda Americas' experimental test setup that is described in the Thermal Management section. Due to the large number of variables in system design, TDK - Lambda Americas recommends that the user verify the module's thermal performance in the end application. The critical component should be thermo- coupled and monitored, and should not exceed the temperature limit specified in the derating curve above. It is critical that the thermocouple be mounted in a manner that gives direct thermal contact otherwise significant measurement errors may result.

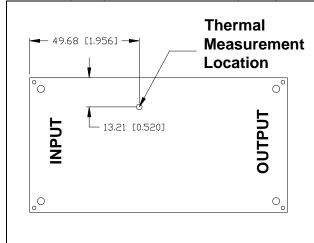
Thermal Performance:

iFB48058A096V- 000 through - 1xx: 9.6V, 58A Output



Maximum output current vs. ambient temperature at nominal input voltage **with base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 4 to pin 1.

Maximum output current vs. ambient temperature at nominal input voltage **with base plate** for airflow rates natural convection (0.3m/s) to 3.0m/s with airflow from pin 1 to pin 4.



Thermal measurement location - top view

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Thermal Management:

An important part of the overall system design process is thermal management; thermal design must be considered at all levels to ensure good reliability and lifetime of the final system. Superior thermal design and ability to operate in severe application environments are key elements of a robust, reliable power module.

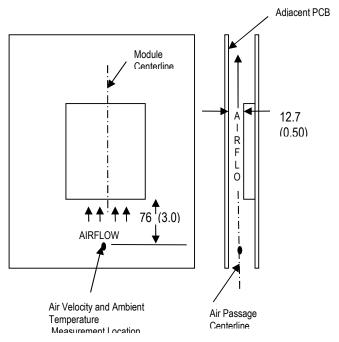
A finite amount of heat must be dissipated from the power module to the surrounding environment. This heat is transferred by the three modes of heat transfer: convection, conduction and radiation. While all three modes of heat transfer are present in every application, convection is the dominant mode of heat transfer in most applications. However, to ensure adequate cooling and proper operation, all three modes should be considered in a final system configuration.

The open frame design of the power module provides an air path to individual components. This air path improves convection cooling to the surrounding environment, which reduces areas of heat concentration and resulting hot spots.

Test Setup: The thermal performance data of the power module is based upon measurements obtained from a wind tunnel test with the setup shown in the wind tunnel figure. This thermal test setup replicates the typical thermal environments encountered in most modern electronic systems with distributed power architectures. The electronic equipment in networking, telecom, wireless, and advanced computer systems operates in similar environments and utilizes vertically mounted printed circuit boards (PCBs) or circuit cards in cabinet racks.

The power module is mounted on a 0.087 inch thick, 12-layer, 2oz/layer PCB and is vertically oriented within the wind tunnel. Power is routed on the internal layers of the PCB. The outer copper layers are thermally decoupled from the converter to better simulate the customer's application. This also results in a more conservative derating.

The cross section of the airflow passage is rectangular with the spacing between the top of the module and a parallel facing PCB kept at a constant (0.5 in). The power module's orientation with respect to the airflow direction can have a significant impact on the unit's thermal performance.



Wind Tunnel Test Setup Figure
Dimensions are in millimeters and (inches).

Thermal Derating: For proper application of the power module in a given thermal environment, output current derating curves are provided as a design guideline in the Thermal Performance section for the power module of interest. The module temperature should be measured in the final system configuration to ensure proper thermal management of the power module. For thermal performance verification, the module temperature should be measured at the location indicated in the thermal measurement location figure in the Thermal Performance section for the power module of interest. In all conditions, the power module should be operated below the maximum operating temperature shown on the derating curve. For improved design margins and enhanced system reliability, the power module may be operated at temperatures below the maximum rated operating temperature.



Heat transfer by convection can be enhanced by increasing the airflow rate that the power module experiences. The maximum output current of the power module is a function of ambient temperature (T_{AMB}) and airflow rate as shown in the thermal performance figures on the thermal performance page for the power module of interest. The curves in the figures are shown for natural convection through 3 m/s (600 ft/min). The data for the natural convection condition has been collected at 0.3 m/s (60 ft/min) of airflow, which is the typical airflow generated by other heat dissipating components in many of the systems that these types of modules are used in. In the final system configurations, the airflow rate for the natural convection condition can vary due to temperature gradients from other heat dissipating components.

Heatsink Usage: For applications with demanding environmental requirements, such as higher ambient temperatures or higher power dissipation, the thermal performance of the power module can be improved by attaching a heatsink or cold plate. The iFB platform is designed with a base plate with four through-hole mounting studs for attaching a heatsink or cold plate. The addition of a heatsink can reduce the airflow requirement, ensure consistent operation and extended reliability of the system. With improved thermal performance, more power can be delivered at a given environmental condition.

Standard heatsink kits are available from TDK - Lambda Americas for vertical module mounting in two different orientations (longitudinal – perpendicular to the direction of the pins and transverse – parallel to the direction of the pins). The heatsink kit contains four M3 x 0.5 steel mounting screws and a precut thermal interface pad for improved thermal resistance between the power module and the heatsink.

During heatsink assembly, the base-plate to heatsink interface must be carefully managed. A thermal pad may be required to reduce mechanical-assembly-related stresses and improve the thermal connection. Please contact TDK - Lambda Americas' engineering for recommendation on this subject.

The system designer must use an accurate estimate or actual measure of the internal airflow rate and temperature when doing the heatsink thermal analysis. For each application, a review of the heatsink fin orientation should be completed to verify proper fin alignment with airflow direction to maximize the heatsink effectiveness. For TDK - Lambda Americas' standard heatsinks, contact TDK - Lambda Americas for latest performance data.

Operating Information:

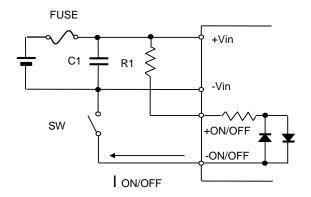
Input Under-voltage Lockout: The power modules also feature an input under voltage lockout circuitry that ensures that the power module is off at low input voltage levels. The power module will operate normally when the input voltage returns to the specified range.

Output Over-Current Protection (OCP): The power modules have current limit protection to protect the module during output overload and short circuit conditions. During overload conditions, the power modules may protect themselves by entering a hiccup current limit mode. The modules will operate normally once the output current returns to the specified operating range. A latched over-current protection option is also available. Consult the TDK - Lambda Americas' technical support for details.

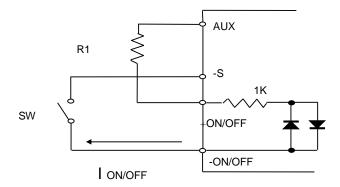
Output Over-Voltage Protection: The power modules have a control circuit, independent of the main control loop, that reduces the risk of over voltage appearing at the output of the power module during a fault condition. If there is a fault in the voltage regulation loop, the over voltage protection circuitry will cause the power module to shut down. The module will try to auto re-start in about 1 sec time period. An optional feature with latched OVP protection can also be offered. Consult the TDK - Lambda Americas' technical support for details.

Thermal Protection: When the power modules exceed the maximum operating temperature, the modules may turn-off to safeguard the power unit against thermal damage. The module will auto restart as the module temperature is cooled below the over temperature protection threshold. A latched over-temperature protection option is also available. Consult the TDK - Lambda Americas' technical support for details.

Remote On/Off: The iFB series power modules have two remote on/off pins, which are isolated from the secondary (or output) side. To control the power module from the input side, the user must supply a switch between the Vin(-) terminal and the -ON/OFF terminal of ON/OFF pins. A 30KΩ external resistor with 0.5W power rating is recommended to connect between the Vin(+) terminal and the +ON/OFF terminal. The maximum allowable leakage current of the switch is 50uA. The maximum current sinking capability of the ON/OFF terminal is 5mA or less. The current required to maintain the module ON status must be greater than 1mA.

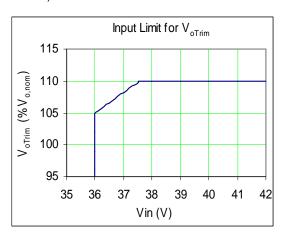


ON/OFF Control from Input Side



ON/OFF Control from Output Side

Output Voltage Adjustment: The output voltage of the power module is adjustable by the user using an external resistor or by applying external voltage. However, when the output voltage is increased, the input voltage range is limited as shown in the following figure (*Note: It only applies to 12V output modules*).



To trim the output voltage up, a fixed or variable resistor, Rext1, shall be connected between the Vout(+) pin and the sense(+) pin while the Vout(-) pin and the sense(-) pin should be shorted by a jumper wire as shown below. The trim pin should be left open. The output voltage trim-up rate is approximately 1V / $K\Omega$. To trim the voltage up to 10.56V, a 0.96K Ω external resistor should be used.

Rup =
$$1003.3035 \times (Vo d - 9.5756)$$
 (K Ω)

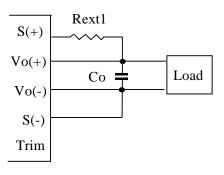


Figure Trim up Connection

If the output voltage adjustment feature is not used, the Vout(+) pin should be shorted to the sense(+) pin and the Vout(-) pin should be shorted to the sense(-) pin by jumper wires.

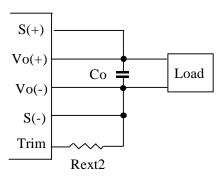
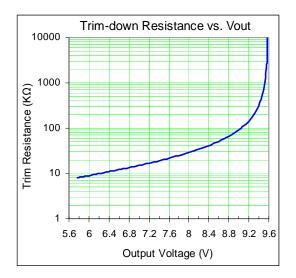


Figure Trim Down Connection

To trim the output voltage down, a fixed or variable resistor, Rext2, shall be connected between the trim pin and the sense(-) pin while the Vout(-) pin and the sense(-) pin are shorted by a jumper wire. The Vout(+) pin and the sense(+) pin should also be shorted by a jumper wire as shown below. The resistor, Rext2 can be chosen according to the following equation:

$$R_{ext2} = \left[\frac{V_{odown} - 1.31187}{1.40185 - 0.146398V_{odown}}\right]$$



In order to trim the output voltage from the minimum value (-40% down) to the maximum value (+10% up) in a linear fashion, a fixed resistor, Rext, should be connected between the trim pin and the sense(-) pin while a 11.8K Ω variable resistor, Rv, shall be connected between the Vout(+) pin and the sense(+) pin as shown below. When Rext=5.11K Ω and Rv=1.69K Ω , the output voltage trim rate is changed to approximately 0.5V / K Ω starting from 5.7V. The resistor, Rv, should be chosen according to the following equation:

$$Rv \cong 1.984 \times (Vo_d - 4.84833)$$
 (K\O)

where Vo_d is the desired output voltage.

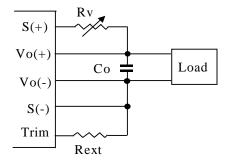
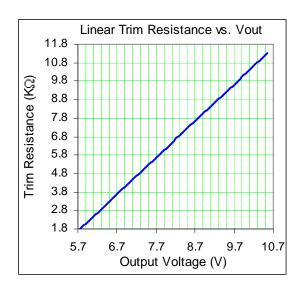


Figure Linear Trim Connection



The output voltage can also be adjusted within the same range by applying external voltage at the trim pin via a buffer. In this case, Vo_d can be approximately determined by the following formula:

 $Vo_d \cong Trim Terminal Voltage \times Vo,nom$

Contact TDK - Lambda Americas for more details on the voltage trim using an external source.

The maximum power available from the power module is fixed. As the output voltage is trimmed up, the maximum output current must be decreased to maintain the maximum rated power of the module.

As the output voltage is trimmed, the output over-voltage set point is not adjusted. Trimming the output voltage too high may cause the output over voltage protection circuit to be triggered.

Remote Sense: The power modules feature remote sense to compensate for the effect of output distribution drops. If the remote sense feature is not being used, the sense(+) pin should be connected to the Vo(+) pin and

the sense(-) pin should be connected to the Vo(-) pin.

The output voltage at the Vo(+) and Vo(-) pins can be increased by either the remote sense or the output voltage adjustment feature. The maximum voltage increase allowed is the larger of the remote sense range or the output voltage adjustment range; it is not the sum of both.

As the output voltage increases due to the use of the remote sense, the maximum output current must be decreased for the power module to remain below the maximum rated power of the module.

Power Good: Not Applicable.

Parallel Operation: Optional droop load share is available when requested.

Auxiliary Bias Power: Not Applicable.

External Synchronization: *Not Applicable.*

EMC Considerations: TDK - Lambda Americas' power modules are designed for use in a wide variety of systems and applications. For assistance with designing for EMI/EMC compliance, please contact TDK - Lambda Americas' technical support.

Input Impedance: The source impedance of the input power feeding the DC/DC converter module will interact with the DC/DC converter, which may cause system instability. To minimize the interaction, two or more $470\mu F$ / 100V input electrolytic capacitor(s) should be present if the source inductance is greater than $4\mu H$.

Reliability:

The power modules are designed using stringent design guidelines for component derating, product qualification, and design reviews. Early failures are screened out by both burn-in and an automated final test. The MTBF is calculated to be about 2.5M hours at nominal input, 100% output power, 0.5" heatsink, 200LFM airflow, and Ta = 40°C using the Telcordia TR-332 issue 6 calculation method.

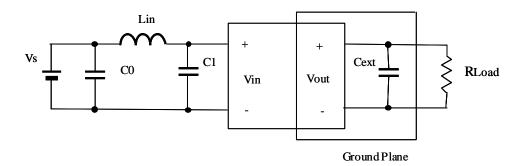
Improper handling or cleaning processes can adversely affect the appearance, testability, and reliability of the power modules. Contact TDK – Lambda Americas' technical support for guidance regarding proper handling, cleaning, and soldering of power modules.

Quality:

TDK - Lambda Americas' product development process incorporates advanced quality planning tools such as FMEA and Cpk analysis to ensure designs are robust and reliable. All products are assembled at ISO certified assembly plants.



Input/Output Ripple and Noise Measurements:



The input reflected ripple is measured with a current probe and oscilloscope. The ripple current is the current through the 15uH inductor, Lin, with esr \leq 10 m Ω , feeding a capacitor, C1, esr \leq 700 m Ω @ 100kHz, across the module input voltage pins. The capacitor C1 across the input shall be at least two (2) 470 μ F/100V capacitors in parallel. A 470 μ F/100V capacitor for C0 is also recommended.

The output voltage ripple measurement is made approximately 5 cm (2 in.) from the power module using an oscilloscope and BNC socket. The capacitor Cext consisting of a $0.1\mu F$ and a $10\mu F$ ceramic capacitors and at least two (2) $470\mu F$ or larger aluminum electrolytic or tantalum capacitor (esr $\leq 300~\text{m}\Omega$) located about 5 cm (2 in.) from the power module. At lo < lo,min, the module output is not required to be within the output voltage ripple and noise specification.

Safety Considerations:

All TDK - Lambda Americas' products are certified to regulatory standards by an independent, Certified Administrative Agency laboratory.

The iFB products have the following certifications:

UL 60950 (US & Canada) VDE 0805 CB Scheme (IEC 950) CE Mark (EN60950)

For safety agency approval of the system in which the DC-DC power module is installed, the power module must be installed in compliance with the creepage and clearance requirements of the safety agency. The isolation is basic insulation. For applications requiring basic insulation, care must be taken to maintain minimum creepage and clearance distances when routing traces near the power module.

As part of the production process, the power modules are 100% hi-pot tested from primary and secondary at a test voltage of 1500Vdc.

When the supply to the DC-DC converter is less than 60Vdc, the power module meets all of the requirements for SELV. If the input voltage is a hazardous voltage that exceeds 60Vdc, the output can be considered SELV only if the following conditions are met:

- 1) The input source is isolated from the ac mains by reinforced insulation.
- 2) The input terminal pins are not accessible.
- 3) One pole of the input and one pole of the output are grounded or both are kept floating.
- 4) Single fault testing is performed on the end system to ensure that under a single fault, hazardous voltages do not appear at the module output.



To preserve maximum flexibility, the power modules are not internally fused. An external input line normal blow fuse with a maximum value of 20A is required by safety agencies.

A lower value fuse can be selected based upon the maximum dc input current and maximum inrush energy of the power module.

Warranty:

TDK - Lambda Americas' comprehensive line of power solutions includes efficient, high-density DC-DC converters. TDK - Lambda Americas offers a three-year limited warranty. Complete warranty information is listed on our web site or is available upon request from TDK - Lambda Americas.

TDK - Lambda Americas Inc.

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