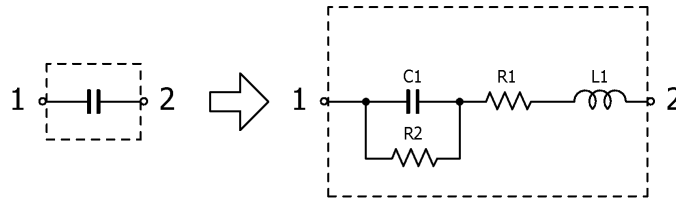


Multilayer Ceramic Chip Capacitors

May 23, 2024
Simple Model

Commercial Grade, Ultra Low Inductance / CLLC1A series

Circuit Diagram



Circuit Parameters

Part No.	C1[pF]	L1[nH]	R1[ohm]	R2[Gohm]
CLLC1AX7R0G104M050AC	100,000	0.065	0.0371	1.00
CLLC1AX7S0G334M050AC	330,000	0.049	0.0153	0.303
CLLC1AX7S0G474M050AC	470,000	0.050	0.0152	0.212
CLLC1AX7S0G684M050AC	680,000	0.043	0.0141	0.147
CLLC1AX7S0G105M050AC	1,000,000	0.046	0.0121	0.100
CLLC1AX7S0G225M050AC	2,200,000	0.046	0.0108	0.0450
CLLC1AX6S0G475M050AC	4,700,000	0.043	0.0078	0.0210